SPECIFICATION
FOR
APPROVAL

( ) Preliminary Specification
(◆) Final Specification

Title	15.0” SXGA+ TFT LCD

BUYER	
MODEL	

SUPPLIER	LG.Philips LCD CO., Ltd.
*MODEL	LP150E05
SUFFIX	A2K1

*When you obtain standard approval,
please use the above model name without suffix

SIGNATURE	DATE
/	
/	
/	

Please return 1 copy for your confirmation with your signature and comments.

APPROVED BY	DATE
J. H. Park / S.Manager	
REVIEWED BY	
K. Y. Chong / Enigneer	
PREPARED BY	
S. Y. Kim / Engineer	

Product Engineering Dept.
LG. Philips LCD Co., Ltd

Contents

Records of revision

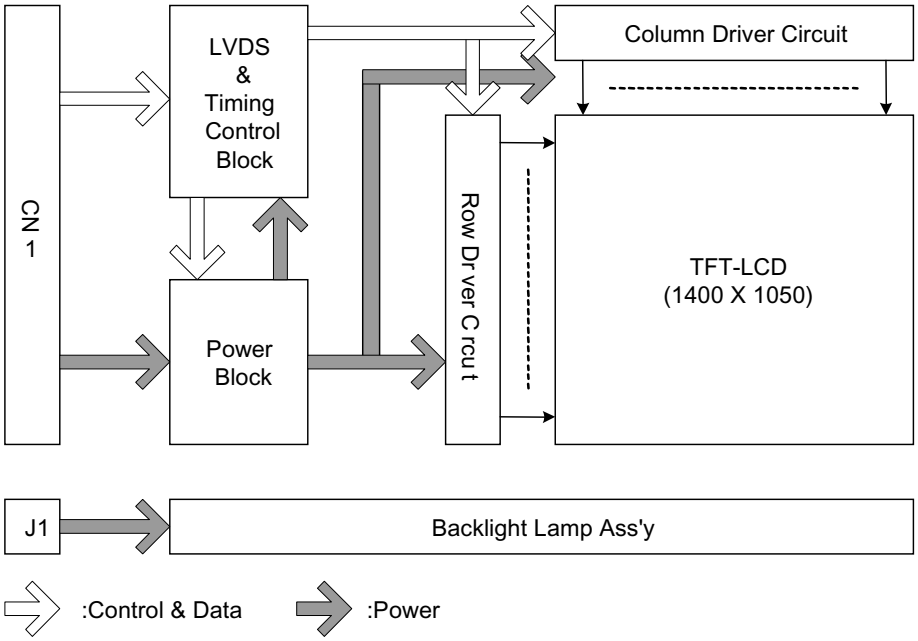
Revision No	Revision Date	Page	DESCRIPTION
0.0	JUN. 10, 2005	-	First Draft.Preliminary Specifications
0.1	OCT. 10, 2005	-	Rev 0.1 Preliminary Specifications
0.2	JAN. 20, 2006	-	Rev 0.2 Final Specifications

1. General Description

The LP150E05 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp(CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. This TFT-LCD has a 15.0 inch diagonally measured active display area with SXGA+ resolution(1050 vertical by 1400 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP150E05 has been designed to apply the interface method that enables low power, high speed, low EMI. Flat Link must be used as a LVDS(Low Voltage Differential Signaling) chip.

The LP150E05 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP150E05 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active screen size	15.0 inch(38.1cm) diagonal
Outline Dimension	317.3(H) x 241.5(V) x 5.7(D) mm(Typ)
Pixel Pitch	0.2175(H) x 0.2175(V) mm
Pixel format	1400 horiz. By 1050 vert. Pixels RGB stripes arrangement
Color depth	6-bit, 262,144 colors
Luminance, white	200 cd/m^2(Typ.) 1Point
Power Consumption	6.71 W (Windows xp Bliss typ, @ 6.5mA)
Weight	520g(Typ.)
Display operating mode	Transmissive mode, normally black
Surface treatments	Anti-glare & hard coating 3H [Nitto : Normal / LNC-SEGCAG160-F034TR]

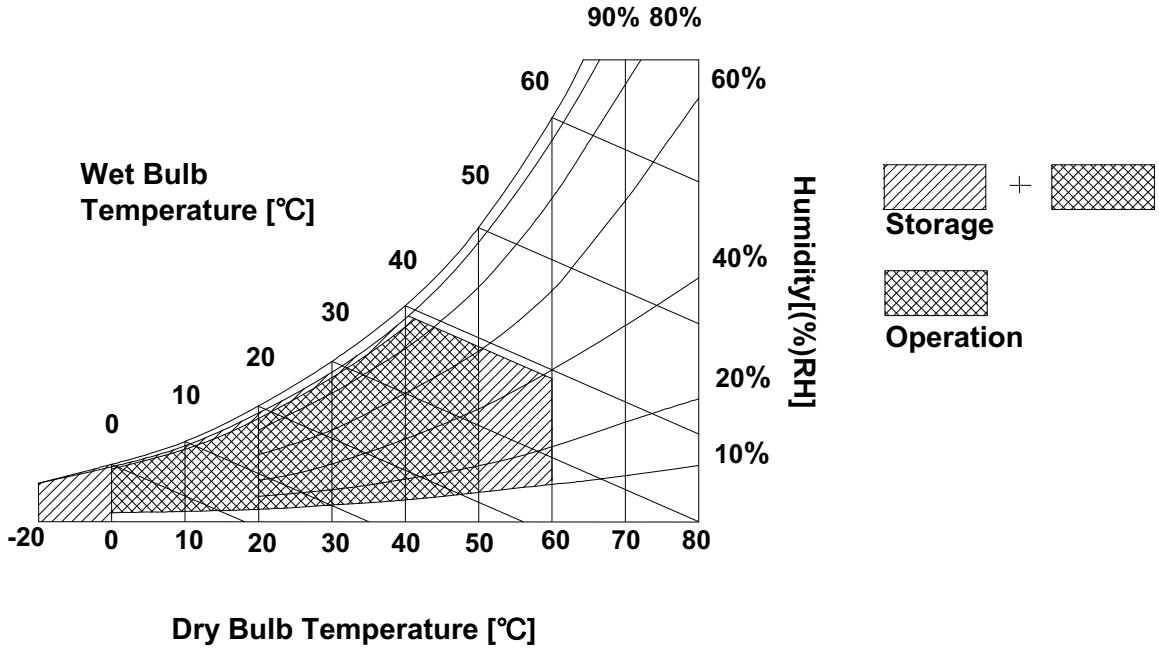
2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	symbol	Values		Units	Notes
		Min.	Max.		
Power Input Voltage	V_{CC}	-0.3	4.0	Vdc	At 25 ± 5°C
Operating Temperature	T_{OP}	0	50	°C	1
Storage Temperature	T_{ST}	-20	60	°C	1
Operating Ambient Humidity	H_{OP}	10	90	%RH	1
Storage Humidity	H_{ST}	10	90	%RH	1

Note : 1. Temperature and relative humidity range are shown in the figure below.
Wet bulb temperature should be 39 °C Max, and no condensation of water.



3. Electrical Specifications

3-1. Electrical Characteristics

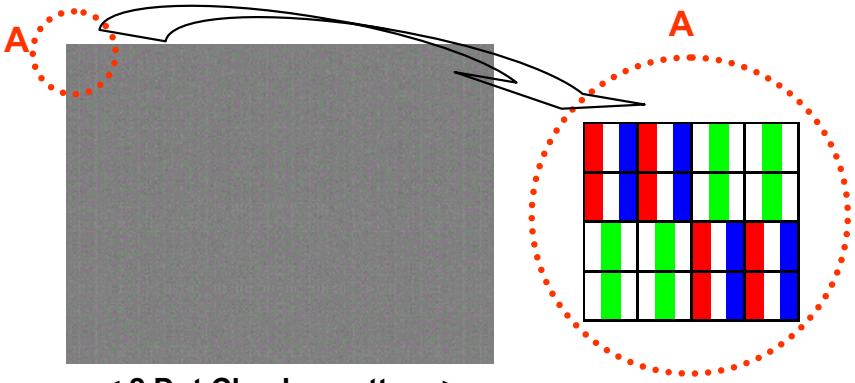
The LP150E05 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Parameter	Symbol	Values			Unit	Notes
		Min	Typ	Max		
MODULE :						
Power Supply Input Voltage	VCC	3.0	3.3	3.6	Vdc	
Power Supply Input Current Windows XP Bliss	I_{CC}	-	730	840	mA	1
Full White		-	700	805	mA	
2 dot check		-	670	770	mA	1
Power Consumption Window XP Bliss	Pc	-	2.41	2.77	Watt	1
Differential Impedance	Zm	90	100	110	ohm	2
LAMP :						
Operating Voltage	V_{BL}	640(6.8mA)	650(6.5mA)	835(2.5mA)	V_{RMS}	3
Operating Current	I_{BL}	2.5	6.5	6.8	mA_{RMS}	
Established Starting Voltage	Vs					4
at 25 °C		-	-	1165	V_{RMS}	
at 0 °C		-	-	1400	V_{RMS}	
Operating Frequency	f_{BL}	50	65	80	kHz	5
Discharge Stabilization Time	T_S	-	-	3	Min	7
Power Consumption	P_{BL}	-	4.3	4.7	Watt	8
Life Time		12,000	-	-	Hrs	9

Notes : **The design of the inverter must have specification for the lamp in LCD Assembly.**
The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter.
When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter(no lighting, flicker, etc) never occurs. When you confirm it, the LCD Assembly should be operated in the same condition as installed in your instrument.

1. The specified current and power consumption are under the VCC=3.3V, 25°C, f_V=60Hz condition whereas Windows xp Bliss pattern is displayed and f_V is the frame frequency.

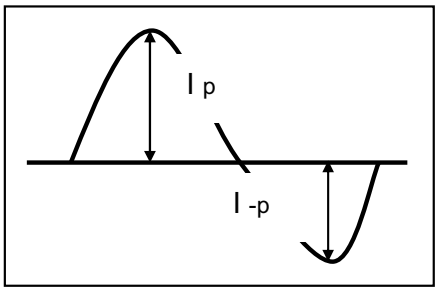
< Windows xp Bliss pattern >



< 2 Dot Checker pattern >

2. This impedance value is needed to proper display and measured from LVDS T_X to the mating connector.
3. The variance of the voltage is ± 10%.
4. The voltage above V_S should be applied to the lamps for more than 1 second for start-up. Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.
5. The voltage above V_S should be applied to the lamps for more than 1 second for start-up. Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.
6. The output of the inverter must have symmetrical(negative and positive) voltage waveform and symmetrical current waveform.(Unsymmetrical ratio is less than 10%) Please do not use the inverter which has unsymmetrical voltage and unsymmetrical current and spike wave.
   Lamp frequency may produce interference with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
7. Let's define the brightness of the lamp after being lighted for 5 minutes as 100%.
   T_S is the time required for the brightness of the center of the lamp to be not less than 95%.
8. The lamp power consumption shown above does not include loss of external inverter.
   The used lamp current is the typical lamp current ($6.5mA_{RMS}$).
9. The life time is determined as the time at which brightness of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at 25 ± 2°C.

* Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following. It shall help increase the lamp lifetime and reduce leakage current.
  a. The asymmetry rate of the inverter waveform should be less than 10%.
  b. The distortion rate of the waveform should be within √2 ± 10%.
  * Inverter output waveform had better be more similar to ideal sine wave.



* Asymmetry rate:

$| I_p - I_{-p} | / I_{rms} * 100\%$

* Distortion rate

I_p (or I_{-p}) / I_{rms}

* Do not attach a conducting tape to lamp connecting wire.
  If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.

3-2. Interface Connections

Interface chip must be used FlatLink, part No. SN75LVDS84(Transmitter made by Texas Instrument Inc or equivalence).
This LCD employs two interface connections, a 30 pin connector is used for the module electronics interface and the other connector is used for the integral backlight system.
The electronics interface connector is a model FI-XB30SR-HF11 manufactured by JAE
The pin configuration for the connector is shown in the table 3.

Table 3. MODULE CONNECTOR PIN CONFIGURATION(LVDS)

Pin	Symbol	Description	Notes
1	GND	Ground	
2	Vcc	Power(3.3V)	
3	Vcc	Power(3.3V)	
4	V_EDID	DDC 3.3V Power	1. LVDS Interfacing
5	NC	No Connection	1.1 System : SN75LVDS84(TI) or equivalent
6	CLK_EDID	DDC Clock	*Pin to Pin compatible with Thine LVDS
7	DATA_EDID	DDC Data	
8	RA1-	Odd Channel Differential Signal	1.2 LCD : THC63LVD64A(THINE)
9	RA1+	Odd Channel Differential Signal	
10	VSS	Ground	
11	RB1-	Odd Channel Differential Signal	
12	RB1+	Odd Channel Differential Signal	2. Connector
13	VSS	Ground	2.1 LCD : JAE FI-XB30SR-HF11
14	RC1-	Odd Channel Differential Signal	2.2 Mating : JAE FI-X30M or equivalent
15	RC1+	Odd Channel Differential Signal	2.3 Connector pin arrangement
16	VSS	Ground	
17	RCLK1-	Odd Channel Differential Signal	
18	RCLK1+	Odd Channel Differential Signal	
19	VSS	Ground	
20	RA2-	Even Channel Differential Signal	
21	RA2+	Even Channel Differential Signal	
22	VSS	Ground	
23	RB2-	Even Channel Differential Signal	
24	RB2+	Even Channel Differential Signal	
25	VSS	Ground	
26	RC2-	Even Channel Differential Signal	
27	RC2+	Even Channel Differential Signal	
28	VSS	Ground	
29	RCLK2-	Even Channel Differential Signal	
30	RCKL2+	Even Channel Differential Signal	

[LCD Module Rear View]

The backlight interface connector is a model BHSR-02VS-1, manufactured by JST. The mating connector part number is SM02B-BHS-1 or equivalent.
The pin configuration for the connector is shown in the table below.

Table 4. BACKLIGHT CONNECTOR PIN CONFIGURATION

Pin	Symbol	Description	Notes
1	HV	Power supply for lamp (High voltage side)	1
2	LV	Power supply for lamp (Low voltage side)	1

Notes : 1. The high voltage side terminal is colored pink. The low voltage side terminal is blue.

3-3. Signal Timing Specifications

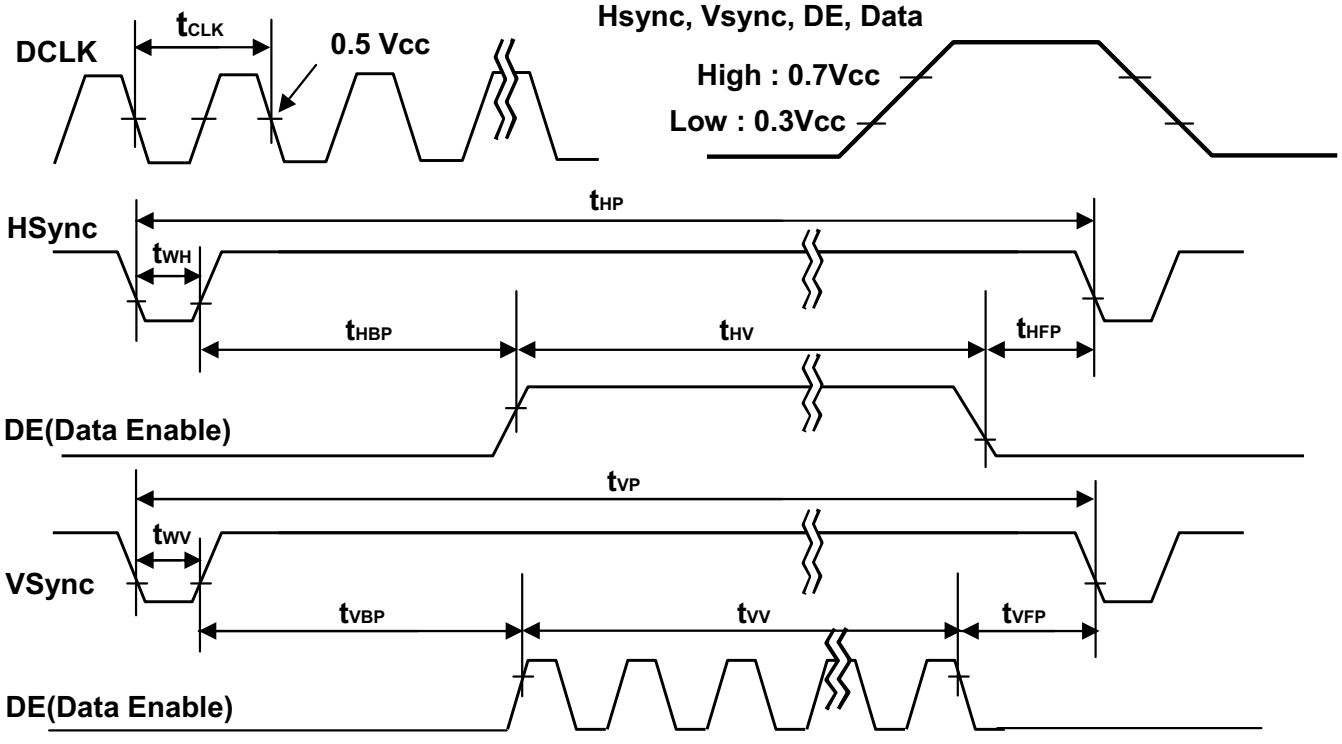
This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for it's proper operation.

Table 5. Timing Table

	ITEM	SYMBOL	MIN	TYP	MAX	UNIT	NOTES
Dclk	Frequency	-	36	54	54.5	MHz	
Hsync	Period	t_{HP}	762	844	848	t_{CLK}	
	Width	t_{WH}	8	-	-		
Vsync	Period	t_{VP}	1060	1066	1150	t_{HP}	
	Frequency	f_V	40	60		Hz	
	Width	t_{WV}	2	-	-	t_{HP}	
DE (Data Enable)	Horizontal Valid	t_{HV}	700	700	700	t_{CLK}	
	Horizontal Back Porch	t_{HBP}	8	-	-		
	Horizontal Front Porch	t_{HFP}	8	-	-		
	Vertical Valid	t_{VV}	1050	1050	1050		
	Vertical Back Porch	t_{VBP}	3	-	-	t_{HP}	
	Vertical Front Porch	t_{VFP}	1	-	-		

1) 60 at Normal mode, 50,40 at Power save mode. Don't care Flicker level.

3-4. Signal Timing Waveforms



3-5. Color Input Data Reference

The brightness of each primary color(red,green and blue) is based on the 6-bit gray scale data input for the color ; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 6. COLOR DATA REFERENCE

Color		Input Color Data																	
		Red						Green						Blue					
		MSB					LSB	MSB					LSB	MSB					LSB
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Red(00) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(02)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63) Bright	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green	Green(00)Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(02)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)Bright	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Blue(00) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(02)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63) Bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

3-6. Power Sequence

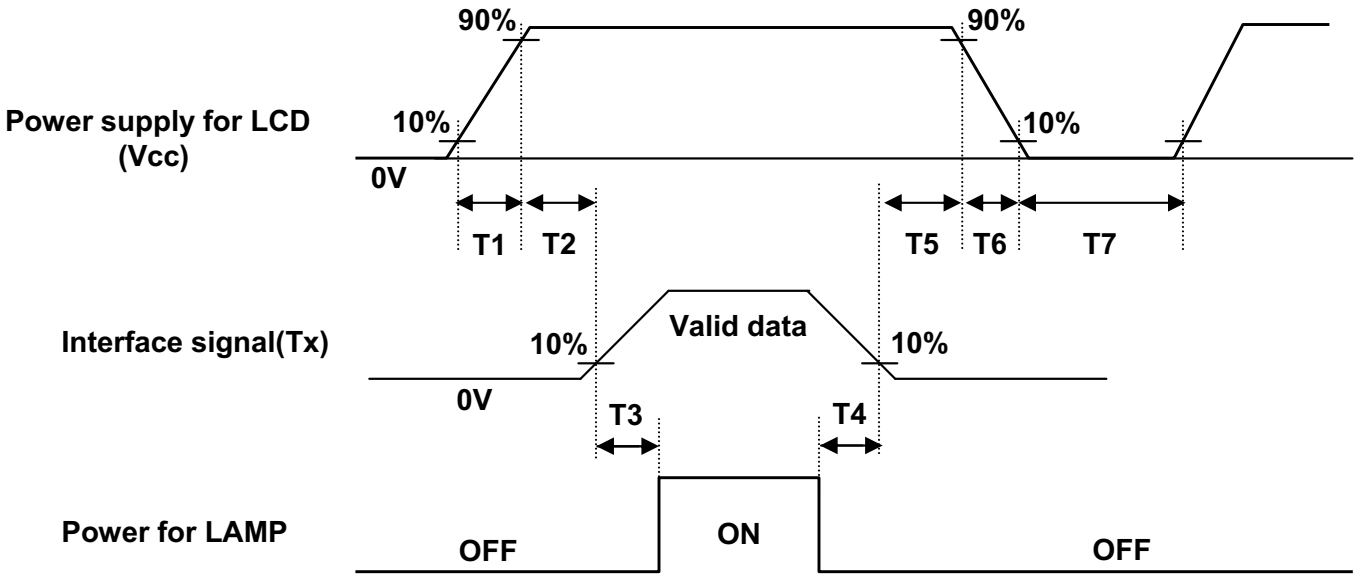


Table 7. POWER SEQUENCE TABLE

Parameter	Values			Units
	Min.	Typ.	Max.	
T 1	0	-	10	(ms)
T 2	0	-	50	(ms)
T 3	200	-	-	(ms)
T 4	0	-	-	(ms)
T 5	0	-	50	(ms)
T 6	0	-	10	(ms)
T 7	150	-	-	(ms)

Notes : 1. Please avoid floating state of interface signal at invalid period.
2. When the interface signal is invalid, be sure to pull down the power supply for LCD V_{CC} to 0V.
3. Lamp power must be turn on after power supply for LCD and interface signals are valid.

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0°.
FIG. 1 presents additional information concerning the measurement equipment and method.

FIG. 1 Optical Characteristic Measurement Equipment and Method

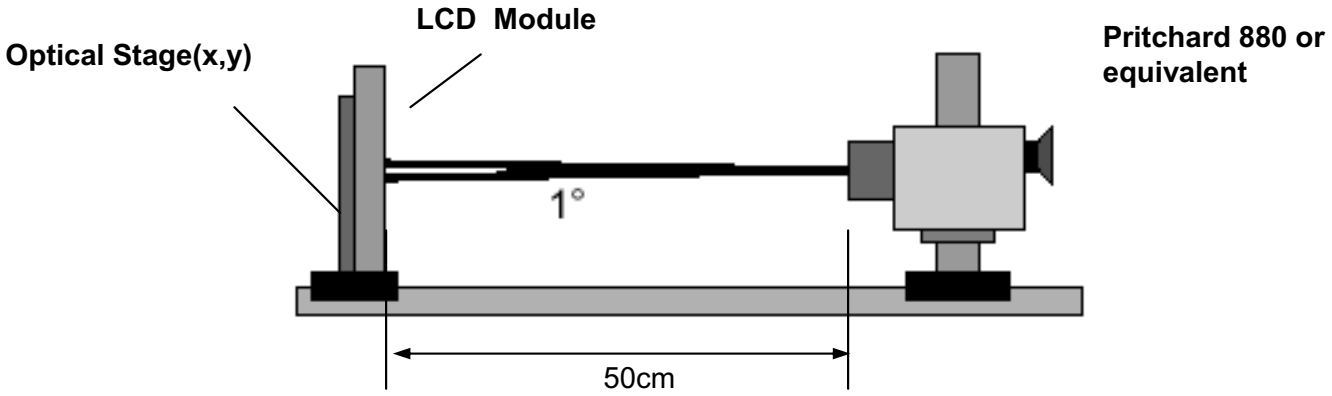


Table 8. OPTICAL CHARACTERISTICS (Ta=25 °C, V_{CC}=3.3V, f_V=60Hz Dclk=54MHz, I_{BL}=6.5mArms)

Parameter	Symbol	Values			Units	Notes
		Min.	Typ.	Max.		
Contrast Ratio	CR	300	550	-		1
Surface Luminance, white (1p) (5p)	L_{WH}	160 150	200 185	-	cd/m²	2
Luminance Variation	δ_{WHITE}	80 60	-	-	5points 13points	3
Response Time						
Rise Time	Tr_R	-	25	-	ms	4
Decay Time	Tr_D	-	25	-	ms	
CIE Color Coordinates						
Red	XR	0.570	0.600	0.630		
	YR	0.316	0.346	0.376		
Green	XG	0.298	0.328	0.358		
	YG	0.520	0.550	0.580		
Blue	XB	0.135	0.165	0.195		
	YB	0.115	0.145	0.175		
White	XW	0.285	0.313	0.341		
	YW	0.309	0.329	0.349		
Viewing Angle				-		
x axis, right(ϕ=0°)	θr	85	88	-	degree	5
x axis, left (ϕ=180°)	θl	85	88	-	degree	
y axis, up (ϕ=90°)	θu	85	88	-	degree	
y axis, down (ϕ=270°)	θd	85	88		degree	
Gray Scale	-	-		-		6

* Measured Inverter : 6632Z-1301A(LG Electronics)

Notes : 1. Contrast Ratio(CR) is defined mathematically as :

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

2. Surface luminance is the center point across the LCD surface 50cm from the surface with all pixels displaying white under the condition of I_{BL}=6.5mA. For more information see FIG 1.

3. The variation in surface luminance , **The Panel total variation (δ_{WHITE})** is determined by measuring L_N at each test position 1 through 13, and then dividing the minimum L_N of 13 points luminance by maximum L_N of 13 points luminance. For more information see FIG 2.

$$\delta_{WHITE} = \text{Minimum}(L_{N1}, L_{N2}, \ldots L_{N13}) \div \text{Maximum}(L_{N1}, L_{N2}, \ldots L_{N13}) * 100$$

4. Response time is the time required for the display to transition from white to black(Rise Time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.

5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.

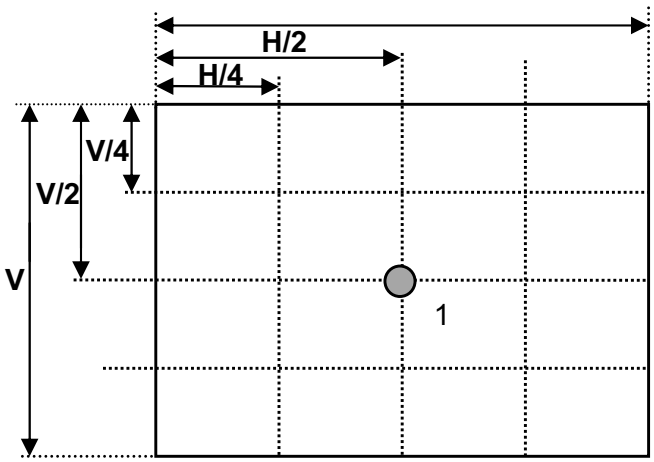
6. Gray scale specification

* f_V=60Hz

Gray Level	Luminance(%) (Typ.)
L0	0.2
L7	0.8
L15	4.25
L23	10.9
L31	21.0
L39	34.8
L47	52.5
L55	74.2
L63	100

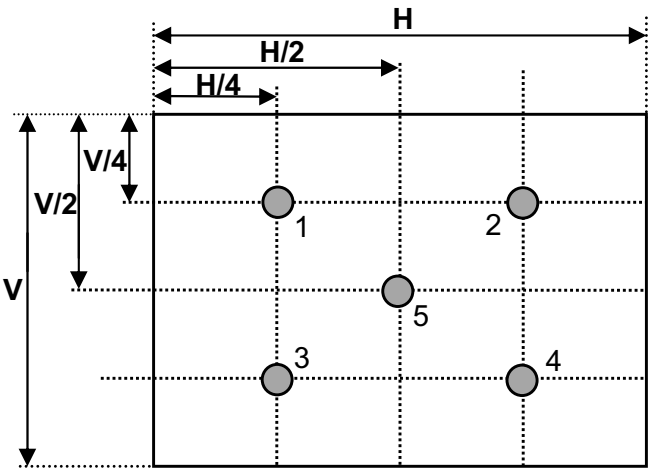
FIG. 2 Luminance & Luminance variation

Luminance measurement 1 point



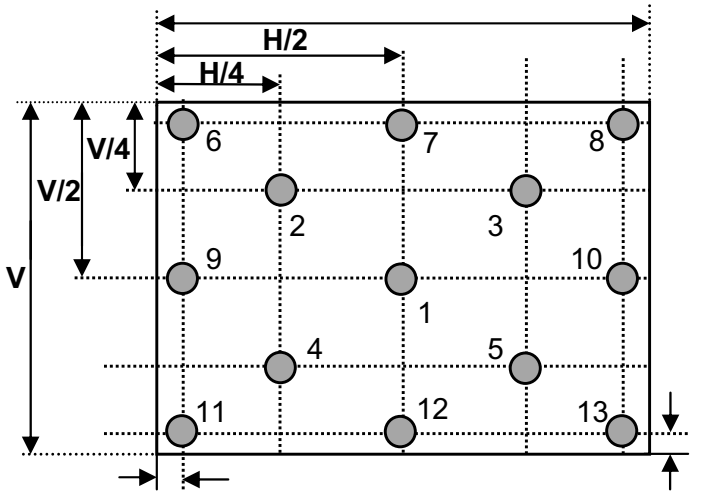
H : 304.5 mm
V : 228.375 mm
@ H,V : Active Area

Luminance & Luminance variation measurement 5 points



H : 304.5 mm
V : 228.375 mm
@ H,V : Active Area

Luminance variation measurement 13 points



H : 304.5 mm
V : 228.375 mm
@ H,V : Active Area

Notes) The Adjacent point must be opposite horizontally or vertically.

FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for “black” and “white”.

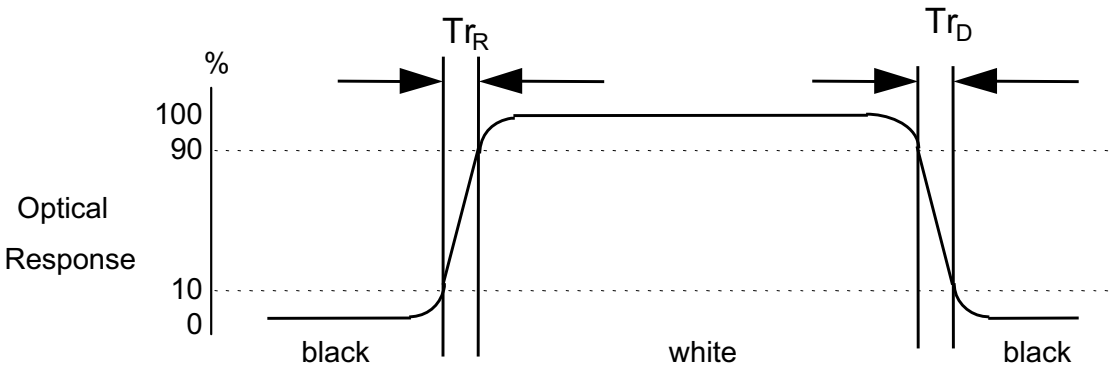
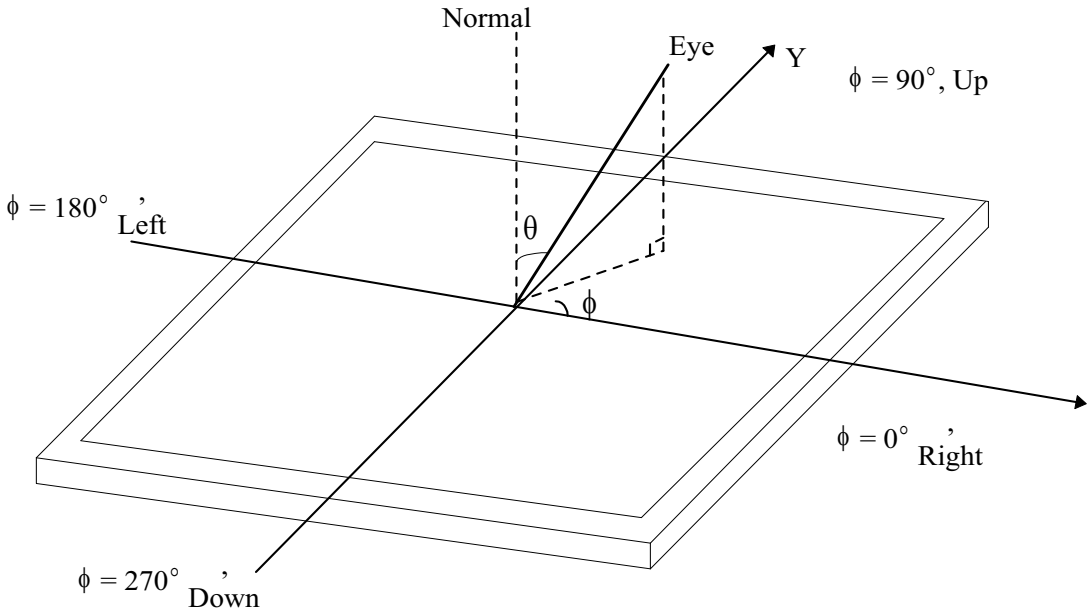


FIG. 4 Viewing angle

<dimension of viewing angle range>

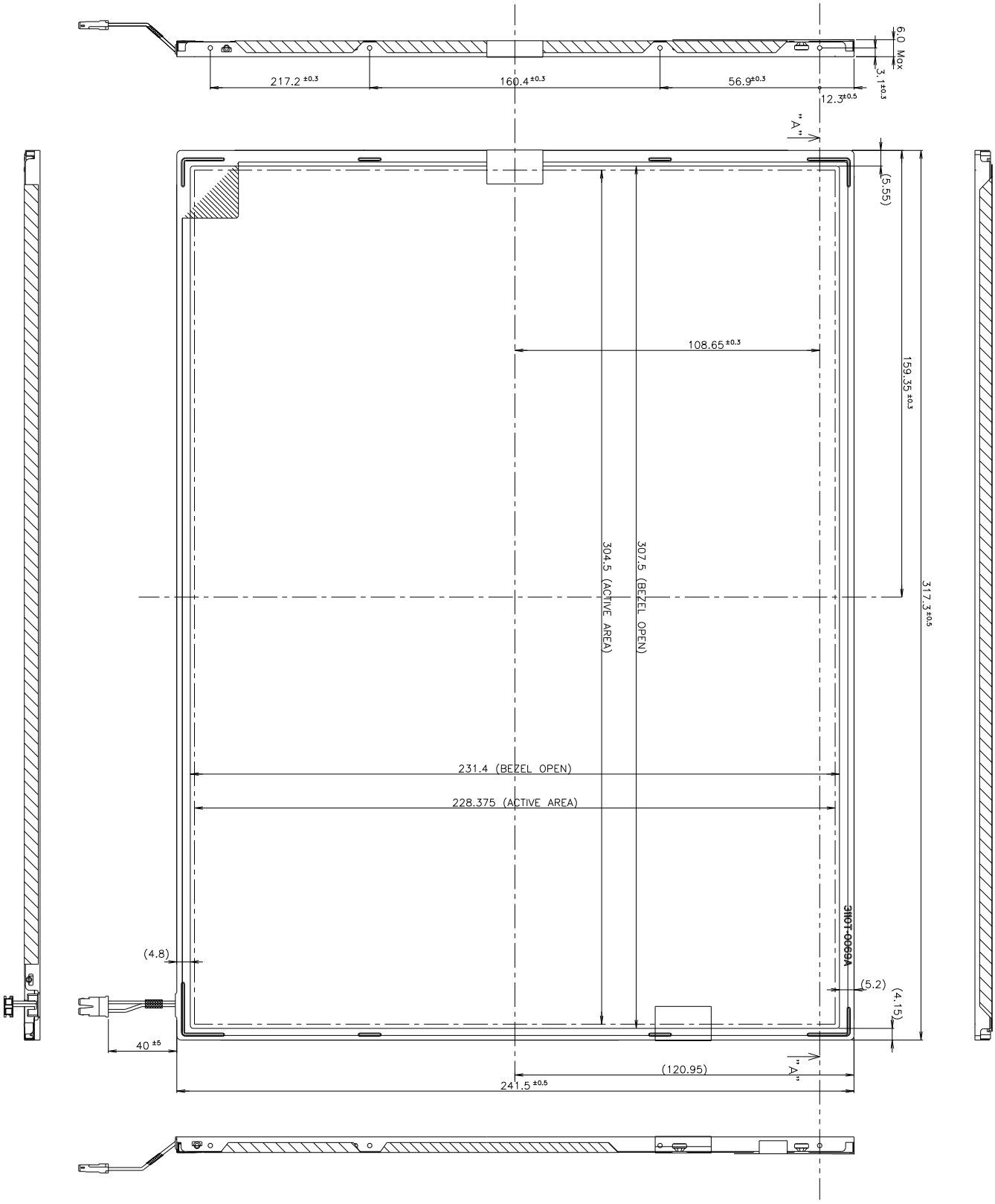


5. Mechanical Characteristics

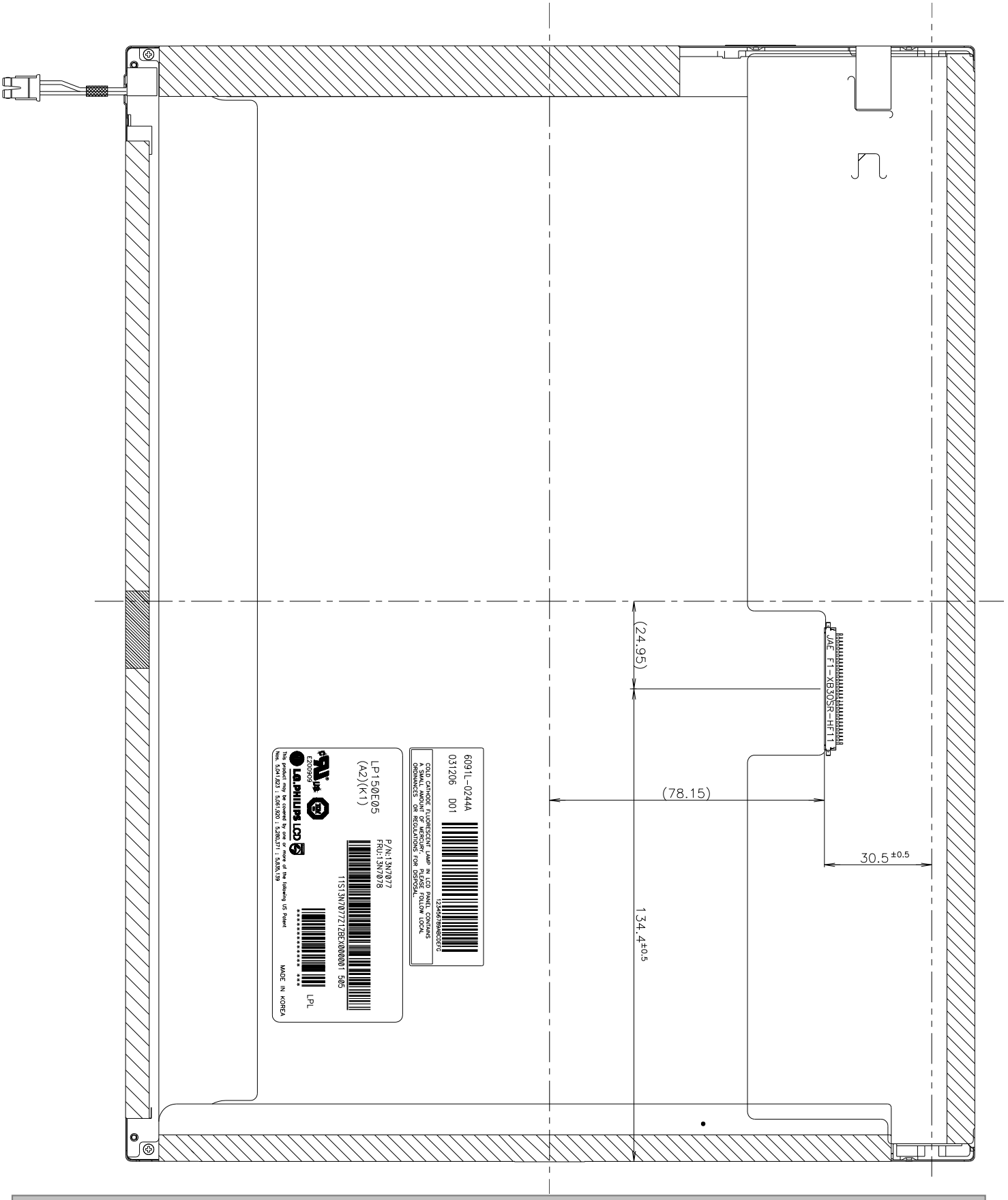
The contents provide general mechanical characteristics for the model LP150E05. In addition the figures in the next page are detailed mechanical drawing of the LCD.

Outside dimensions	Horizontal	317.3 ± 0.5mm
	Vertical	241.5 ± 0.5mm
	Depth	5.7 ± 0.3mm
Bezel area	Horizontal	307.5 ± 0.5mm
	Vertical	231.4 ± 0.5mm
Active display area	Horizontal	304.5mm
	Vertical	228.375mm
Weight	520g(Typ.)	
Surface Treatment	Anti-glare & hard coating 3H [Nitto : Normal / LNC-SEGCAG160-F034TR]	

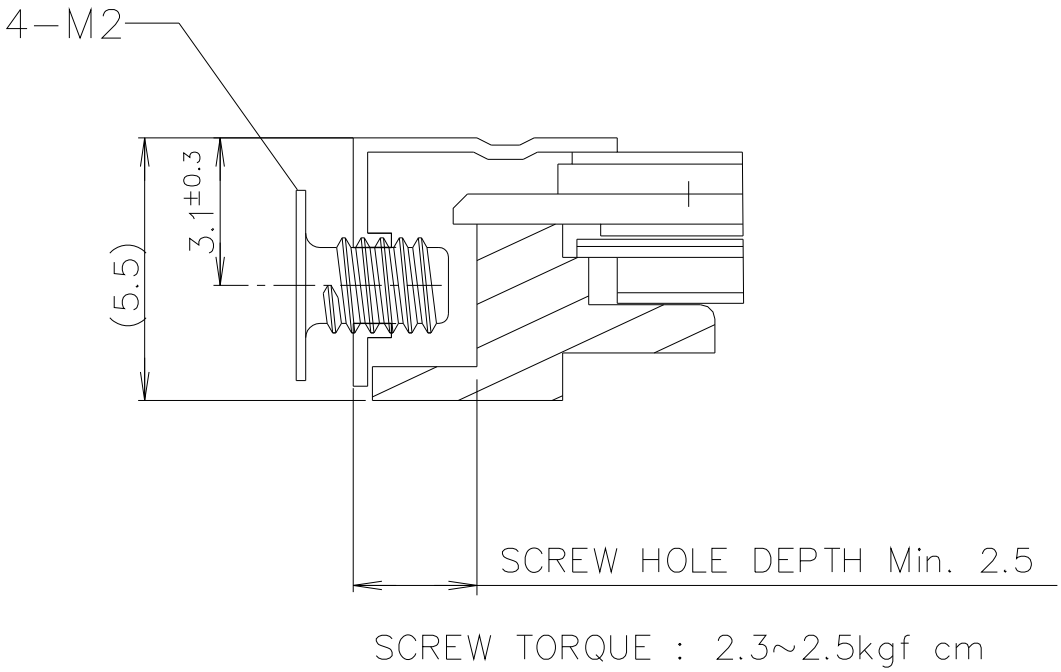
<FRONT VIEW>


6.0 Max
3.1±0.3
217.2 ±0.3
160.4 ±0.3
56.9 ±0.3
12.3 ±0.5
" A "
(5.55)
108.65 ±0.3
159.35 ±0.3
304.5 (ACTIVE AREA)
307.5 (BEZEL OPEN)
317.3 ±0.5
231.4 (BEZEL OPEN)
228.375 (ACTIVE AREA)
3NOT-0069A
(4.8)
(5.2)
(4.15)
40 ±5
(120.95)
241.5 ±0.5

<REAR VIEW>


(24.95)
JAE FI-XB30SR-HF11
(78.15)
30.5 ±0.5
134.4±0.5
6091L-0244A
031206 D01
COLD CATHODE FLUORESCENT LAMP IN LCD PANEL CONTAINS MERCURY. PLEASE FOLLOW LOCAL ORDINANCES OR REGULATIONS FOR DISPOSAL
LP150E05
(A2)(K1)
P/N:13N7077
FRU:13N7078
11S13N7772ZBEX000001 505
MADE IN KOREA

<DETAIL DESCRIPTION OF SIDE MOUNTING SCREW>



SECTION "A"-"A"
SCALE 5/1

Notes : 1. Screw plated through the method of non-electrolytic nickel plating is preferred to reduce possibility that results in vertical and/or horizontal line defect due to the conductive particles from screw surface.

6. Reliability

Environment test condition

No.	Test Item	Conditions
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis
6	Shock test (non-operating)	Half sine wave, 100G, 6ms one shock of each six faces(I.e. run 100G 6ms for all six faces)
7	Altitude operating storage / shipment	 0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr

{ Result Evaluation Criteria }
There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

- ON/OFF Cycle
: The display module will be capable of being operated over 24,000 ON/OFF cycles (Lamp power & Vcc ON/OFF)

- Mean time Between Failure
: The LCD Panel and interface board assembly (excluding the CCFL) have a mean time between failures of 30,000 hours with a confidence level 90%.

7. International Standards

7-1. Safety

a) UL 60950, Third Edition, Underwriters Laboratories, Inc., Dated Dec. 11, 2000.
Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.
b) CAN/CSA C22.2, No. 60950, Third Edition, Canadian Standards Association, Dec. 1, 2000.
Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.
c) EN 60950 : 2000, Third Edition
IEC 60950 : 1999, Third Edition
European Committee for Electrotechnical Standardization(CENELEC)
EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

a) ANSI C63.4 “Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. “American National Standards Institute(ANSI), 1992
b) C.I.S.P.R “Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment.“ International Special Committee on Radio Interference.
c) EN 55022 “Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment.“ European Committee for Electrotechnical Standardization.(CENELEC), 1998
(Including A1: 2000)

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A	B	C	D	E	F	G	H	I	J	K	L	M

A,B,C : SIZE(INCH)
D : YEAR
E : MONTH
F : FACTORY CODE
G : ASSEMBLY CODE
H ~ M : SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	A	B	C

3. FACTORY CODE

Factory Code	LPL Gumi	LPL Nanjing	HEESUNG
Mark	K	C	D

4. SERIAL NO.

Mark	100001~199999, 200001~299999, 300001~399999, …., A00001~A99999, ….., Z00001~Z99999

b) Location of Lot Mark

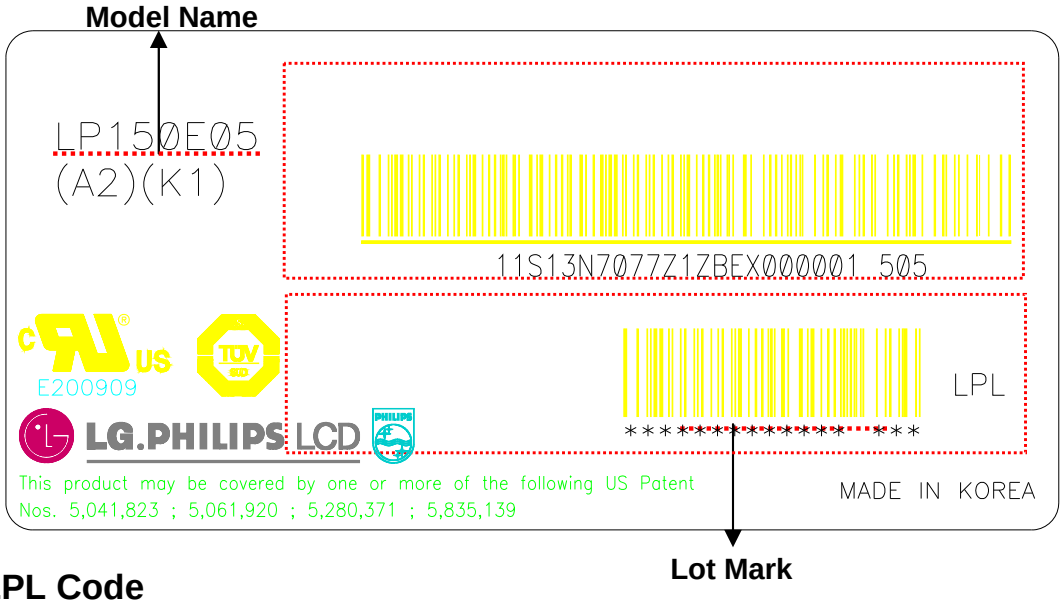
Serial No. is printed on the label. The label is attached to the backside of the LCD module.
This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box : 12 pcs

b) Box size : 376mm X 321mm X 317mm.

8-3. Label Description



LPL Code

(1) Model Name : **L P 1 5 0 E 0 5 - A 2 K 1**

- Revision Code
- Panel FAB : P2
- Basic Model Modify History (A~Z)
- Basic Model Serial No.
- Resolution : SXGA+
- Size (inch) : 15.0”
- Application : NBPC
- LGPL Brand Initial: L

(2) Lot Mark : **1 5 0 4 8 K Z 0 0 0 0 0 1**

- SERIAL NO.
- ASSEMBLY CODE
- FACTORY CODE
- MONTH
- YEAR
- SIZE

9. PRECAUTIONS

Please pay attention to the following when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

(1) You must mount a module using holes arranged in four corners.
(2) You should consider the mounting structure so that uneven force(ex. Twisted stress) is not applied to the module.
And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
(3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to resist external force.
(4) You should adopt radiation structure to satisfy the temperature specification.
(5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
(6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
(8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
(9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

(1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : V=±200mV(Over and under shoot voltage)
(2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
(3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
(4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
(5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
(6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.

(2) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

(1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.

(2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
Please carefully peel off the protection film without rubbing it against the polarizer.

(3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.

(4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.