

SPECIFICATION FOR APPROVAL

- (◆) Preliminary Specification
- () Final Specification

Title	15.0" SXGA+ TFT LCD
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BUYER	Lenovo
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LP150E07
Suffix	TL03

*When you obtain standard approval,
please use the above model name without suffix

SIGNATURE	DATE
/	_____
/	_____
/	_____

Please return 1 copy for your confirmation with your signature and comments.

APPROVED BY	Signaure
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PREPARED BY	
N.J. Seong / Engineer	_____

**Product Engineering Dept.
LG. Philips LCD Co., Ltd**

Product Specification

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RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	Note
0.0	Nov 01 2005	-	First Draft	
0.1	Feb 09 2006	p 12	Change of Contrast Ratio	
			- 200:1 (min) → 200:1(min), 300:1(typ)	
		p 12	Change of Viewing Angle (Up/Down)	
			- 45/45/15/35 (typ) → 45/45/20/40 (typ)	
		P16~17	Change of Outline Dimension	
			- 317.2(H) × 241.4(V) → 317.3(H) × 241.4(V)	
		p 19	Update specification of side mounting screw	
			- Screw Torque : 2.3~2.5kgf cm → 2.5kgf cm (max)	
			- Screw Length : 2.5 (max), 2.3 (min)	
0.2	Feb 24 2006	p 4	Add the typical weight	
			- 535g (max) → 520g (typ) 535 (max)	
0.3	Feb 28 2006	p 6	Change of Lamp operating voltage	
		p 17	Specify the CCFL exit position	
0.4	Jul 20 2006	p18	Adding Pet PAD near the User Connector(Rear View Schematic)	

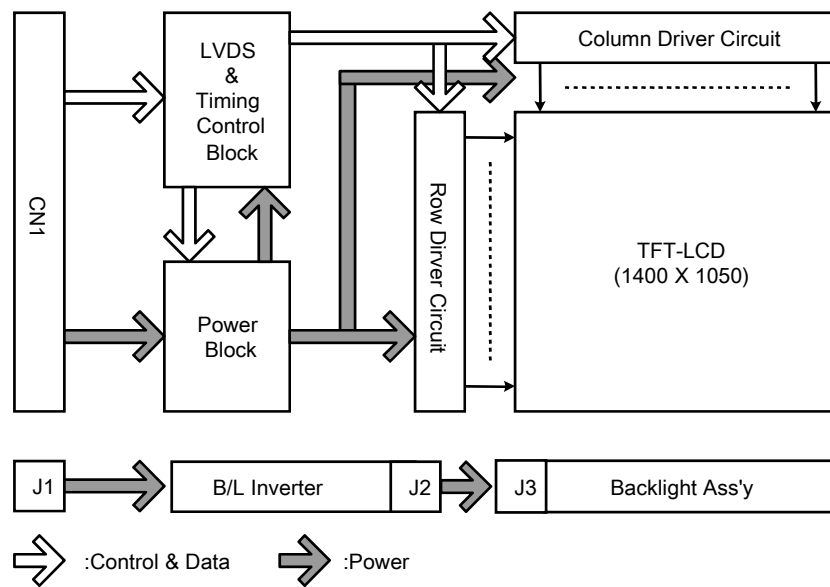
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1. General Description

The LP150E07 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp (CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 15.0 inches diagonally measured active display area with SXGA+ resolution(1050 vertical by 1400 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP150E07 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP150E07 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP150E07 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	15.0 inches(38.1cm) diagonal
Outline Dimension	317.3(H) × 241.4(V) × 6.0(D) mm (Max.)
Pixel Pitch	0.2175 mm × 0.2175 mm
Pixel Format	1400 horiz. By 1050 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	200cd/m ² (Typ.), 170 cd/m ² (Min.), 1p
Power Consumption	Total 5.6 W (Typ.) (1.6W Logic / 4.0W Backlight)
Weight	520g (Typ.) 535g (Max)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Hard coating(3H) Maker:Nitto Denco ARC150T

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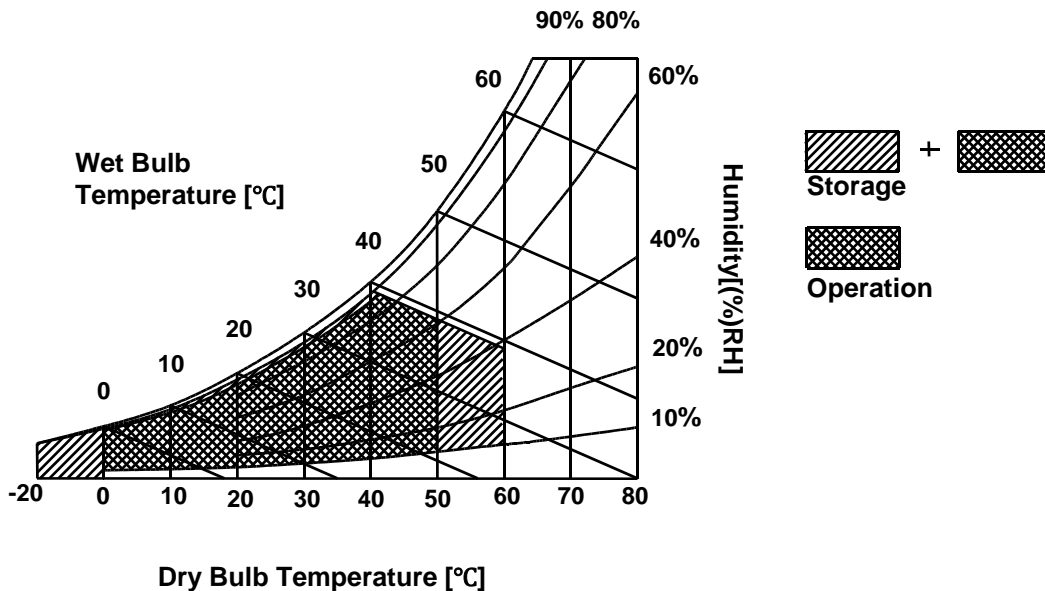
2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Values		Units	Notes
		Min	Max		
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C
Operating Temperature	TOP	0	50	°C	1
Storage Temperature	TST	-20	60	°C	1
Operating Ambient Humidity	HOP	10	90	%RH	1
Storage Humidity	HST	10	90	%RH	1

Note : 1. Temperature and relative humidity range are shown in the figure below.
 Wet bulb temperature should be 39°C Max, and no condensation of water.



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3. Electrical Specifications

3-1. Electrical Characteristics

The LP150E07 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Values			Unit	Notes	
		Min	Typ	Max			
MODULE :							
Power Supply Input Voltage	VCC	3.0	3.3	3.6	Vdc		
Power Supply Input Current	I _{CC}	-	Window XP Bliss	480	552	mA	1
			Mosaic	420	483	mA	
Power Consumption	Pc	-	1.6	2.0	Watt	1	
Differential Impedance	Zm	90	100	110	ohm	2	
LAMP :							
Operating Voltage	V _{BL}	648	665	805	V _{RMS}	3	
Operating Current	I _{BL}	3.0	6.0	6.5	mA _{RMS}		
Established Starting Voltage	Vs					4	
		at 25 °C	-	-	1165	V _{RMS}	
		at 0 °C	-	-	1400	V _{RMS}	
Operating Frequency	f _{BL}	50	65	80	kHz	5	
Discharge Stabilization Time	T _S	-	-	3	Min	7	
Power Consumption	P _{BL}	-	4.0	4.4	Watt	8	
Life Time		10,000	-	-	Hrs	9	

Note : The design of the inverter must have specifications for the lamp in LCD Assembly.

The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter(no lighting, flicker, etc) never occurs. When you confirm it, the LCD – Assembly should be operated in the same condition as installed in you instrument.

1. The specified current and power consumption are under the VCC=3.3V, 25°C, f_V=60Hz condition whereas Windows XP Bliss pattern is displayed and f_V is the frame frequency.

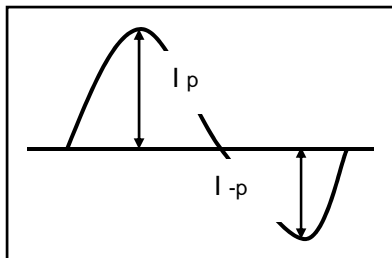


< Windows xp Bliss pattern >

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2. This impedance value is needed to proper display and measured from LVDS T_x to the mating connector.
3. The variance of the voltage is ± 10%.
4. FOS, and reliability test condition is at 6.0mA
5. The voltage above V_S should be applied to the lamps for more than 1 second for start-up. Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.
6. The output of the inverter must have symmetrical(negative and positive) voltage waveform and symmetrical current waveform.(Unsymmetrical ratio is less than 10%) Please do not use the inverter which has unsymmetrical voltage and unsymmetrical current and spike wave. Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
7. Let's define the brightness of the lamp after being lighted for 5 minutes as 100%. T_S is the time required for the brightness of the center of the lamp to be not less than 95%.
8. The lamp power consumption shown above does not include loss of external inverter. The used lamp current is the lamp typical current.
9. The life is determined as the time at which brightness of the lamp is 50% compared to that of initial value at the maximum lamp current(6.0mA_{RMS}) on condition of continuous operating at 25 ± 2°C
10. Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following. It shall help increase the lamp lifetime and reduce leakage current.
 - a. The asymmetry rate of the inverter waveform should be less than 10%.
 - b. The distortion rate of the waveform should be within $\sqrt{2} \pm 10\%$.

* Inverter output waveform had better be more similar to ideal sine wave.



* Asymmetry rate:

$$| I_p - I_{-p} | / I_{rms} * 100\%$$

* Distortion rate

$$I_p \text{ (or } I_{-p}) / I_{rms}$$

- ※ Do not attach a conducting tape to lamp connecting wire.
If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.

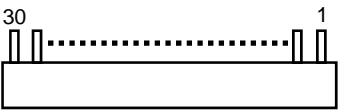
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3-2. Interface Connections

The interface connections are compatible with ISP (Industry Standard Panels) 15.0" Mounting and Top Level Interface Requirements (Version2, June,2000) defined by SPWG (Standard Panels Working Group). This LCD employs two interface connections, a 30 pin connector is used for the module electronics and the other connector is used for the integral backlight system.

The electronics interface connector is a model GT101-30S-HR11 manufactured by LG Cable. The pin configuration for the connector is shown in the table below.

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	VSS	Ground	<p>1, Interface chips 1.1 LCD : KZ4E064R11(LCD Controller) including LVDS Receiver 1.2 System : THC63LVDF823A or equivalent * Pin to Pin compatible with TI LVDS</p> <p>2. Connector 2.1 LCD : FI-XB30SR-HF11, JAE or equivalent. 2.2 Mating : FI-X30M or equivalent. 2.3 Connector pin arrangement</p> 
2	VCC	Power Supply, 3.3V Typ.	
3	VCC	Power Supply, 3.3V Typ.	
4	VEDID	for V _{EDID} .	
5	NC	For supplier test point	
6	Clk _{EDID}	for Clk _{EDID} .	
7	DATA _{EDID}	for Data _{EDID} .	
8	Odd_R _{IN} 0-	-LVDS differential data (odd pixels R0-R5, G0)	
9	Odd_R _{IN} 0+	+LVDS differential data (odd pixels R0-R5, G0)	
10	VSS	Ground	
11	Odd_R _{IN} 1-	-LVDS differential data (odd pixels G1-G5, B0-B1)	
12	Odd_R _{IN} 1+	+LVDS differential data (odd pixels G1-G5, B0-B1)	
13	VSS	Ground	
14	Odd_R _{IN} 2-	-LVDS differential data (odd pixels B2-B5, HS, VS, DE)	
15	Odd_R _{IN} 2+	+LVDS differential data (odd pixels B2-B5, HS, VS, DE)	
16	VSS	Ground	
17	Odd_Clk _{IN} -	-LVDS differential clock (odd pixels)	
18	Odd_Clk _{IN} +	+LVDS differential clock(odd pixels)	
19	VSS	Ground	
20	Even_R _{IN} 0-	-LVDS differential data (even pixels R0-R5, G0)	
21	Even_R _{IN} 0+	+LVDS differential data (even pixels R0-R5, G0)	
22	VSS	Ground	
23	Even_R _{IN} 1-	-LVDS differential data (even pixels G1-G5, B0-B1)	
24	Even_R _{IN} 1+	+LVDS differential data (even pixels G1-G5, B0-B1)	
25	VSS	Ground	
26	Even_R _{IN} 2-	-LVDS differential data (even pixels B2-B5, HS, VS, DE)	
27	Even_R _{IN} 2+	+LVDS differential data (even pixels B2-B5, HS, VS, DE)	
28	VSS	Ground	
29	Even_Clk _{IN} -	-LVDS differential clock (even pixels)	
30	Even_Clk _{IN} +	+LVDS differential clock (even pixels)	

Note: All GND(ground) pins should be connected together and to GND which should also be connected to the LCD's metal frame. All VCC (power input) pins should be connected together.

The backlight interface connector is a model BHSR-02VS-1, manufactured by JST or Compatible. The mating connector part number is SM02B-BHSS-1 or equivalent.

Table 4. BACKLIGHT CONNECTOR PIN CONFIGURATION (J1)

Pin	Symbol	Description	Notes
1	HV	Power supply for lamp (High voltage side)	1
2	LV	Power supply for lamp (Low voltage side)	1

Notes : 1. The high voltage side terminal is colored pink and the low voltage side terminal is Green.

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3-3. Signal Timing Specifications

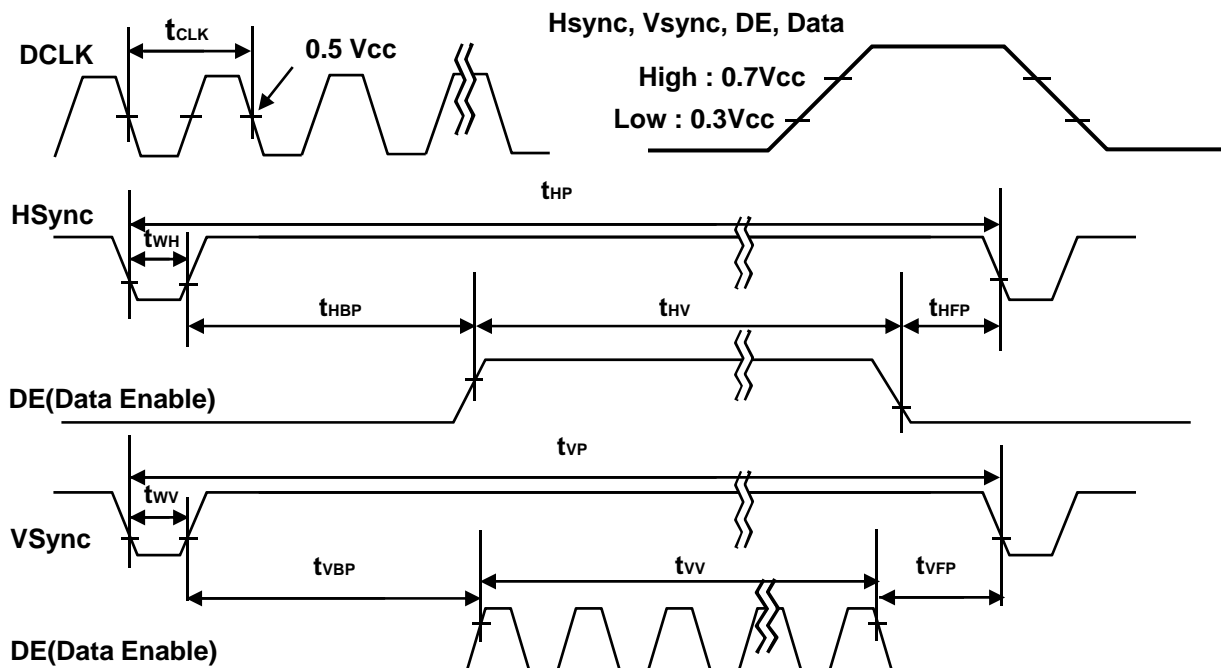
This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 5. Timing Table

	ITEM	SYMBOL	MIN	TYP	MAX	UNIT	NOTES
Dclk	Frequency	-	36	54	54.5	MHz	
Hsync	Period	t_{HP}	732	800	848	t_{CLK}	
	Width	t_{WH}	8	-	-		
Vsync	Period	t_{VP}	1060	1125	1150	t_{HP}	
	Frequency	f_V	40	60		Hz	1)
	Width	t_{WV}	2	-	-	t_{HP}	
DE (Data Enable)	Horizontal Valid	t_{HV}	700	700	700	t_{CLK}	
	Horizontal Back Porch	t_{HBP}	8	-	-		
	Horizontal Front Porch	t_{HFP}	8	-	-		
	Vertical Valid	t_{VV}	1050	1050	1050	t_{HP}	
	Vertical Back Porch	t_{VBP}	3	-	-		
	Vertical Front Porch	t_{VFP}	1	-	-		

1) 60 at Normal mode, 50,40 at Power save mode. Don't care Flicker level.

3-4. Signal Timing Waveforms



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3-5. Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color ; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

Color		Input Color Data																	
		RED						GREEN						BLUE					
		MSB			LSB			MSB			LSB			MSB			LSB		
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
					
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
					
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
BLUE	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
					
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

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3-6. Power Sequence

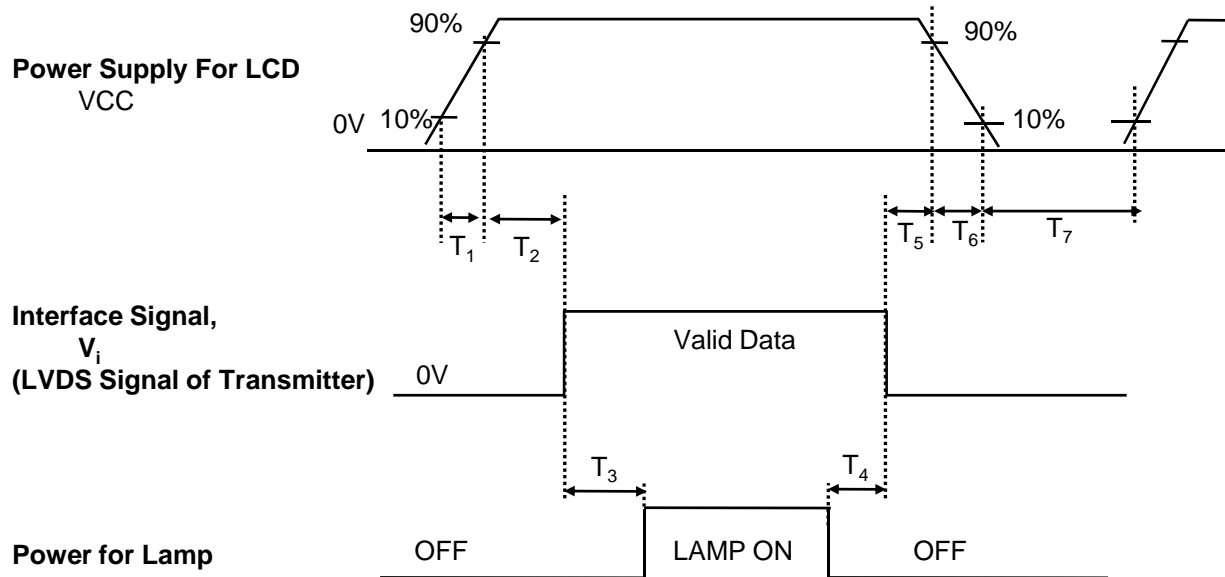


Table 7. POWER SEQUENCE TABLE

Parameter	Value			Units
	Min.	Typ.	Max.	
T ₁	-	-	10	(ms)
T ₂	0	-	50	(ms)
T ₃	200	-	-	(ms)
T ₄	0	-	-	(ms)
T ₅	0	-	-	(ms)
T ₆	0	-	10	(ms)
T ₇	150	-	-	(ms)

- Notes :
1. Please avoid floating state of interface signal at invalid period.
 2. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
 3. Lamp power must be turn on after power supply for LCD and interface signal are valid.

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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0°.

FIG. 1 presents additional information concerning the measurement equipment and method.

FIG. 1 Optical Characteristic Measurement Equipment and Method

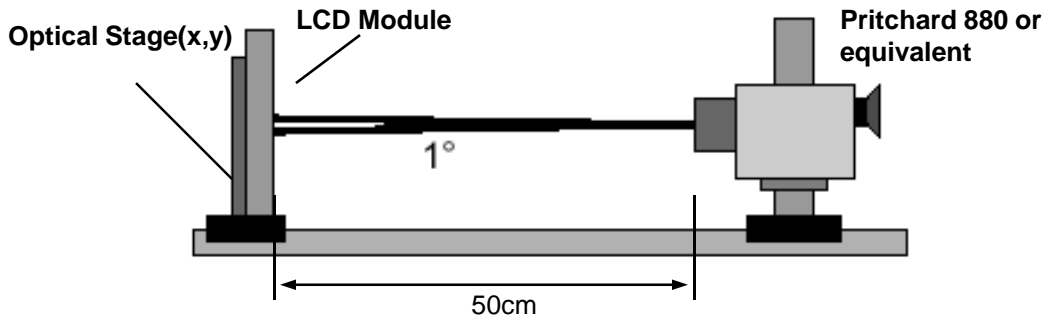


Table 9. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, fv=60Hz, Iout = 6.0mA

Parameter	Symbol	Values			Units	Notes
		Min	Typ	MAx		
Contrast Ratio	CR	200	300	-		1
Surface Luminance, white	L _{WH} (at 6.0mA)	170	200	-	cd/m ²	2
Luminance Variation	δ_{WHITE}	-	-	55	%	3
Response Time	Tr(Tr _{R+} , Tr _D)	-	30	45	ms	4
Color Coordinates						
RED	RX	0.558	0.588	0.618		
	RY	0.313	0.343	0.373		
GREEN	GX	0.290	0.320	0.350		
	GY	0.510	0.540	0.570		
BLUE	BX	0.125	0.155	0.185		
	BY	0.110	0.140	0.170		
WHITE	WX	0.285	0.313	0.341		
	WY	0.309	0.329	0.349		
Viewing Angle						
x axis, right($\Phi=0^\circ$)	Θ_r	40	45	-	degree	5
x axis, left ($\Phi=180^\circ$)	Θ_l	40	45	-	degree	
y axis, up ($\Phi=90^\circ$)	Θ_u	10	20	-	degree	
y axis, down ($\Phi=270^\circ$)	Θ_d	30	40	-	degree	
Color Gamut			45	-	%	

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Note)

1. Contrast Ratio(CR) is defined mathematically as

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

2. Surface luminance is the average of 1 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.
 (For surface Luminance [white], 200 cd/m² in IBM system is same as 190 cd/m² in LPL
 LPL use the PR880 for measurement with LG 663EZ-1301A inverter or equivalent)

$$L_{WH} = L_1$$

3. The variation in surface luminance , The panel total variation (δ_{WHITE}) is determined by measuring L_N at each test position 1 through 13 and then defined as followed numerical formula.
 For more information see FIG 2.

$$\delta_{WHITE} = \frac{\text{Maximum}(L_1, L_2, \dots L_{13}) - \text{Minimum}(L_1, L_2, \dots L_{13})}{\text{Maximum}(L_1, L_2, \dots L_{13})} \times 100$$

4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.

6. Gray scale specification

* $f_v = 60\text{Hz}$

Gray Level	Luminance [%] (Typ)
L0	0.20
L7	0.65
L15	3.30
L23	9.50
L31	20.3
L39	35.4
L47	53.0
L55	79.3
L63	100

FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

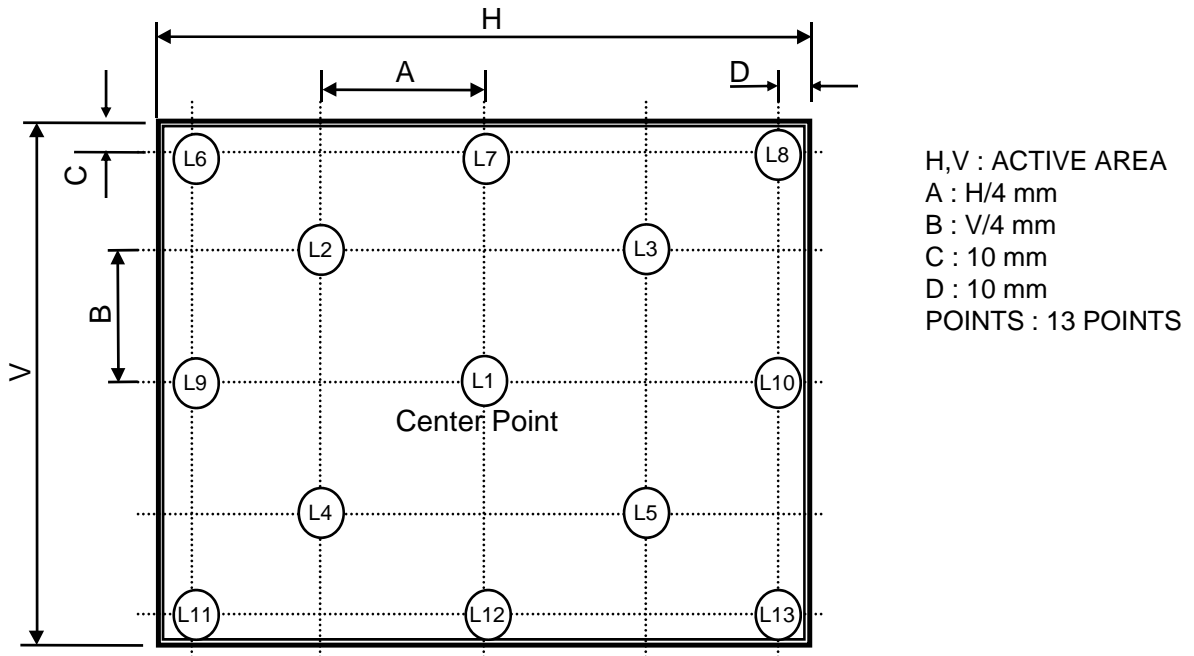
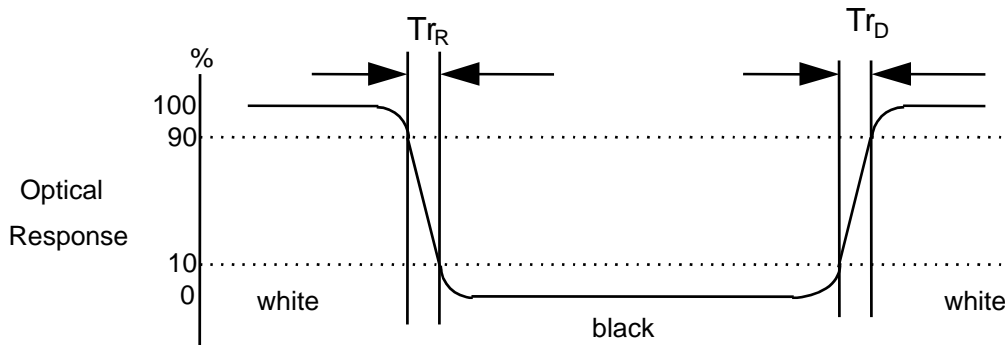


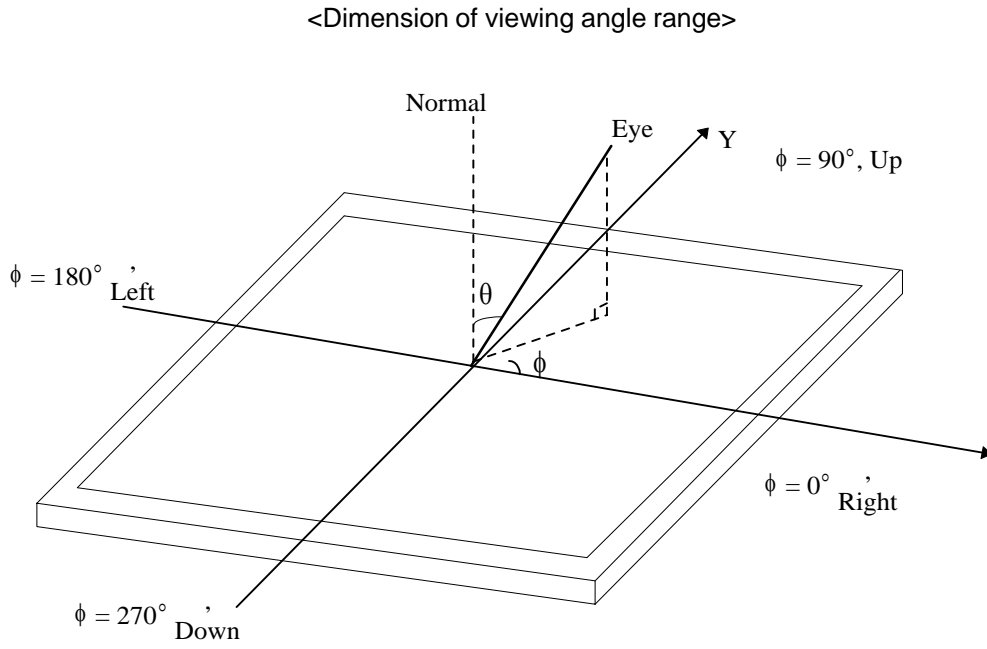
FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



Product Specification

FIG. 4 Viewing angle



Product Specification

5. Mechanical Characteristics

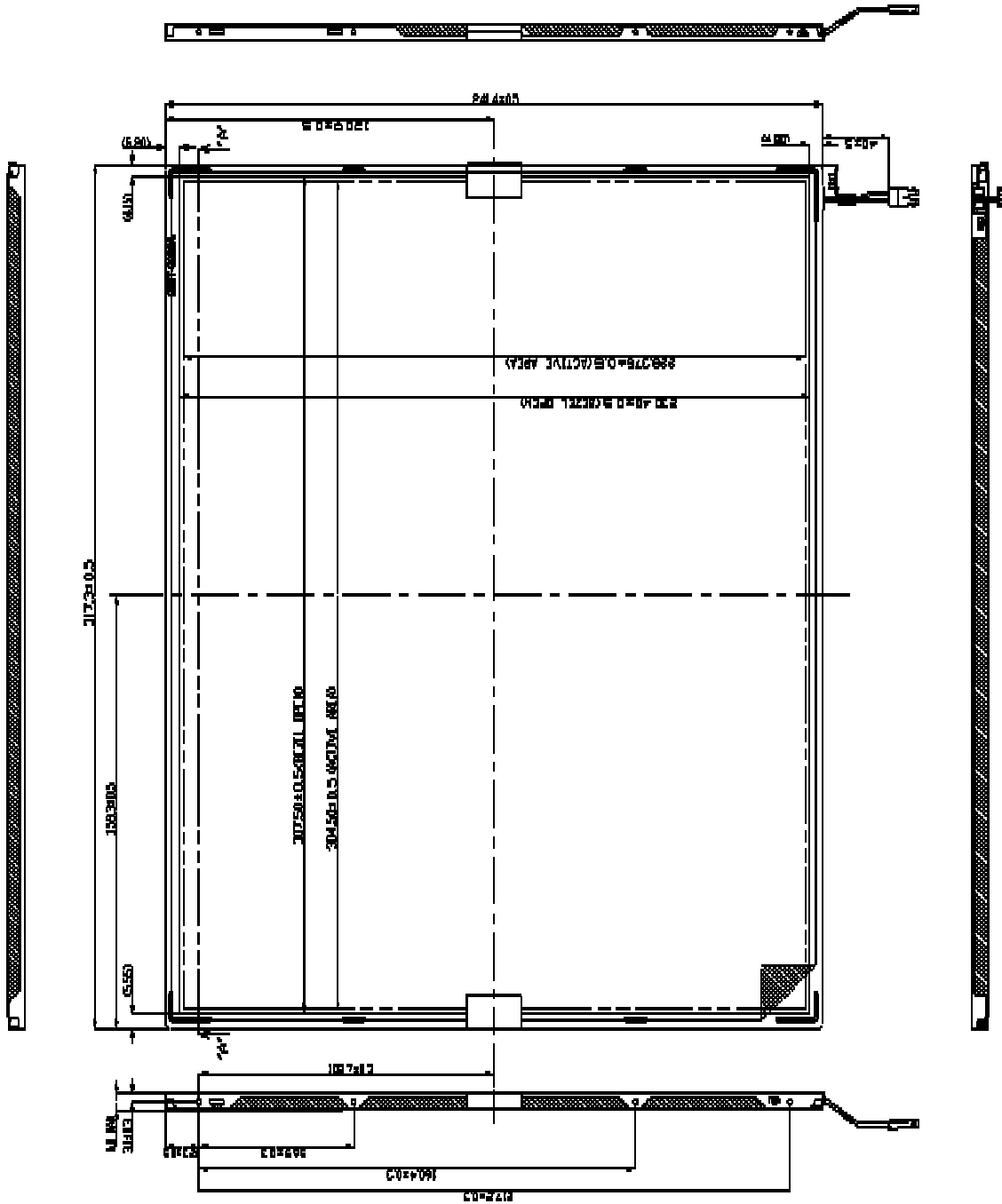
The contents provide general mechanical characteristics for the model LP150E07. In addition the figures in the next page are detailed mechanical drawing of the LCD.

Outline Dimension	Horizontal	317.3 ± 0.5mm
	Vertical	241.4 ± 0.5mm
	Depth	6.0mm(Max.)
Bezel Area	Horizontal	307.5 ± 0.5mm
	Vertical	231.4 ± 0.5mm
Active Display Area	Horizontal	304.5 mm
	Vertical	228.375 mm
Weight	520g (Typ.) 535g (Max)	
Surface Treatment	Hard coating(3H) Maker:Nitto Denco ARC150T	

Product Specification

<FRONT VIEW>

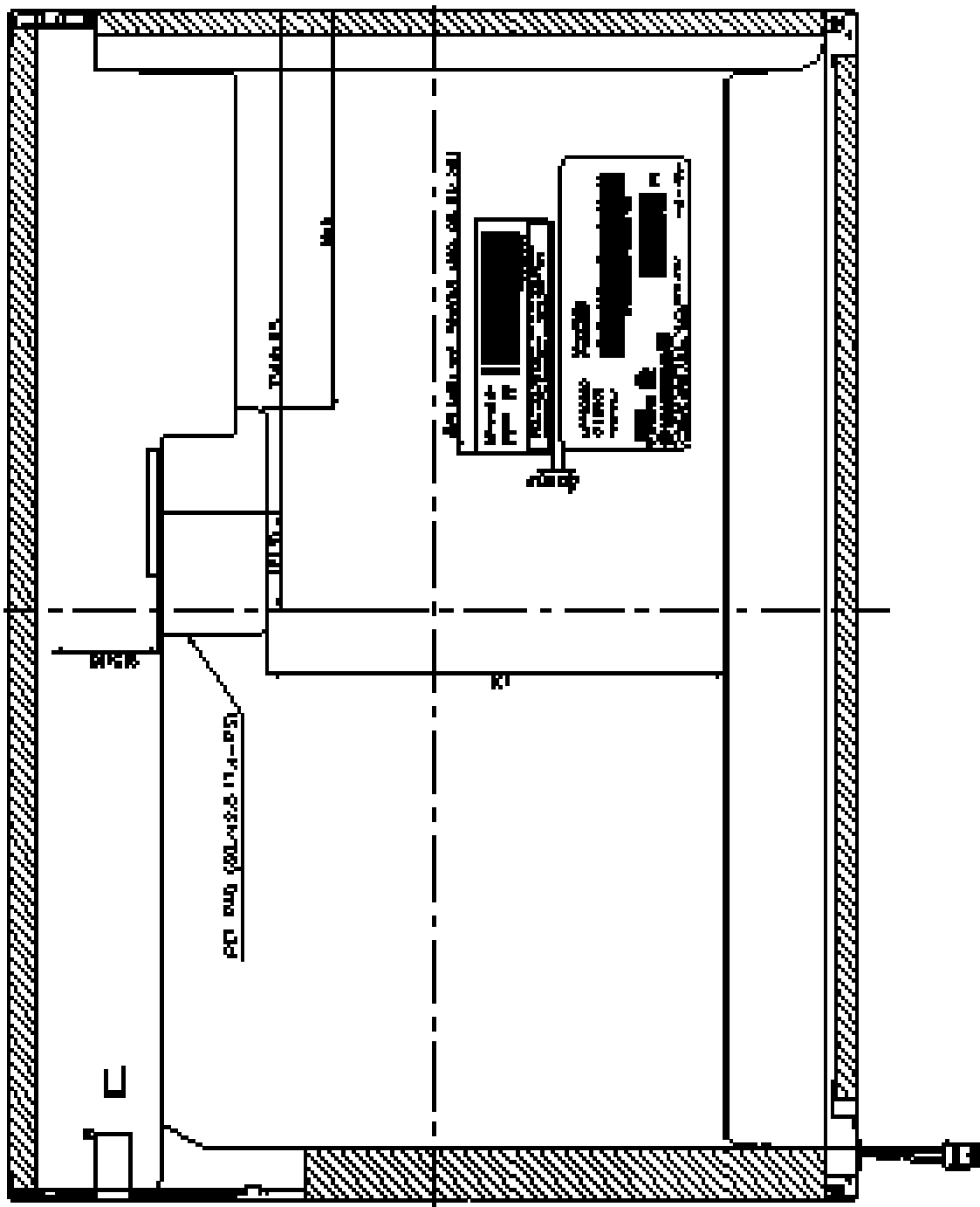
Note) Unit:[mm], General tolerance: $\pm 0.5\text{mm}$



Product Specification

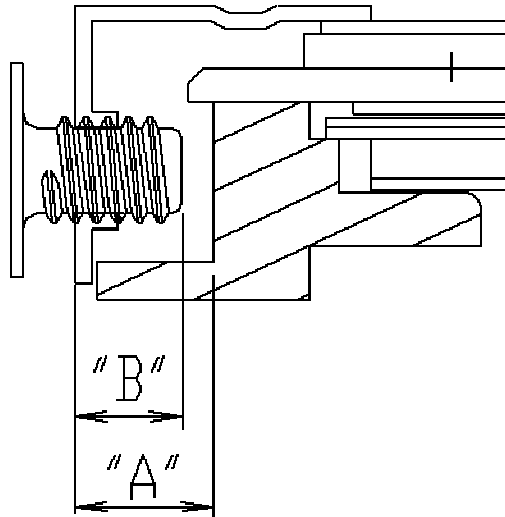
<REAR VIEW>

Note) Unit:[mm], General tolerance: $\pm 0.5\text{mm}$



Product Specification

[DETAIL DESCRIPTION OF SIDE MOUNTING SCREW]



SECTION A-A

- * Screw Torque : 2.5kgf.cm Max
- * Screw Hole Depth ("A") : Max 2.5mm
- * Screw Penetration Length ("B") : Max 2.5, Min 2.3

Product Specification

6. Reliability

Environment test condition

No.	Test Item	Conditions
1	High temperature storage test	Ta= 60°C, 240h
2	Low temperature storage test	Ta= -20°C, 240h
3	High temperature operation test	Ta= 50°C, 50%RH, 240h
4	Low temperature operation test	Ta= 0°C, 240h
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(i.e. run 180G 2ms for all six faces)
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr

{ Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

7. International Standards

7-1. Safety

- a) UL 60950, Third Edition, Underwriters Laboratories, Inc., Dated Dec. 11, 2000.
Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.
- b) CAN/CSA C22.2, No. 60950, Third Edition, Canadian Standards Association, Dec. 1, 2000.
Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.
- c) EN 60950 : 2000, Third Edition
IEC 60950 : 1999, Third Edition
European Committee for Electrotechnical Standardization(CENELEC)
EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)

Product Specification

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A	B	C	D	E	F	G	H	I	J	K	L	M
---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C : SIZE(INCH)

E : MONTH

G : ASSEMBLY CODE

D : YEAR

F : FACTORY CODE

H ~ M : SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	A	B	C

3. FACTORY CODE

Factory Code	LPL Gumi	LPL Nanjing	HEESUNG
Mark	K	C	D

4. SERIAL NO.

Mark	100001~199999, 200001~299999, 300001~399999,, A00001~A99999,, Z00001~Z99999
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b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module.
This is subject to change without prior notice.

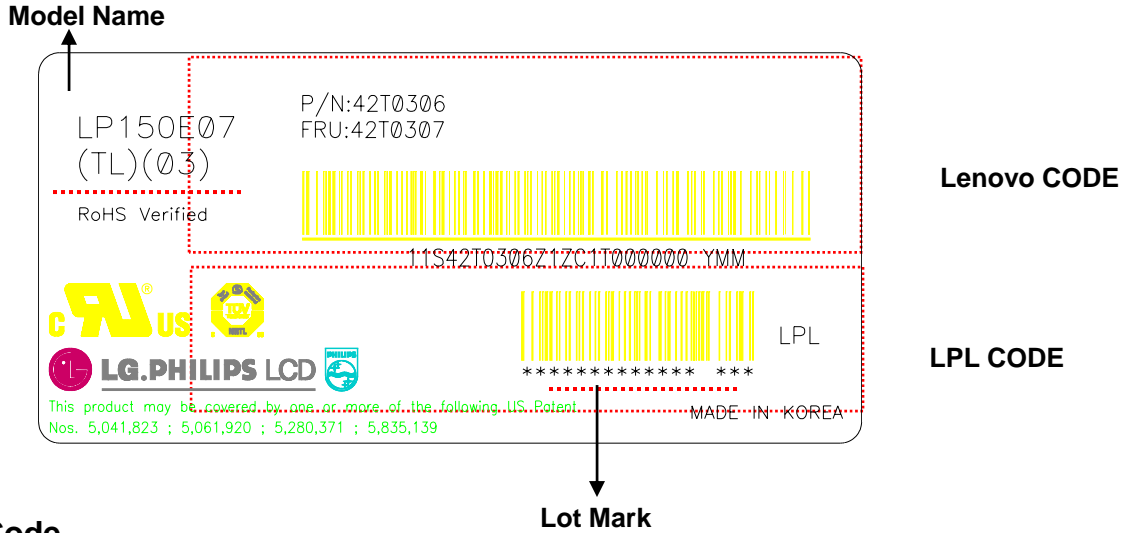
8-2. Packing Form

a) Package quantity in one box : 12 pcs

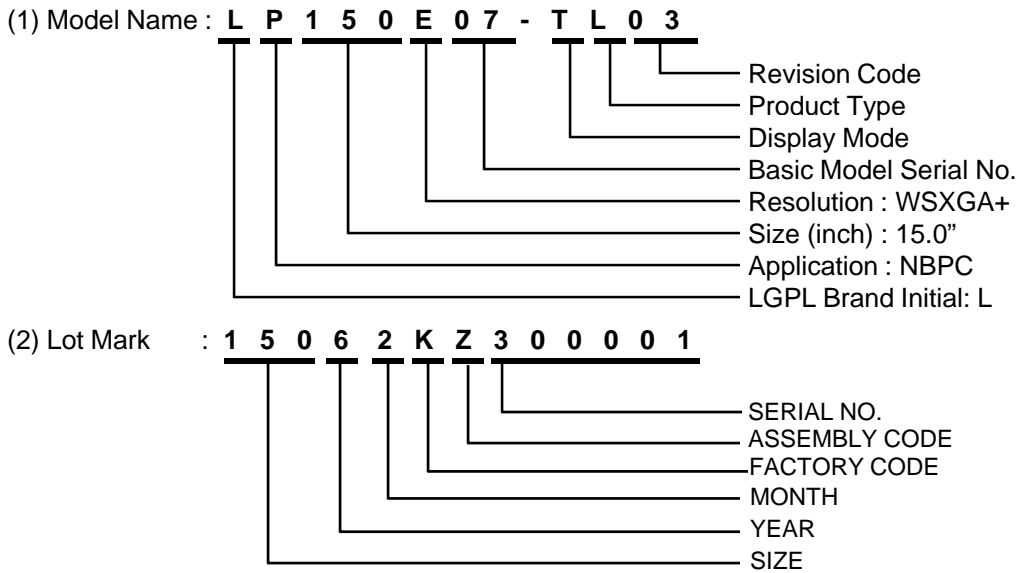
b) Box Size : 376mm × 321mm × 317mm

Product Specification

8-3. Label Description



LPL Code



Lenovo Code

- 1)P/N : 42T0306
- 2)FRU : 42T0307

9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
 $V = \pm 200\text{mV}$ (Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape.
When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

Product Specification

APPENDIX [A] - Enhanced Extended Display Identification Data (EEDID™) 1/3

Byte# (decimal)	Byte# (HEX)	Field Name and Comments	Value (HEX)	Value (binary)	
0	00	Header	0 0	00000000	Header
1	01	Header	F F	11111111	
2	02	Header	F F	11111111	
3	03	Header	F F	11111111	
4	04	Header	F F	11111111	
5	05	Header	F F	11111111	
6	06	Header	F F	11111111	
7	07	Header	0 0	00000000	
8	08	ID system Manufacturer Name	3 0	00110000	Vender/ Product ID
9	09	Compressed ASCII	A F	10101110	
10	0A	ID Product Code	4 2	01000010	
11	0B	ID Product Code	4 0	01000000	
12	0C	LCD Module Serial No. = 0 (if not used)	0 0	00000000	
13	0D	LCD Module Serial No. = 0 (if not used)	0 0	00000000	
14	0E	LCD Module Serial No. = 0 (if not used)	0 0	00000000	
15	0F	LCD Module Serial No. = 0 (if not used)	0 0	00000000	
16	10	Week of Manufacture	0 0	00000000	
17	11	Year of Manufacture	0 F	00001111	
18	12	EDID Structure version	0 1	00000001	EDID Version/ Revision
19	13	EDID Revision	0 3	00000011	
20	14	Video input definition = Digital / p.non TMDS CRGB	8 0	10000000	Display Parameter
21	15	Max H image size (cm) = 33.12cm	1 F	00011110	
22	16	Max V image size (cm) = 20.70cm	1 7	00010111	
23	17	Display gamma = 2.2	7 8	01111000	
24	18	Feature support (DPMS) = Active off, RGB Color	F A	11101010	Color Characteristic
25	19	Red/Green low Bits	3 C	00111100	
26	1A	Blue/White low Bits	8 0	10000000	
27	1B	Red X Rx = 0.588	9 6	10010110	
28	1C	Red Y Ry = 0.343	5 7	01010111	
29	1D	Green X Gx = 0.320	5 1	01010001	
30	1E	Green Y Gy = 0.540	8 A	10001010	
31	1F	Blue X Bx = 0.155	2 7	00100111	
32	20	Blue Y By = 0.140	2 3	00100011	
33	21	White X Wx = 0.313	5 0	01010000	Established Timings
34	22	White Y Wy = 0.329	5 4	01010100	
35	23	Established Timing I	2 1	00100001	
36	24	Established Timing II	0 8	00001000	
37	25	Manufacturer's Timings	0 0	00000000	Standard Timing ID
38	26	Standard Timing Identification 1 was not used	8 1	10000001	
39	27	Standard Timing Identification 1 was not used	8 0	10000000	
40	28	Standard Timing Identification 2 was not used	0 1	00000001	
41	29	Standard Timing Identification 2 was not used	0 1	00000001	
42	2A	Standard Timing Identification 3 was not used	0 1	00000001	
43	2B	Standard Timing Identification 3 was not used	0 1	00000001	
44	2C	Standard Timing Identification 4 was not used	0 1	00000001	
45	2D	Standard Timing Identification 4 was not used	0 1	00000001	
46	2E	Standard Timing Identification 5 was not used	0 1	00000001	
47	2F	Standard Timing Identification 5 was not used	0 1	00000001	
48	30	Standard Timing Identification 6 was not used	0 1	00000001	
49	31	Standard Timing Identification 6 was not used	0 1	00000001	
50	32	Standard Timing Identification 7 was not used	0 1	00000001	
51	33	Standard Timing Identification 7 was not used	0 1	00000001	
52	34	Standard Timing Identification 8 was not used	0 1	00000001	
53	35	Standard Timing Identification 8 was not used	0 1	00000001	

Product Specification

APPENDIX [A] - Enhanced Extended Display Identification Data (EEDID™) 2/3

Byte# (decimal)	Byte# (HEX)	Field Name and Comments	Value (HEX)	Value (binary)		
54	36	Pixel Clock/10,000 (LSB)	3 0	00110000	Detailed Timing Description #1	
55	37	Pixel Clock/10,000 (MSB) /	2 A	00101010		
56	38	Horizontal Active	7 8	01111000		
57	39	Horizontal Blanking	2 0	00100000		
58	3A	Horizontal Active : Horizontal Blanking	5 1	01010001		
59	3B	Vertical Active	1 A	00011010		
60	3C	Vertical Blanking	1 0	00010000		
61	3D	Vertical Active : Vertical Blanking	4 0	01000000		
62	3E	Horizontal Sync Offset	3 0	00110000		
63	3F	Horizontal Sync Pulse Width	7 0	01110000		
64	40	Vertical Sync Offset : Sync Width	1 3	00010011		
65	41	Horizontal Vertical Sync Offset/Width upper 2bits = 0	0 0	00000000		
66	42	Horizontal Image Size	3 0	00110000		
67	43	Vertical Image Size	F 4	11100100		
68	44	Horizontal & Vertical Image Size	1 0	00010000		
69	45	Horizontal Border = 0	0 0	00000000		
70	46	Vertical Border = 0	0 0	00000000		
71	47	Non-interlaced, Normal display, no stereo, Digital separate sync, H/V pol negatives	1 8	00011000		
72	48	Pixel Clock/10,000 (LSB) 50Hz	2 5	00100101		Detailed Timing Description #2
73	49	Pixel Clock/10,000 (MSB) / 50Hz	2 3	00100011		
74	4A	Horizontal Active	7 8	01111000		
75	4B	Horizontal Blanking	2 0	00100000		
76	4C	Horizontal Active : Horizontal Blanking	5 1	01010001		
77	4D	Vertical Active	1 A	00011010		
78	4E	Vertical Blanking	1 0	00010000		
79	4F	Vertical Active : Vertical Blanking	4 0	01000000		
80	50	Horizontal Sync Offset	3 0	00110000		
81	51	Horizontal Sync Pulse Width	7 0	01110000		
82	52	Vertical Sync Offset : Sync Width	1 3	00010011		
83	53	Horizontal Vertical Sync Offset/Width upper 2bits = 0	0 0	00000000		
84	55	Horizontal Image Size	3 0	00110000		
85	55	Vertical Image Size	F 4	11100100		
86	56	Horizontal & Vertical Image Size	1 0	00010000		
87	57	Horizontal Border = 0	0 0	00000000		
88	58	Vertical Border = 0	0 0	00000000		
89	59	Non-interlaced, Normal display, no stereo, Digital separate sync, H/V pol negatives	1 8	00011000		
90	5A	Detailed Timing Descriptor #3	0 0	00000000	Detailed Timing Description #3	
91	5B		0 0	00000000		
92	5C	ASCII Data String Tag (Supplier Name)	0 0	00000000		
93	5D		0 F	00001111		
94	5E		0 0	00000000		
95	5F	(Horizontal active pixel / 8) - 31	9 0	10010000		
96	60	Image Aspect Ratio(16:10)	4 3	01000011		
97	61	Low Refresh Rate #1(50Hz)	3 2	00110010		
98	62	(Horizontal active pixel / 8) - 31	9 0	10010000		
99	63	Image Aspect Ratio(16:10)	4 3	01000011		
100	64	Low Refresh Rate #2(40Hz)	2 8	00101000		
101	65	Brightness(1/10nit)	1 4	00010100		
102	66	Feature flag(TN mode)	0 1	00000001		
103	67	Reserved 00h	0 0	00000000		
104	68	EISA manufacturer code(3 Character ID)	3 2	00110010		
105	69	Compressed ASCII	0 C	00001100		
106	6A	Panel Supplier Reserved - Product code	5 3	01010011		
107	6B	(Hex LSB first)	2 1	00100001		

Product Specification

APPENDIX [A] - Enhanced Extended Display Identification Data (EEDID™) 3/3

Byte# (decimal)	Byte# (HEX)	Field Name and Comments	Value (HEX)	Value (binary)	
108	6C	Detailed Timing Descriptor #4	0 0	00000000	Detailed Timing Description #4
109	6D		0 0	00000000	
110	6E	ASCII Data String Tag (Supplier S/ N)	0 0	00000000	
111	6F		E E	11111110	
112	70		0 0	00000000	
113	71	I	4 C	01001100	
114	72	P	5 0	01010000	
115	73	1	3 1	00110001	
116	74	5	3 5	00110101	
117	75	0	3 0	00110000	
118	76	F	4 5	01000101	
119	77	0	3 0	00110000	
120	78	7	3 7	00110111	
121	79	-	2 D	00101101	
122	7A	T	5 4	01010100	
123	7B	I	4 C	01001100	
124	7C	0	3 0	00110000	
125	7D	3	3 3	00110011	
126	7E	Extension flag = 00	0 0	00000000	Extension Flag
127	7E	Checksum	C A	11001010	Checksum