MITSUBISHI ELECTRIC Corp. LCD Division, Design Technology Department 997 MIYOSHI, KOSHI-SHI KUMAMOTO, 861-1197, JAPAN PHONE +81-96-242-6175 FAX +81-96-242-5871

Messrs. DISTECK DISPLAYS INC.

Products Specification

Products nameCOLOR TFT-LCDType nameAA104VC01

This products specification includes d/d(documents and drawings) in the below table.

item	d/d No.	Rev. No	Title of d/d	Number of Pages	Remarks
01	AA104VC01 - 02 - 02	1 1 1 1 1	AA104VC01 TECHNICAL SPECIFICATION	25	
02	AA104 - 03	в	AA104 SERIES PACKAGING SPECIFICATION	6	
03	PRODUCTLABELE	м	PRODUCTS NUMBER LABELING FORMS	3	
04	AA104VC01 - 06	н	LAMP UNIT for 10.4"VGA (AA104VC**)	9	
05					

Authorization for submission, (Oct.30, 2007)						
	Name	Title	Signature			
Prepared	H.Ito	Module Products Design & Planning	伊藤 寬泰			
Checked	T.Ikemoto	Deputy Manager Module Products Design & Planning	池市的也			
Approved	M.Nishimura	Manager Module Products Design & Planning	刑打磨			

(note 1) Three copies of this specification are submitted. Please return one copy with the receipt signature to us.

(note 2) When altering this specification, please consult us in advance and correct with red ink.

(note 3) In the case that we change applicable specifications, the revised specification shall be submitted for your receipt.

	Specification Receipt							
Date	Name	Title	Signature					

Products Specification	Rev. No.
ADPS - AA104VC01 - 01 - 02	

Revision Status for Products Specification

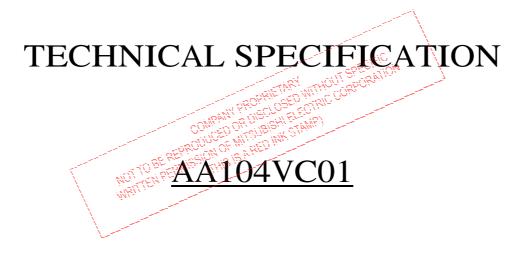
Products nameCOLOR TFT-LCDType nameAA104VC01

Rev.	Description	Rev.Date	Prepared	Checked
Kev.	Description	Rev.Date	Trepared	Approved
First	First Revision	Oct.30,'07	H.Ito	T.Ikemoto
THSU		001.30,07	H.110	M.Nishimura

Products Specification	Rev.No.
ADPS - AA104VC01 - 01 - 02	

For DISTECK DISPLAYS INC.

10.4"VGA



MITSUBISHI ELECTRIC Corp.

Date: Oct.30,'07

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1. APPLICATION

This specification applies to color TFT-LCD module, AA104VC01.

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MITSUBISHI classifies the usage of the TFT-LCD module as follows. Please confirm the usage before using the product.

(1) Standard Usage

Computers, office equipment, factory automation equipment, test and measurement equipment, communications, transportation equipment(automobiles, ships, trains, etc.), provided, however, that operation is not influenced by TFT-LCD directly.

(2) Special Usage

Medical equipment, safety equipment, transportation equipment, provided, however, that TFT-LCD is necessary to its operation.

(3) Specific Usage

Cockpit Equipment, military systems, aerospace equipment, nuclear reactor control systems, life support systems and any other equipment. MITSUBISHI should make a contract that stipulate apportionment of responsibilities between MITSUBISHI and our customer.

The product specified in this document is designed for "Standard Usage" unless otherwise specified in this document. If customers intend to use the product for applications other than those specified for "Standard Usage", they should first contact MITSUBISHI sales representative for it's intended use in writing.

MITSUBISHI has been making continuous effort to improve the reliability of its products. Customers should implement sufficient reliability design of their application equipments such as redundant system design, fail-safe functions, anti-failure features.

MITSUBISHI assumes no responsibility for any damage resulting from the use of the product that does not comply with the instructions and the precautions specified in this document.

Please contact and consult a MITSUBISHI sales representative for any questions regarding this product.

2. OVERVIEW

AA104VC01 is 10.4" color TFT-LCD (Thin Film Transistor Liquid Crystal Display) module composed of LCD panel, driver ICs, control circuit, and backlight unit.

By applying 6 bit digital data 640×480 , 262k-color images are displayed on the 10.4" diagonal screen. Input power voltages are 3.3 / 5.0V for LCD driving.

The type of data and control signals are digital and transmitted via CMOS interface per Typ. 25 MHz clock cycle.

Inverter for backlight is not included in this module. General specifications are summarized in the following table:

ITEM	SPECIFICATION
Display Area (mm)	211.2(H) × 158.4 (V) (10.39-inch diagonal)
Number of Dots	$640 \times 3 (H) \times 480 (V)$
Pixel Pitch (mm)	0.33 (H) × 0.33 (V)
Color Pixel Arrangement	RGB vertical stripe
Display Mode	Normally white TN
Number of Color	262k
Luminance (cd/m ²)	430
Wide Viewing Angle Technology	Optical compensation film
Viewing Angle (CR \ge 10)	$-65 \sim 65^{\circ}$ (H) $-45 \sim 65^{\circ}$ (V)
Surface Treatment	Anti-glare and hard-coating 3H
Electrical Interface	CMOS
Optimum Viewing Angle(Contrast ratio)	12 o'clock
Module Size (mm)	243.0 (W) × 181.6 (H) × 12.2 (D)
Module Mass (g)	540
Backlight Unit	CCFL, 2-tubes, edge-light, replaceable

Characteristic value without any note is typical value.

3. ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX.	UNIT
Power Supply Voltage for LCD	VCC	-0.3	6.5	V
Logic Input Voltage	VI	-0.3	6.5	V
Lamp Voltage	VL	0	1500	Vrms
Lamp Current	IL	0	10.0	mArms
Lamp Frequency	FL		80	kHz
Operation Temperature(Panel) Note 1,2)	Top(Panel)	-20	70	°C
Operation Temperature(Ambient) Note 2)	Top(Ambient)	-20	70	°C
Storage Temperature Note 2)	T _{stg}	-20	80	°C

[Note]

1) Measured at the center of active area and at the center of panel back surface

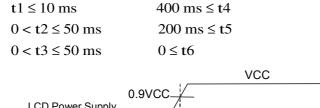
2) Top,Tstg $\leq 40^{\circ}$ C : 90%RH max. without condensation

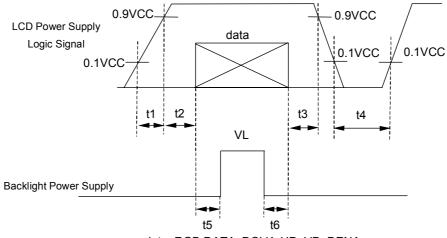
Top,Tstg > 40°C : Absolute humidity shall be less than the value of 90%RH at 40°C without condensation.

4. ELECTRICAL CHARACTERISTICS

(1) TFT- LCD		Ambient Temperature : $Ta = 25^{\circ}C$					
ITEN	А	SYMBOL	MIN.	TYP.	MAX.	UNIT	Remarks
Power Supply	3.3V powered	VCC	3.0	3.3	3.6	V	*1)
Voltage for LCD	5.0V powered	VCC	4.75	5.0	5.25	V	*1)
Power Supply	3.3V powered	ICC	-	300	450	mA	*2)
Current for LCD	5.0V powered	ICC	-	200	300	mA	*2)
Permissive Input Ripple Voltage		VRP	-	-	100	mVp-p	VCC = +3.3V/5.0V
Logic Input	High	VIH	2.0	-	5.25	V	
Voltage	Low	VIL	0	-	0.8	V	

*1) Power and signals sequence:





data: RGB DATA, DCLK, HD, VD, DENA

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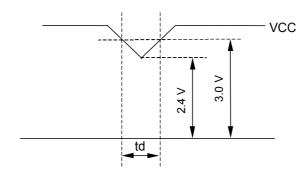
AA104VC01_02_02_First

VCC-dip conditions:

(a) 3.3 V powered

- 1) When 2.4 V \leq VCC < 3.0 V, td \leq 10 ms
- 2) When VCC < 2.4 V

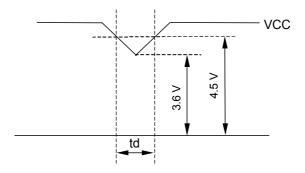
VCC-dip conditions should also follow the power and signals sequence.



(b) 5.0V powered

- 1) When 3.6 V \leq VCC < 4.5 V, td \leq 10 ms
- 2) When VCC < 3.6 V

VCC-dip conditions should also follow the power and signals sequence.



*2) Typical current condition:

64- gray- bar-pattern

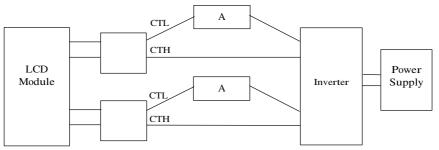
480 line mode

VCC = + 3.3 / 5.0 V, f_H=31.5 kHz, f_V=60 Hz, f_{CLK}=25 MHz

(2) Backlight	(2) Backlight Ta=25°C							
ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	Remarks		
Lamp Voltage	VL		480		Vrms	IL=6.0 mArms		
Lamp Current	IL	3.0	6.0	7.0	mArms	*1),*5)		
Lamp Frequency	FL	40		80	kHz	*2)		
	VS	900			Vrms	$Ta = 25^{\circ}C$		
Starting Lamp Voltage		1170			Vrms	$Ta = 0^{\circ}C$		
		1300			Vrms	$Ta = -20^{\circ}C$		
Lamp Life Time	LT	50000			h	*3),*4) IL=6.0mArms Continuous Operation		

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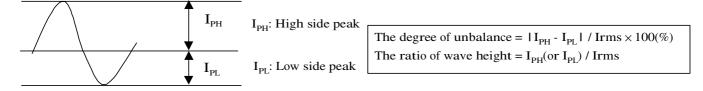
*1) Lamp Current measurement method (The current meter is inserted in low voltage line.)



- *2) Lamp frequency of inverter may produce interference with horizontal synchronous frequency, and this may cause horizontal beat on the display. Therefore, please adjust lamp frequency, and keep inverter as far from module as possible or use electronic shielding between inverter and module to avoid the interference.
- *3) Lamp life time is defined as the time either when the brightness becomes 50% of the initial value, or when the starting lamp voltage does not meet the value specified in this table.
- *4) The life time of the backlight depends on the ambient temperature. The life time will decrease under low/high temperature.
- *5) Please use the inverter which has symmetrical current wave form as follows,

The degree of unbalance: less than 10%

The ratio of wave height: less than $\sqrt{2} \pm 10\%$



CURRENT WAVE FORM

5. INTERFACE PIN CONNECTION

(1) CN 1(Interface Signal)

Used connector: DF9B-31P-1V(32) (HIROSE) Corresponding connector: DF9-31S-1V (HIROSE)

Corresponding connector: DF9-31S-1V (HIROSE)							
Pin No.	Symbol	Function					
1	GND						
2	DCLK	Clock signal for sampling catch data signal					
3	HD	Horizontal sync signal					
4	VD	Vertical sync signal					
5	GND						
6	R0	Red data signal(LSB)					
7	R1	Red data signal					
8	R2	Red data signal					
9	R3	Red data signal					
10	R4	Red data signal					
11	R5	Red data signal(MSB)					
12	GND						
13	G0	Green data signal(LSB)					
14	G1	Green data signal					
15	G2	Green data signal					
16	G3	Green data signal					
17	G4	Green data signal					
18	G5	Green data signal(MSB)					
19	GND						
20	B0	Blue data signal(LSB)					
21	B1	Blue data signal					
22	B2	Blue data signal					
23	B3	Blue data signal					
24	B4	Blue data signal					
25	B5	Blue data signal(MSB)					
26	GND						
27	DENA	Data enable signal (to settle the viewing area)					
28	VCC	3.3 / 5.0 V Power Supply					
29	VCC	3.3 / 5.0 V Power Supply					
30	TEST	This pin should be open. Test signal output for only internal test use.					
31	SC	Scan direction control (Low=Normal, High=Reverse)					
		case is connected with GND					

*) The shielding case is connected with GND

(2) CN 2, CN 3 (Backlight)

Backlight-side connector: BHR-02(8.0)VS-1N (JST) Inverter-side connector: SM02(8.0)B-BHS(LF)(SN) (JST)

Pin No.	Symbol	Function
1	CTH	VBLH (High Voltage)
2	CTL	VBLL (Low Voltage)
[Note]		

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6. INTERFACE TIMING

	ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
	Frequency	fclk		25	29	MHz
DCLK	Period	t CLK	34.5	40		ns
DCLK	Low Width	t _{WCL}	12			ns
	High Width	t _{WCH}	12			ns
DATA(R,G,B), DENA,HD,	Set up time	t _{DS}	5			ns
VD	Hold time	t DH	5			ns
	Horizontal Active Time	t ha	640	640	640	t CLK
	Horizontal Front Porch	t _{HFP}	10	16		t _{CLK}
	Horizontal Back Porch	t _{HBP}	2	138		t _{CLK}
DENA	Vertical Active Time	tva	480	480	480	t _H
	Vertical Front Porch	tvfp	1	13		t _H
	Vertical Back Porch	t _{VBP}	2	33		t _H
	Frequency	$\mathbf{f}_{\mathbf{H}}$	27	31.5	38	kHz
HD	Period	t H	26.3	31.7	37.0	μs
	Low Width	twhl	5	96		t CLK
	Frequency	fv	55	60	70	Hz
VD	Period	tv	14.3	16.7	18.2	ms
	Low Width	twvl	3			t _H

(1) Timing Specifications

[Note]

1) DATA is latched at fall edge of DCLK in this specification.

2) Polarities of HD and VD are negative in this specification.

3) DENA (Data Enable) should always be positive polarity as shown in the timing specification.

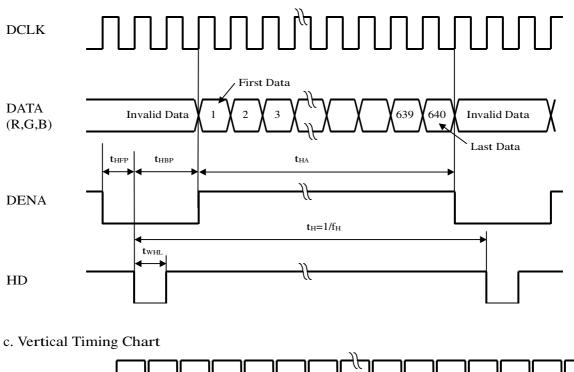
4) DCLK should appear during all invalid period, and HD should appear during invalid period of frame cycle.

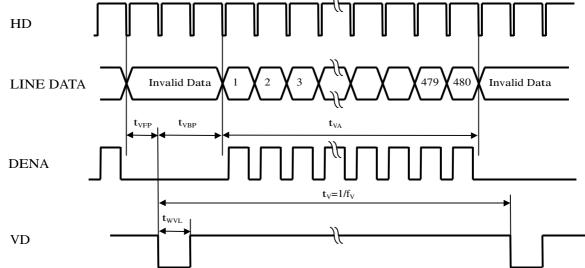
5) there + there ≥ 20 tclk

(2) Timing Chart

a. Pixel Timing Chart DCLK DATA(R,G,B), DENA, HD, VD

b. Horizontal Timing Chart





(3) Color Data Assignment

	Data Assign		-						IN	PUT	' DA'	ГА							
				R D.	ATA					G D	ATA					B D.	ATA		
C	OLOR	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	В5	B4	B3	B2	B1	BO
		MSB					LSB	MSB					LSB	MSB					LSB
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
BASIC	BLUE(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
COLOR	CYAN	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	MAGENTA	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																			
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	GREEN(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
GREEN																			
	GREEN(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	BLUE(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
BLUE												ļ	ļ					ļ	
													ļ					ļ	
	BLUE(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

[Note]

1) Definition of gray scale

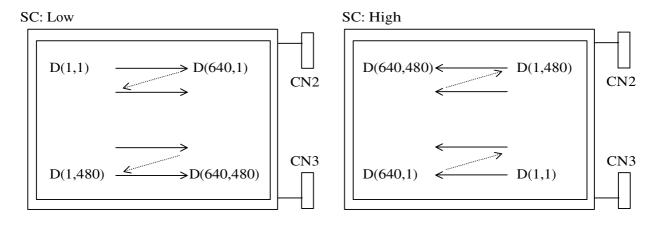
Color (n) --- n indicates gray scale level. Higher n means brighter level.

2) Data

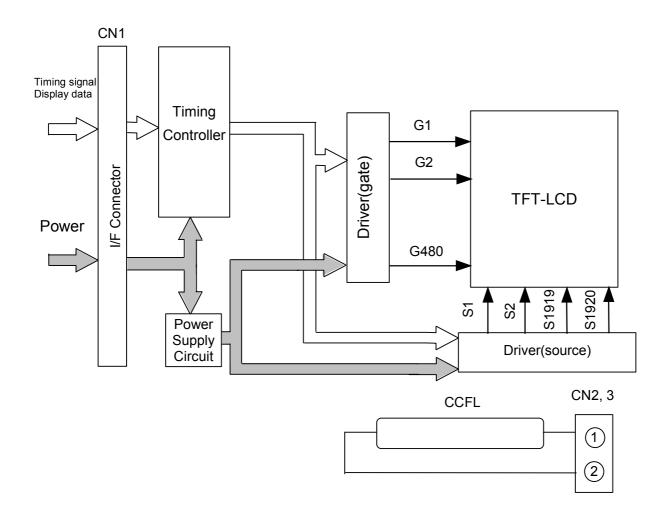
1:High, 0: Low

(4) Display Position and Scan Direction

D(X,Y) shows the data number of input signal for LCD panel signal processing PCB.

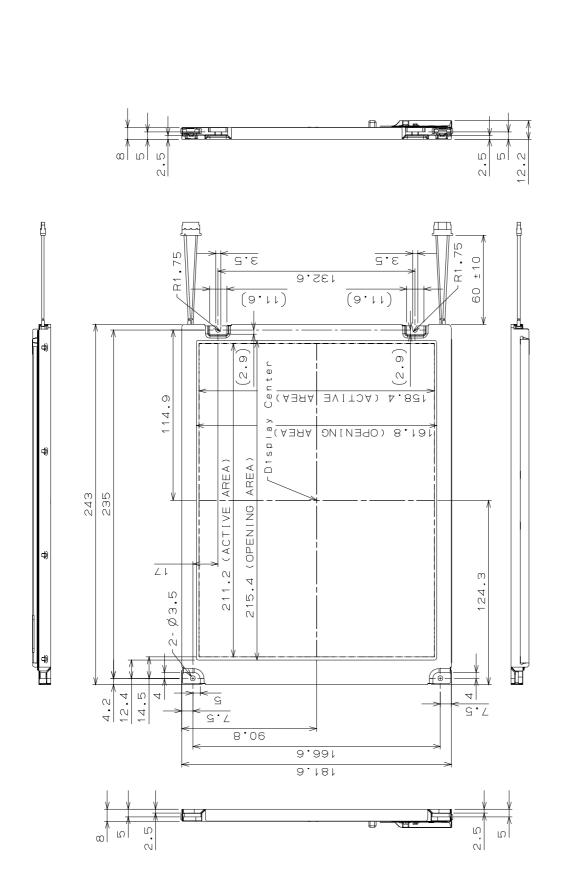


7. BLOCK DIAGRAM



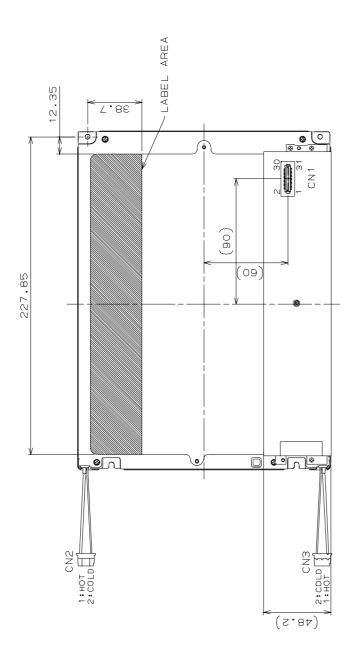
8. MECHANICAL SPECIFICATIONS

(1) Front Side



1)Tolerance is $\pm 0.5 \text{mm}$ unless noted 2)Except for thickness of PET film

(Unit:mm)



1)Tolerance is ±0.5mm unless noted 2)Except for thickness of PET film

> CN1:DF9B-31P-1V(32)(H1ROSE) CN2,CN3:BHR-02(8.0)VS-1N(JST)

> > (Unit:mm)

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9. OPTICAL CHARACTERISTICS

			Га=25°С, VCC=3.	· · · · ·		V 1	1	1 in Section 6
ITE	M	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	Remarks
Contrast Rat	io	CR	$\theta_V=0^\circ, \theta_H=0^\circ$	350	500			*1)*2)*5)
Luminance		Lw	$\theta_V=0^\circ, \theta_H=0^\circ$	340	430		cd/m ²	*1)*5)
Luminance U	Jniformity	ΔLw	$\theta_V=0^\circ, \theta_H=0^\circ$			30	%	*1)*3)*5)
Desmanas Tie		tr	$\theta_V=0^\circ, \theta_H=0^\circ$		10		ms	*1)*4)*5)
Response Tir	ne	tf	$\theta_V=0^\circ, \theta_H=0^\circ$		30		ms	*1)*4)*5)
	Horizontal	$\theta_{\rm H}$	CR ≥ 10	-50~50	-65~65		0	*1)*5)
Viewing	Vertical	$\theta_{\rm V}$	$CK \ge 10$	-30~40	-45~65		0	*1)*5)
Angle	Horizontal	$\theta_{\rm H}$	$CD \geq 5$	-65~65	-80~80			*1)*5)
	Vertical	$\theta_{\rm V}$	$CR \ge 5$	-40~50	-50~80			*1)*5)
Image Sticki	ng	tis	2 h			2	s	*6)
	Red	Rx		0.555	0.585	0.615		
		Ry		0.304	0.334	0.364		
Color	Green	Gx		0.300	0.330	0.360		
Coordinates		Gy	θv=0°, θ _H =0°	0.506	0.536	0.566		*1)*5)
	Blue	Bx	0v=0,0H=0	0.138	0.168	0.198		
		By		0.143	0.173	0.203		
	White	Wx		0.300	0.330	0.360		
		Wy		0.312	0.342	0.372		

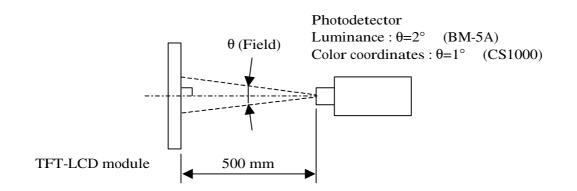
Ta-25°C VCC-3.3/50V Input Signals: Typ Values shown in Section 6

[Note]

These items are measured using CS1000(MINOLTA) for color coordinates, EZContrast(ELDIM) for viewing angle and CS1000 or BM-5A(TOPCON) for others under the dark room condition (no ambient light) after more than 30 minutes from turning on the lamp unless noted.

Condition: IL=6.0 mArms, FL=43 kHz

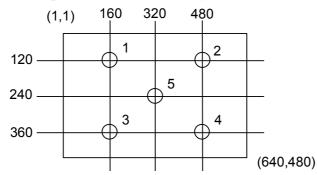
Measurement method for luminance and color coordinates is as follows.



The luminance is measured according to FLAT PANEL DISPLAY MEASUREMENTS STANDARD (VESA Standard).

*1) Measurement Point

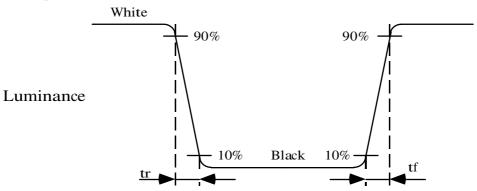
Contrast Ratio, Luminance, Response Time, Viewing Angle, Color Coordinates: Display Center Luminance Uniformity: point 1~5 shown in a figure below



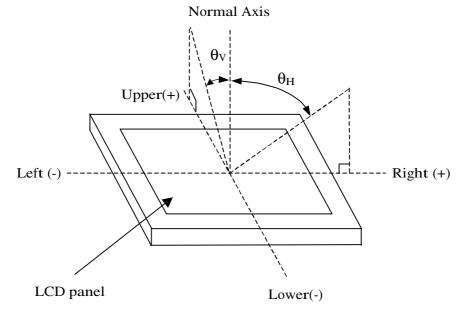
*2) Definition of Contrast Ratio

CR=Luminance with all white pixels / Luminance with all black pixels

- *3) Definition of Luminance Uniformity $\Delta Lw=[Lw(MAX)/Lw(MIN)-1] \times 100$
- *4) Definition of Response Time



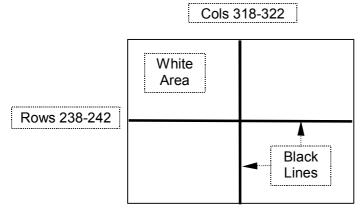
*5) Definition of Viewing Angle (θ_V , θ_H)



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*6) Image Sticking

Continuously display the test pattern shown in the figure below for two-hours. Then display a completely white screen. The previous image shall not persist more than two seconds at 25°C.



TEST PATTERN FOR IMAGE STICKING TEST

10. RELIABILITY TEST CONDITION

(1) Temperature and Humidity

ITEM	CONDITIONS
HIGH TEMPERATURE HIGH HUMIDITY OPERATION	40°C, 90%RH, 240 h (No condensation)
HIGH TEMPERATURE OPERATION	70°C, 240 h
LOW TEMPERATURE OPERATION	–20°C, 240 h
HIGH TEMPERATURE STORAGE	80°C, 240 h
LOW TEMPERATURE STORAGE	–20°C, 240 h
THERMAL SHOCK(NON-OPERATION)	BETWEEN –20°C (1h) and 80°C(1h) 100 CYCLES

(2) Shock & Vibration

ITEM	CONDITIONS
SHOCK (NON-OPERATION)	Shock level: 1470m/s ² (150G) Waveform: half sinusoidal wave, 2ms Number of shocks: one shock input in each direction of three mutually perpendicular axis for a total of six shock inputs
VIBRATION (NON-OPERATION)	Vibration level: 9.8m/s ² (1.0G) Waveform: sinusoidal Frequency range: 5 to 500Hz Frequency sweep rate: 0.5 octave /min Duration: one sweep from 5 to 500 Hz in each of three mutually perpendicular axis(total 3 hours)

(3) Judgment standard

The judgment of the above tests should be made as follow:

Pass: Normal display image, no damage of the display function. (ex. no line defect) Partial transformation of the module parts should be ignored.

Fail: No display image, damage of the display function. (ex. line defect)

11. INSPECTION STANDARDS

Inspection condition is as follows:

- Viewing distance: approximately 35 cm.
- Viewing angle: normal to the LCD panel $\pm 10^{\circ}$ horizontal and vertical.
- Ambient temperature: approximately 25°C.
- Ambient light: 300 500 lx.

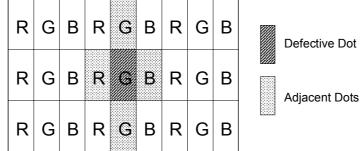
Bright Dot is defined as follows:

Visible through 5% transmission ND filter under the condition that black image (color 0) is on the display.

DE	FECT TYPE	LIMI	Т		
		$0.01 \text{ mm} < W \le 0.05 \text{ mm}$ L $\le 10 \text{ mm}$	N ≤ 4		
	SCRATCH	0.01 mm < W 10 mm < L	N = 0		
		0.05 mm < W	N = 0		
	DENT	$0.2 \text{ mm} < \phi \le 0.4 \text{ mm}$	$N \leq 4$		
VISUAL	DENI	0.4 mm < φ	N = 0		
DEFECT	BLACK SPOT	$0.2 \text{ mm} < \phi \le 0.4 \text{ mm}$	N ≤ 5		
	BUBBLE	0.4 mm < ∳	$\mathbf{N} = 0$		
		$L \le 3 mm$ $W \le 0.1 mm$	$N \leq 4$		
	LINT	3 mm < L W $\leq 0.1 \text{ mm}$	$\mathbf{N} = 0$		
		0.1 mm < W	ACCORDING TO BLACK SPOT		
	BRIGHT DOT	$N \leq 2$	5		
	DARK DOT	$N \leq 2$	5		
	TOTAL DOT	$N \leq S$	8		
ELECTRICAL	TWO ADJACENT DOT				
DEFECT	BRIGHT DOT	≤ 2 PAIRS			
_	DARK DOT	\leq 2 PAIRS			
THREE OR MORE ADJACENT DOT		NOT ALLOWED			
	LINE DEFECT	NOT ALL	OWED		

*1) W: width,L: length, \$\$\overline{0}\$: diameter, N: number

*2) DEFINITION OF ADJACENT



The defects that are not defined above and considered to be problem shall be reviewed and discussed by both parties.

12. OTHER FEATURE

This LCD module complies with RoHS. *)

*) RoHS: Restriction of the use of certain hazardous substances in electrical and electronic equipment

13. HANDLING PRECAUTIONS FOR TFT-LCD MODULE

Please pay attention to the followings in handling TFT-LCD products;

(1) ASSEMBLY PRECAUTION

- a. Please mount the LCD module by using mounting hole with a screw clamping torque (recommended value: 0.3 Nm). Please do not bend or wrench the LCD module in assembling. Please do not drop, bend or twist the LCD module in handling.
- b. Please design display housing in accordance with the following guide lines.
 - (a) Housing case must be designed carefully so as not to put stresses on LCD all sides and not to wrench module. The stresses may cause non-uniformity even if there is no non-uniformity statically.
 - (b) Keep sufficient clearance between LCD module back surface and housing when the LCD module is mounted. Approximately 1.0mm of the clearance in the design is recommended taking into account the tolerance of LCD module thickness and mounting structure height on the housing.
 - (c) When some parts, such as, FPC cable and ferrite plate, are installed underneath the LCD module, still sufficient clearance is required, such as 0.5mm. This clearance is, especially, to be reconsidered when the additional parts are implemented for EMI countermeasure.
 - (d) Design the inverter location and connector position carefully so as not to give stress to lamp cable, or not to interface the LCD module by the lamp cable.
 - (e) Keep sufficient clearance between LCD module and the others parts, such as inverter and speaker so as not to interface the LCD module. Approximately 1.0mm of the clearance in the design is recommended.
 - (f) To avoid local elevation/decrease of temperature, considering location of heating element, heat release, thermal design should be done.
- c. Please do not push or scratch LCD panel surface with anything hard. And do not soil LCD panel surface by touching with bare hands. (Polarizer film, surface of LCD panel is easy to be flawed.)
- d. Please do not press any parts on the rear side such as source TCP, gate TCP, control circuit board and FPCs during handling LCD module. If pressing rear part is unavoidable, handle the LCD module with care not to damage them.
- e. Please wipe off LCD panel surface with absorbent cotton or soft cloth in case of it being soiled.
- f. Please wipe off drops of adhesives like saliva and water on LCD panel surface immediately. They might damage to cause panel surface variation and color change.
- g. Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.
- h. Please do not touch metal frames with bare hands and soiled gloves. A color change of the metal

frames can happen during a long preservation of soiled LCD modules.

- i. Please handle metal frame carefully because edge of metal frame is very sharp.
- j. Please pay attention to handling lead wire of backlight so that it is not tugged in connecting with inverter.
- k. Please connect the metal frame of LCD module to GND in order to minimize the effect of external noise and EMI.
- 1. Be sure to connect the cables and the connecters correctly.

(2) OPERATING PRECAUTIONS

- a. Please be sure to turn off the power supply before connecting and disconnecting signal input cable.
- b. Please do not change variable resistance settings in LCD module. They are adjusted to the most suitable value. If they are changed, it might happen LCD does not satisfy the characteristics specification.
- c. LCD backlight takes longer time to become stable of radiation characteristics in low temperature than in room temperature.
- d. The interface signal speed is very high. Please pay attention to transmission line design and other high speed signal precautions to satisfy signal specification.
- e. A condensation might happen on the surface and inside of LCD module in case of sudden change of ambient temperature.
- f. Please pay attention not to display the same pattern for very long time. Image might stick on LCD. Even if image sticking happens, it may disappear as the operation time proceeds.
- g. Please obey the same safe instructions as ones being prepared for ordinary electronic products.

(3) PRECAUTIONS WITH ELECTROSTATICS

- a. This LCD module use CMOS-IC on circuit board and TFT-LCD panel, and so it is easy to be affected by electrostatics. Please be careful with electrostatics by the way of your body connecting to the ground and so on.
- b. Please remove protection film very slowly from the surface of LCD module to prevent from electrostatics occurrence.

(4) STORAGE PRECAUTIONS

- a. Please do not leave the LCDs in the environment of high humidity and high temperature such as 60°C90%RH.
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b. Please do not leave the LCDs in the environment of low temperature; below -20°C.

(5) SAFETY PRECAUTIONS

- a. When you waste damaged or unnecessary LCDs, it is recommended to crush LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- b. If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.
- c. Be sure to turn off the power supply when inserting or disconnecting the cable.
- d. Inverter should be designed carefully so as not to keep working in case of detecting over current or open circuit on the lamp.

(6) OTHERS

- a. A strong incident light into LCD panel might cause display characteristics changing inferior because of polarizer film, color filter, and other materials becoming inferior. Please do not expose LCD module direct sunlight and strong UV rays.
- b. Please pay attention to a panel side of LCD module not to contact with other materials in preserving it alone.
- c. For the packaging box, please pay attention to the followings;
 - (a) Packaging box and inner case for LCD are designed to protect the LCDs from the damage or scratching during transportation. Please do not open except picking LCDs up from the box.
 - (b) Please do not pile them up more than 5 boxes. (They are not designed so.) And please do not turn over.
 - (c) Please handle packaging box with care not to give them sudden shock and vibrations. And also please do not throw them up.
 - (d) Packaging box and inner case for LCDs are made of cardboard. So please pay attention not to get them wet. (Such like keeping them in high humidity or wet place can occur getting them wet.)

14. REVISION STATUS

Rev.	Description	Date	Prepared	Checked	
Kev.	Description	Date	Trepared	Approved	
First	First Revision	Oct.30,'07	H.Ito	T.Ikemoto	
First		001.30,07	11.100	M.Nishimura	

AA104 SERIES PACKAGING SPECIFICATION

MITSUBISHI ELECTRIC Corp.

Date: Sep.14,'07

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1. PACKAGING BOX

material:cardboard, polyethylene form (Anti-electrostatic spec.)construction:See Fig. 1max. packaging number:10 pcs.dimension: $457(W) \times 302(D) \times 422(H)$ [mm]mass(including 10 modules):7.0 kglabel:Labels are put on the box.(See Fig. 2, 3, 4)

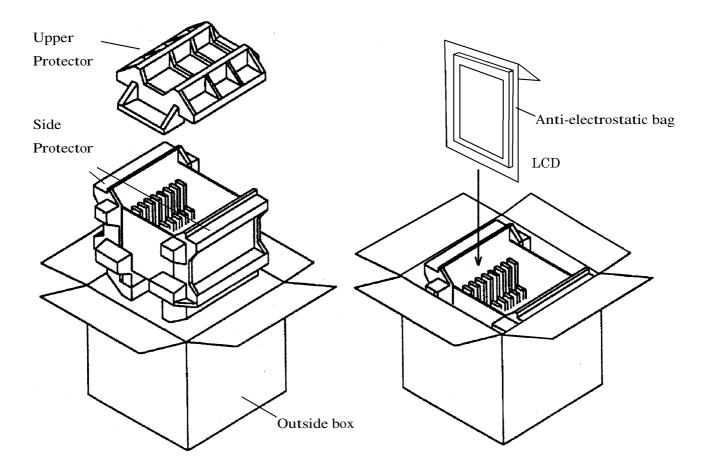


Fig 1. Illustration of packaging box structure

Product name	Packaging number
Bar-code	Bar-code
Serial No.	Serial No.
Bar-code	Bar-code
Serial No.	Serial No.
Bar-code	Bar-code
Serial No.	Serial No.
Bar-code	Bar-code
Serial No.	Serial No.
Bar-code	Bar-code
Serial No.	Serial No.
Bar-code	Bar-code

Fig 2. Label 1

Shipping No. Box No. MADE IN Bar-code Shipping date	Overseas sales office Product name
MADE IN Bar-code	Shipping No.
Bar-code	Box No.
	MADE IN
Shipping date	Bar-code
	Shipping date

Fig 4. Label 3

Box No.MassShipping No.Bar-codeShipping No.Bar-codePackaging No.Bar-codeShipping No.Bar-codeShipping No.Bar-codeProducts nameBar-code

Fig 3. Label 2

ABC ⊺FT - LCD: AA104∗∗∗∗
P / O NO. ********
C / S No. 049 OF 400 MADE IN JAPAN

Fig 5. Sample of Label 3

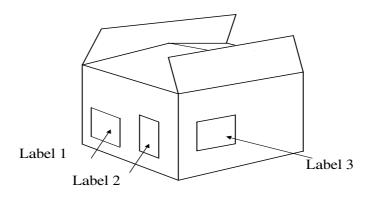


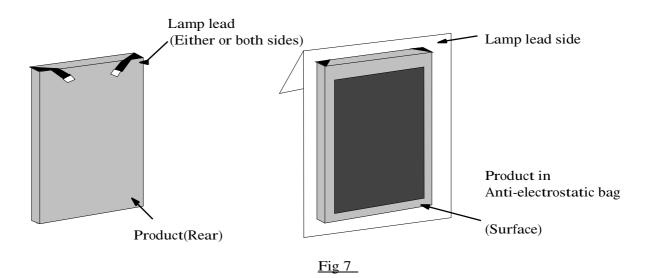
Fig 6. Location of labels

2. LOCATION OF LABEL ON THE PACKAGING BOX

Labels are put on the box.(See. Fig 6)

3. PACKAGING FORM OF PRODUCT

- (1) Each of LCD modules is packed in anti-electrostatic bag(Fig 7).
- (2) The packaging box accumulates maximum 10 modules.
- (3) Upper protector is put on the products, and shut the box.



4. CAUTIONS OF SHIPPING & STORAGE

- (1) Do not turn the packaging upside down while storage and transportation. The boxes should not be piled up more than 5.
- (2) Handle with care. Keep off from rain & dew.
- (3) Keep off from direct sunlight exposure. Please store under room temperature & low humidity in original packaging condition when they were shipped.
- (4) Keep other cautions described in handling manual.

5. REVISION STATUS

Rev.	Description	Date	Prepared	Checked
				Approved
В	First Revision	Sep.14,'07	H.Ito	T.Ikemoto
				M.Nishimura

Products Number Labeling Forms

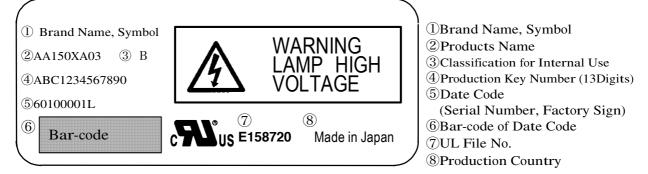
MITSUBISHI ELECTRIC Corp.

Date: Jan.25,'07

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(1/3)

Products number label is constructed as below;



- 1 Brand Name, Symbol MITSUBISHI ELECTRIC
- ② Products Name

ex.: AA150XA03

3 Classification for Internal Use

Blank or Alphabet or Number or Symbol etc.

ex.1 : A ex.2 : *

④ Production Key Number

(ID Number for Production Control)

(5) Date Code (Serial Number, Factory Sign)

1 2 3 4 5 6 7 8 9	
	Factory Sign
	Serial Number on Every Production Week
	(00001~99999)
	Production Week Number $(01 \sim 53)$
<u> </u>	Production Year Number
	(Last 1 Digit of AD Year Number)

• Date Code is constructed by 9 Digits as below;

1st Digit	: Production Year Number (Last 1 Figure of AD Year)
2nd~3rd Digit	: Production Week Number in a Year
	(A Year is divided to 53 weeks from Monday to Saturday)
4th∼8th Digit	Serial Number on Every Production Weeks.
	$(00001 \sim 999999)$
	These are numbered in order according to Production Name.
9th Digit	: Factory Sign (on the Module Test Process)
	(L: Shisui Factory Line, W: CPT Fab-1, R: CPT Fab-2,
	U: CPT Wujiang-LCM-1, V: CPT Wujiang-LCM-2)

6 Bar-code(Date Code)

Bar-code Line for computer reading Date Code mentioned as above.

- ⑦ UL File No.
 MDTI: E158720, CPT: E194548
- ⑧ Production CountryMDTI: Made in Japan, CPT: Made in Taiwan/China

LAMP UNIT for 10.4"VGA (AA104VC**)

TECHNICAL SPECIFICATION

AA-L5903326G01

MITSUBISHI ELECTRIC Corp.

Date: Oct.30,'07

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AA104VC01_06_H

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1. APPLICATION

This technical literature applies to the replaceable lamp unit that is the maintenance parts for 10.4"VGA TFT-LCD module industrial use (model name: AA104VC**).

2. MECHANICAL CHARACTERISTICS

Item	Specification	Remarks
Outline Dimension of Reflector	$228.5 \times 5.4 \times 4.6 \text{ (mm)}$	Except Wire and Lamp Rubber Cushion
Mass	12 (g)(MAX)	
Lamp Diameter	¢2.6 (mm)	

See 8. DRAWING OF OUTLINE DIMENTIONS (Page5)

3. ENVIRONMENTAL CONDITIONS

Item	Operation		Non Op	eration	Remarks
	MIN	MAX	MIN	MAX	
Ambient Temperature	-20°C	70°C	-20°C	80°C	No Condensation

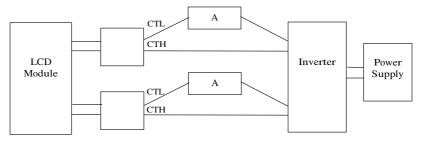
 $Ta \leq 40^{\circ}C:90\%$ RH max. without condensation

Ta > 40°C : Absolute humidity shall be less than the value of 90% RH at 40°C.

4. ELECTRICAL CHARACTERISTICS

Operation conditions							
Item	Symbol	Condition	MIN	TYP	MAX	Unit	Remarks
Lamp Current	IL	$Ta = 25^{\circ}C$	3.0	6.0	7.0	mArms	
Lamp Voltage	VL	$Ta = 25^{\circ}C$	-	480	-	Vrms	
	e VS	$Ta = 25^{\circ}C$	900	-	-	Vrms	
Starting Lamp Voltage		$Ta = 0^{\circ}C$	1170	-	-	Vrms	
		$Ta = -20^{\circ}C$	1300	-	-	Vrms	
I	FL	$Ta = 25^{\circ}C,$	40	-	80	kHz	
Lamp Frequency		IL = 6.0 mArms					

*) Lamp Current measurement method (The current meter is inserted in low voltage line.)



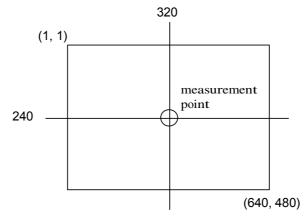
5. OPTICAL CHARACTERISTICS

							Ta=25°C, 60±10%RH
I	Item		MIN	TYP	MAX	Unit	Remarks
Luminance	VC01,01A,02	Lw	340	430	-	cd/m ²	
Lummance	VC03	Lw	280	380	-	cd/m ²	measurement point
Calar	dinates	Wx	0.300	0.330	0.360	-	shown in the figure
Color		Wy	0.312	0.342	0.372	-	below
(White)		Wx	0.303	0.333	0.363	-	001010
(winte)	VC03	Wy	0.319	0.349	0.379	-	

[Conditions]

IL = 6.0mArms, Inverter frequency: 43 kHz

[Measurement Point]



These items are measured when lamp unit are assembled into AA104VC**, and using CS1000(MINOLTA) for color coordinates, and CS1000 or BM-5A(TOPCON) for Luminance under the dark room condition (no ambient light) after more than 30 minutes from turning on the lamp unless noted.

6. LIFE TIME OF THE LAMP UNIT

Environmental Conditions are as follows: Ambient temperature is 25±5°C. Lamp Current is 6.0 mArms.

Continuous Operation	50,000 hours
Number of turning on and off	100,000 times (30sec ON-OFF)

- (1) Lamp life time is defined as the time either when the brightness becomes 50% of the initial value, or when the starting lamp voltage does not meet the value specified in the table of section 4.
- (2) The life time of the backlight depends on the ambient temperature. The life time will decrease under low/high temperature.

7. INTERFACE PIN CONNECTION

Backlight-side connector: BHR-02(8.0)VS-1N (JST) Inverter-side connector: SM02(8.0)B-BHS(LF)(SN) (JST)

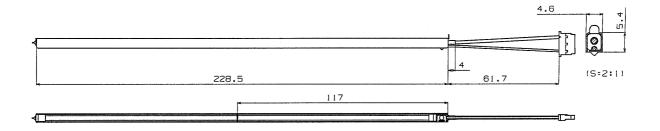
Pin No).	Symbol	Function		
1		СТН	VBLH (High voltage)		
2		CTL	VBLL (Low voltage)		

[Note]

VBLH - VBLL = VL

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8. DRAWING OF OUTLINE DIMENTIONS



9. METHOD OF REPLACING THE LAMP UNIT

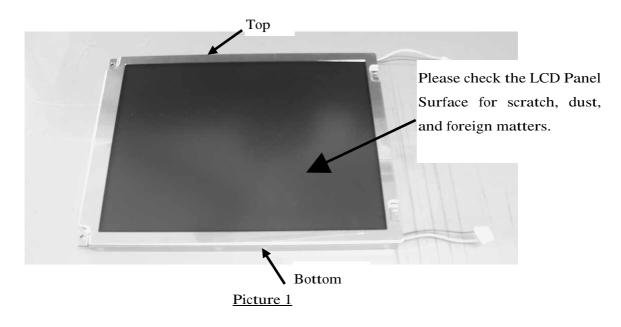
(1) <u>Precautions</u>

Please pay attention to the following items while replacing the Lamp Unit.

- a. Please do not damage the LCD Panel Surface, and do not touch it with bare hands. (Wearing gloves is recommended.)
- b. Please be careful with electrostatics, and work in clean environment to prevent entering dust and/or foreign matters that will cause bad display image.(Using clean bench or similar environment is recommended.)
- c. Please be careful of the edge of the frame metal.
- d. Please replace top and bottom lamp units at the same time. Replace only one lamp unit will cause inferior display image.

(2) Method of replacing the Lamp Unit

1) Put the TFT-LCD Module on the table.(LCD Panel Surface is upside.)

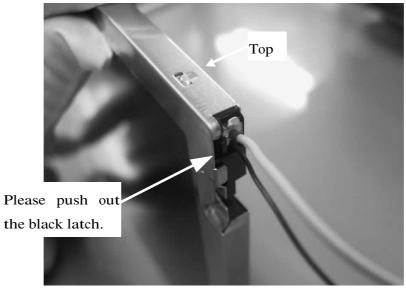


2) Turn the TFT-LCD Module upside down.



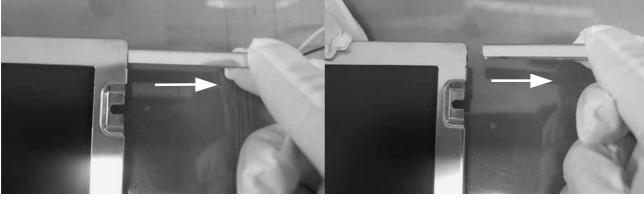
Picture 2

3) Stand the TFT-LCD Module up and push out the black latch that fastens the Lamp Unit.



Picture 3

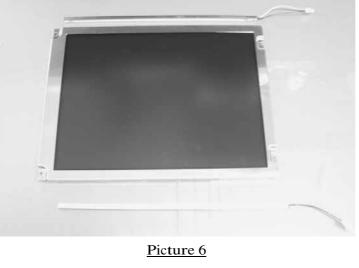
4) Push out the latch and pull all cables together slowly to remove the Lamp Unit.

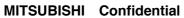




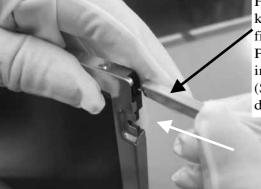
Picture 5

- 5) Remove the other Lamp Unit at the opposite(bottom) side of LCD Module in the same way. See. 3) and 4)
- 6) Picture 6 shows the TFT-LCD Module after removing the Lamp Units.



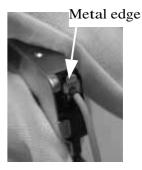


- 7) Prepare to insert the new Lamp Unit.
 - Open the package and take the new Lamp Unit out.
 - Check the new Lamp Unit for dust and foreign matters.
- 8) Stand the TFT-LCD Module up and insert the new Lamp Unit.



Fold down the black film to keep the end of the black film out.

Push the black latch out, and insert the new Lamp Unit. (Set the Lamp opening face down.)

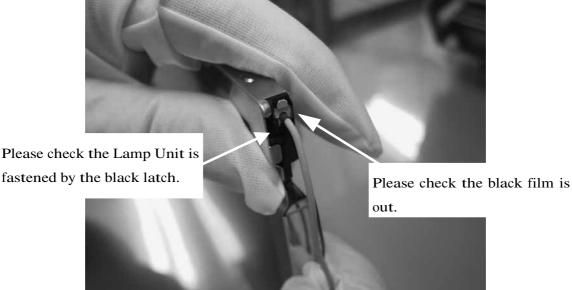


Picture 7

Please do not insert it in the wrong position.

When inserting the lamp unit, please slide in the lamp unit until the latch locks on to the unit. When handling, please push the metal edge and avoid applying stress to the cable.

9) After inserting, please check the Lamp Unit is fastened by the black latch.



Picture 8

10) Insert the other Lamp Unit to the opposite(bottom) side of the LCD module in the same way.

See. 8) to 9)

- 11) After replacing the Lamp Unit, please check the following items.
 - Appearance of TFT-LCD Module is not changed after replacing Lamp Unit. (See. <u>Picture 1</u> and <u>Picture 2</u>)
 - There is no damage, dust, or foreign matters on the LCD Panel Surface.
 - Install the TFT-LCD Module then check turning on the lamps.

10. REVISION STATUS

Rev.	Description	Rev. Date	Prepared	Checked
Kev. Description		Kev. Date	Trepared	Approved
Н	First Revision	Oct.30,'07	H.Ito	T.Ikemoto
	Thist Revision			M.Nishimura