				First Edition Feb 4, 2004
	LCD Modu	lle Techi	nical Specification	Final Revision
Type No	T-51440	GL070H-	FW-AF	
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Tab	le of Content	S	Prepared by (Module Co	ordination Group)
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1. Application

This specification applies to 7.0"color TFT-LCD module(T-51440GL070H-FW-AF). The applications of the panel are for automotive entertainment and car navigation.

2. General Specifications

T-51440GL070H-FW-AF (AF) No. 2004-0023

Screen Size	: 7 inches (18cm) diagonal
Display Mode	: Normally White
Driving Method	: a-Si TFT active matrix format Line Scan/non-interlace Reverse Horizontal Line
Composition	: TFT Cell, Driver IC, Timing controller IC, Backlight unit, Inverter DC/DC Converter, and Video circuit
Input Power Supply	: +8V-16V(DC)
Input Signal	: Composite Video Signal (NTSC/PAL) or Specific Analog RGB signal (NTSC/PAL) + Composite or Separate Synchronized signal
Output Signal	: Horizontal/Vertical Synchronized signal (negative)
Resolution	: 480(W) x 234(H)
Dot Resolution	: 1440(W) x 234(H)
Dot Pitch	: 0.3210(W) x 0.3720(H) mm
Pixel Configuration	: RGB Stripe
Active Area	: 154.08(W) x 87.048(H) mm
Backlight	: Triple wavelength L-shaped Cold Cathode Fluorescent Lamp, Dimming Ratio 1-100%
Viewing Direction	: 6 O'clock (Maximum Contrast)
Surface Treatment	: AGLR Coating (Low Reflectance)
Outer Dimension	: 164.9(W) x 101.9(H) x 23.1(D) mm
Weight	: 300g max.
Attached Drawing	: Dimensional Outline UE-210777

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3. Operating Conditions

Item		Conditions	Temperature Range	Remark
Operating Temperature Range	LCD Module w/Backlight	Ambient Temperature (Panel Surface)	–30 ~ 75°C (–30 ~ 85°C)	Note3-1,3-2
	LCD Panel	Surface Temperature	–30 ~ 85°C	
Storage Temperature Range	LCD Module w/Backlight	Surface Temperature	–40 ~ 85°C	Note3-3
remperature Mange	LCD Panel	Surface Temperature	–40 ~ 85°C	

Note2-1: Operating temperature range defines the operation only and the contrast, response time and other display optical characteristics are set at Ta=+25°C.

Note2-2: Panel surface temperature indicates the temperature of the backlight panel surface on the five points from the four corners and the center. Also note that the panel temperature of backlight side is 10°C (reference value) higher than the other side.

Note2-3: Backlight is not activated.

4. Electrical Specifications

	Item	Symbol	Conditions	Min.	Max.	Unit
Power	Video Circuit	V _{PW}	Ta=25±5°C	Vss-0.2	16.0	V
Supply	Backlight	V _{BL}	V _{SS} =0V	Vss-0.2	16.0	V
	Composite Video	VIDEO Ta=25±5°C		-	1.5	V_{P-P}
	Analog RGB	R,G,B	75Ω V _{PW} =V _{BI} =+12.0V	-	1.5	V_{P-P}
	Chroma Signal	CYSYNC	V PW- V BL- 1 1 2:0 V	-	1.5	V_{P-P}
Input	Synchronous Signals	VSY,HSY		-	3.6	V_{P-P}
Signal	Adjustment Signals COLOF BRT,CON TINT,DI		Ta=25±5°C Vss=0V	Vss-0.3	V _{DD} +0.3	V
	Switching Signals	U/D,R/L, MOD1~3, N/P,R/CMP, YC/CMP	V _{PW} =V _{BL} =+12.0V	Vss–0.3	-	V

4.1.Absolute Maximum Ratings

Note: Absolute maximum ratings should not exceed the limit anytime. If the product exceeds the limit, it may cause damage.Please be cautious to the changes in supply voltage, connection parts, surge of signals and ambient temperature.

4.2.Recommended Operating Conditions

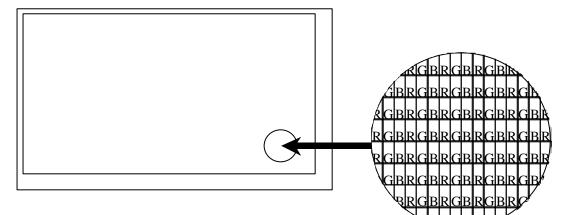
						٦)	Га=2	5±5°C,	Vss=0V
	Item	Symbol	C	ondition	Min.	Тур.	Ν	/lax.	Unit
Power	System	V _{PW}			8.0	12.0		6.0	V
Supply	Backlight	V _{BL}			8.0	12.0	`	6.0	V
	Composite Video	VIDEO				1.0			Vp-p
	Analog RGB	R,G,B		75Ω		0.7			Vp-p
	Chroma Signal	YCSYNC				1.0			Vp-p
	Synchronous Signals	HSY,VSY				0.7		3.3	Vр-р
	Brightness	BRT				2.4			V
Input Signals	Tint	TINT			1.0	2.7			V
Signals	Color	COLOR				2.0			V
	Contrast	CONT				2.2			V
	Backlight Dimmer	DIM			1			100	%
	Soloot Signala	MOD1~3,N/P U/D,R/CMP		H level		OPEN			V
	Select Signals	R/L,YC/CMP		L level	0			0.7	v
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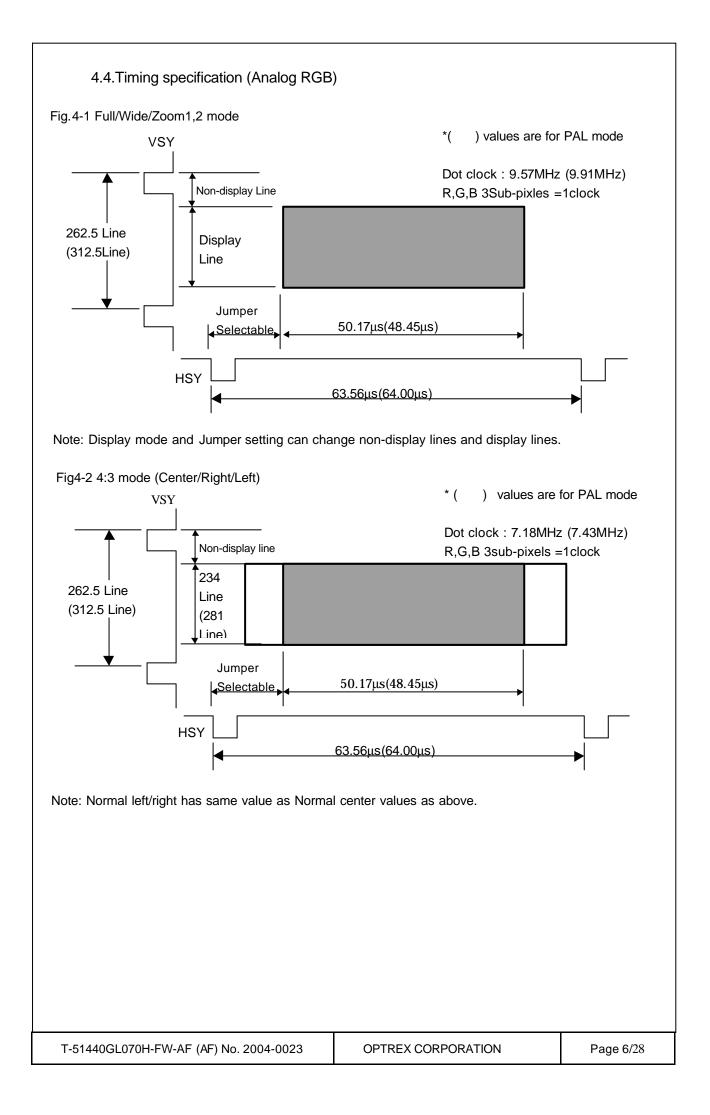
	f _{VDN}	NITEO	57.14	59.939	62.86	Hz
Supehronous Fraguenau	f _{HDN}	f _{HDN} NTSC		15.734	16.50	KHz
Synchronous Frequency	f _{VDP}	DAL	48.64	50.00	51.20	Hz
	f _{HDP}	PAL	15.20	15.625	16.00	KHz
David Organization	P_{PW}	V _{PW} =V _{BL} =12V		2400		
Power Consumption	P _{BL}	DIM=3.3V _{DC}		4200		mW

Note: Recommended Operating Conditions defines the guaranteed range of operation and it is out of guarantee if the product exceeds the range even if within the range of Item3.Absolute Maximum Ratings.

Please use within the range of Recommended Operating Conditions.

4.3.Pixel Alignment





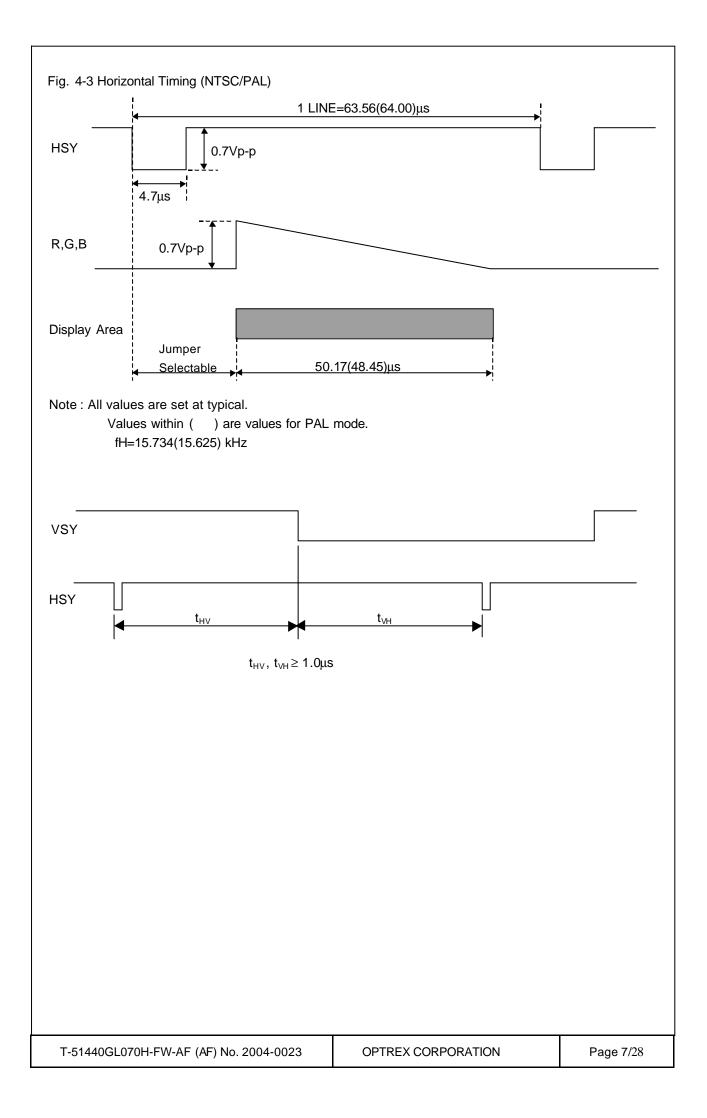


Fig.4-4 Vertical Timing (NTSC: Full/4:3mode)		
Line No. 524 525 1 2 3 4 5 6 7 8 9	10 11 12 13 14 15 16 25 26	27 28 29 30
VSY		
0.7/p-p R,G,B	0.7Vp-p	
Display Area		
	Jumper Selectable (17 ~ 24H)	
	74 275 276 277 271 272 273 282 283	284 285 286 287
VSY		
R.G.B	AA	
Display Area Jumper Selectable	Jumper Selectable	
	(274 ~ 281H)	
Note: Wide/Zoom mode has different starting loca	ation	
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4.5.Video Board Adjustment

4.5.1. Jumper Switch (J1,2)

Jumper Switch Number	Contents	Remark
J1	Horizontal Display Starting Position Adjust(From HSY)J1 4 3 2 1Wait TimeOFF OFF OFF OFF11.9875uS(13.1875uSOFF OFF OFF ON11.6750uS(12.8750uSOFF OFF ON11.3625uS(12.5625uSOFF OFF ON11.3625uS(12.5625uSOFF ON0FFOFF ON0FFOFF ON0FFOFF ON0FFOFF ON0FFOFF ON0FFOFF ON0FFOFF ON0NOFF ON0NOFF ON0NOFF ON0NOFF ON0NOFF ON0NOFF ON0NOFF ON0NON0FF OFF9.8000uS(11.0000uSON0NOFF ON0N9.8000uS(11.0000uSON0NOFF ON0N9.8000uS(11.0000uSON0NOFF ON9.1750uS(10.3750uSON0NON0NON0NOFF ON0N8.8625uS(10.0625uSON0N <t< td=""><td>Default Set J1-1 : OFF J1-2 : ON J1-3 : OFF J1-4 : OFF J1-5 : ON J1-6 : OFF J1-7 : ON J1-8 : ON</td></t<>	Default Set J1-1 : OFF J1-2 : ON J1-3 : OFF J1-4 : OFF J1-5 : ON J1-6 : OFF J1-7 : ON J1-8 : ON
J2	J2-1 : ON(Default) J2-2 : ON(Default) J2-3 : UOS(Under On Screen) Display Select ON : Active OFF : Non Active	Default Set J2-1 : ON J2-2 : ON J2-3 : OFF

4.5.2. Potentiometers for Display Image Adjustment

Symbol	Contents	Remark
VR301	TINT : Tint Adjustment	For PAL mode, tint tied to 0V
VR302	COLOR : Color Adjustment	
VR305	CONT : Contrast Adjustment	
VR312	BRT : Brightness Adjustment	

4.6.Backlight

Parameter	Symbol	Min.	Тур.	Max.	Units	Remark
Life Time	-	20000	-	-	hours	Note 1,2

Note 1: Reference derived from the use of lamp.

Note 2: When the luminance of lamp is 50% of initial luminance (Under setting of

Ta=25°C) and continuous standard lighting.

5. Optical Specifications

ltere		Currente e l	С	onditio	ns	Sta	undard Va	lue	1.1.0.14	Method of	
ltem		Symbol	θ	φ	С	Min.	Тур.	Max.	Unit	Measure	Remark
(1)Brightness		В	0°	0°		-	(400)	-	Cd/m ²		Note5-1
(2)Contrast		CR		imum wing		60	150	-	-	(Fig.5-1)	
		Х	0 °	0 °		0.310	0.350	0.390	-	(
(3)White Chroma	(3)White Chromaticity		0°	0 °		0.325	0.365	0.405	-		
(4)Brightness Uni	formity	-	0°	0 °		0.7	-	-	-	(Fig.5-2)	
(5)Vertical	Up	θ_{U}	-	0°	≥10	-	(30)	-	Degree		
Viewing Angle	Down	θ_{D}	-	0 °	≥10	-	(60)	-	Degree		
(6)Horizontal	Left	¢_	0 °	-	≥10	-	(60)	-	Degree	(Fig.5-3)	
Viewing Angle	Right	¢ _R	0 °	-	≥10	-	(60)	-	Degree		
(7)Response	Rise	τr	0°	0°		-	10	20	ms		
Time Decay		τd	0°	0 °		-	20	40	ms	(Fig.5-4)	

Note5-1: Under the condition of tube current 6.0mA

Conditions for Measuring

Environment: Dark room with no light or close to no light.

Temperature: 25±5°C

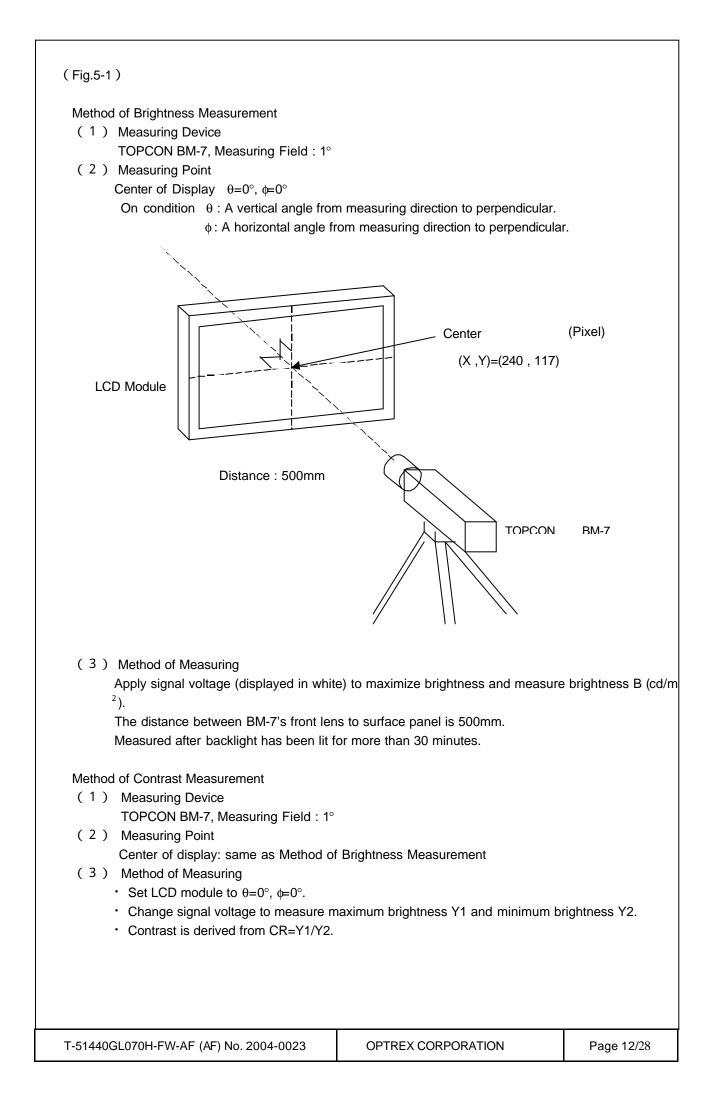
Humidity: 40~70%RH

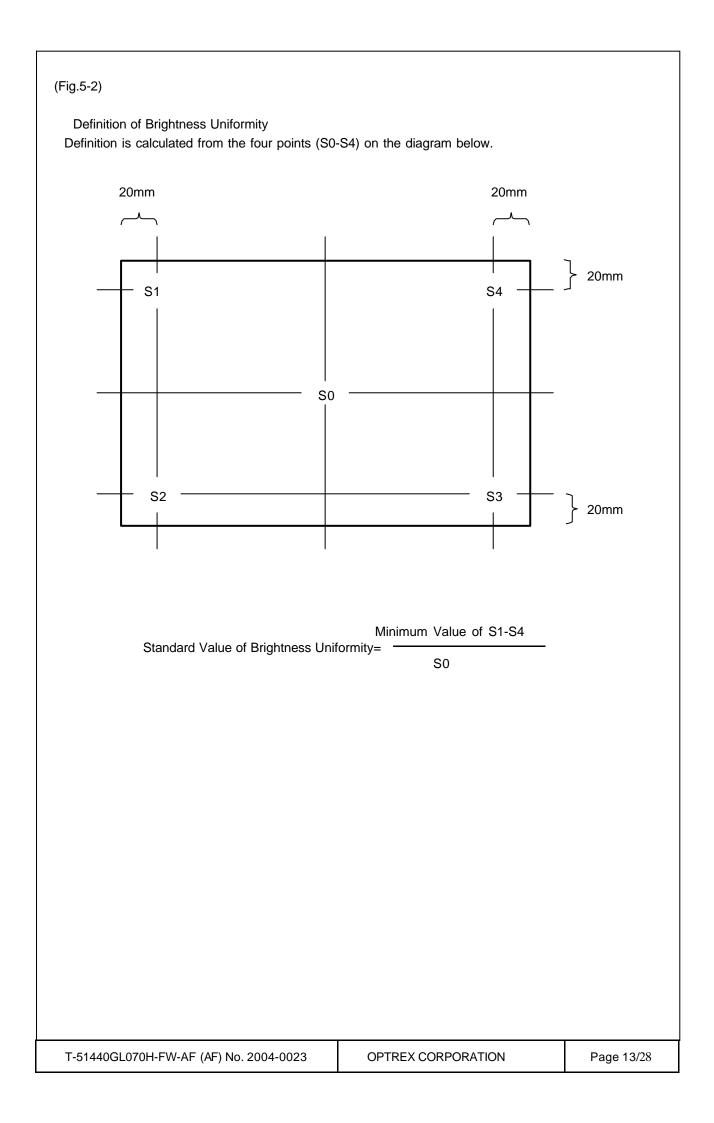
After backlight has been lit more then 30 minutes, driving voltage is set for optimal contrast to measure center of display.

Measure by the specified inverter or similar product.

Optimal viewing angle (The angle with best contrast)

	6 O'clock	
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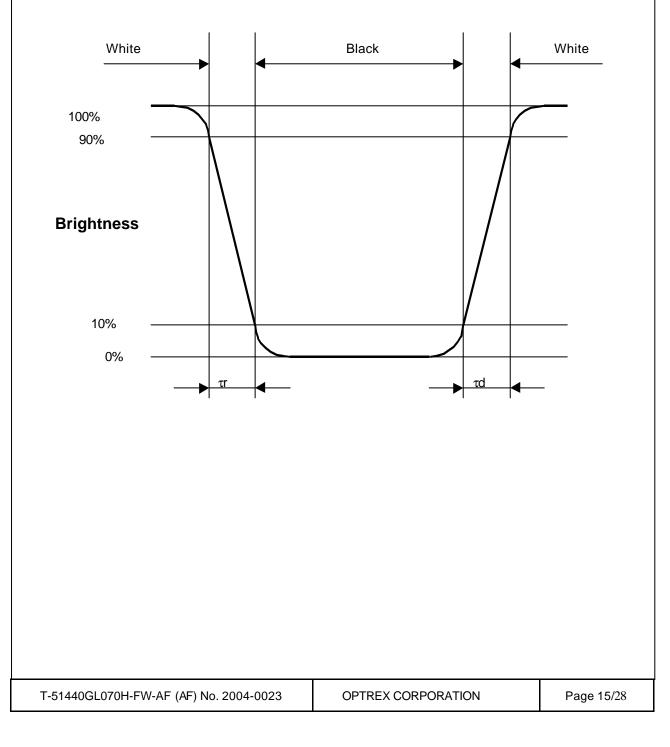


(Fig.5-3)			
Method of Viewing Angle Measurement (1) Measuring Device TOPCON BM-7, Measuring Field	d : 1°		
(2) Measuring PointCenter of display : Same as Met	hod of	Brightness Measurement	
 (3) Angle of Measuring θ: An angle vertical to perpendice φ: An angle horizontal to perpendice 		-	
	Ten	nperature Chamber	
TOPCON BM-7		Rotation Table (θ, φ)	
		Unit &	
(4) Method of Measuring Set rotation table to $\phi=0^{\circ}$ and set BM-7 to o direction of horizontal viewing angle ϕ . Also to measure angle± θ for up and down direction	set ro	tation table to $\phi=90^{\circ}$ and set BM-7 to	-
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(Fig.5-4)

Measuring Response Time

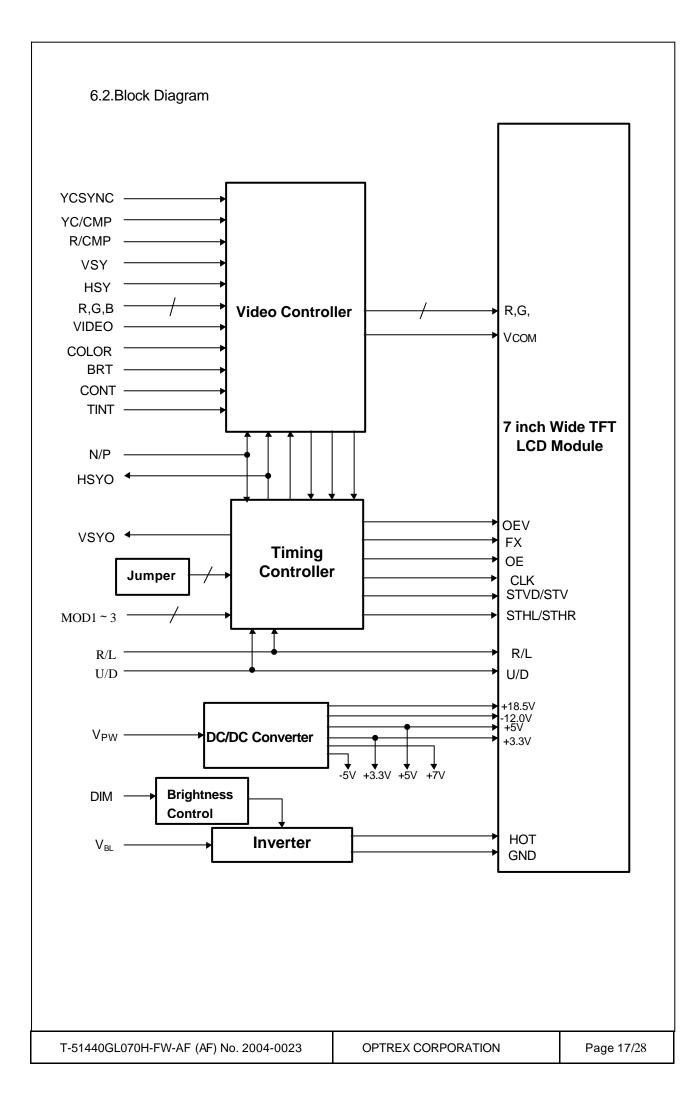
- (1) Measuring Device TOPCON BM-7, Measuring Field : 1°
 - Tektronix Digital Oscilloscope
- (2) Measuring Point
 - Center of display, same as Method of Brightness Measurement
- (3) Method of Measuring
 - Set LCD panel to θ =0°,and ϕ =0°.
 - Input white \rightarrow black \rightarrow white to display by switching signal voltage.
 - If the luminance is 0% and 100% immediately before the change of signal voltage, then τr is optical response time during the change from 90% to 10% immediately after rise of signal voltage, and τd is optical response time during the change from 10% to 90% immediately after decay of signal voltage.



6. I/O Terminal

6.1.Pin Assignment

No.	Symbol	Level	Function	I/O	Remark
1	V_{DD}	-	Power Supply (+5V)	Output	
2	COLOR	-	Color Adjustment (0~5V)	Input	
3	BRT	-	Brightness Adjustment (0~5V)	Input	
4	CONT	-	Contrast Adjustment (0~5V)	Input	
5	VIDEO	-	Composite Video Signal Input $(1.0V_{P-P},75\Omega)$	Input	
6	Vss	-	Signal Ground	-	
7	Vss	-	Backlight Ground	-	
8	Vss	-	Backlight Ground	-	
9	V _{BL}	-	Power Supply for Backlight (+8~16V)	Input	
10	V_{BL}	-	Power Supply for Backlight (+8~16V)	Input	
11	U/D	H/L	Up/Down Scanning Direction (Open:Down to Up, GND:Up to Down)	Input	
12	R/L	H/L	Left/Right Scanning Direction (Open: Left to Right, GND: Right to Left)	Input	
13	DIM	-	Backlight Dimming (1~100% Adjustable)	Input	
14	MOD1	H/L	Display Mode 1	Input	
15	MOD2	H/L	Display Mode 2	Input	
16	MOD3	H/L	Display Mode 3	Input	
17	N/P	H/L	NTSC/PAL Select (Open : NTSC, GND : PAL)	Input	
18	TINT	-	Tint Adjustment (1.6~5V)	Input	
19	R/CMP	H/L	RGB/Composite Select (Open: RGB, GND:Composite)	Input	
20	YC/CMP	H/L	Synchronous Signal Select (Open: YC Separate, GND: Composite)	Input	
21	YCSYNC	-	Chroma Signal (0.7V _{P-P} ,75Ω)	Input	
22	VSY	-	Vertical Sync. (0.7V _{P-P} , Active Low)	Input	
23	HSY	-	Horizontal Sync. (0.7V _{P-P} , Active low)	Input	
24	Vss	-	Signal Ground		
25	G	-	Green Color Video Signal ($0.7V_{P-P},75\Omega$)	Input	
26	В	-	Blue Color Video Signal (0.7V _{P-P} ,75Ω)	Input	
27	R	-	Red Color Video Signal (0.7V _{P-P} ,75Ω)	Input	
28	V _{PW}	-	System Power Supply (+8~16V)	Input	
29	VSYO	-	Vertical Sync.Output (3.3V,Active Low)	Output	:
30	HSYO	-	Horizontal Sync.Output (3.3V,Active Low) Output		
Mating C	onnector : SH	IDR-30V-9			
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6.3.Signal Definition

Pin No.	Symbol	Function		I/O Intermal Equiv	valent Circuit
1	V _{DD}	5V output terminal Please use this for each adjust terminal (2 ~ 4, 18 pin)	table	Output Current(Idd) =	Less than 10mA
2	COLOR	Color adjustment input terminal. Can be selected to change anywhere between 0 ~ 5V.			
3	BRT		Brightness Adjustment for RGB signal. Can be selected to change anywhere between 0 ~ 5V.		
4	CONT	Contrast adjustment terminal. Can be selected to change anywhere between 0 ~ 5V.			
5	VIDEO	Composite video signal input terminal. Please use standard input level 1 Vp-p of composite video signal. When using composite video input signal fix YCSYNC(21pin) to GND.			
6	Vss	Signal Ground terminal. Connect to GND.			
7	Vss	Backlight grounding terminal. Connect to GND.			
8	Vss	Backlight grounding terminal. Connect to GND.			
9	V_{BL}	Power supply input terminal for Use standard 12V.	r backlight.		
10	V_{BL}	Power supply input terminal for Use standard 12V.	r backlight.		
11	U/D	Up/Down scanning direction select terminal. When open, it will scan down to up. When connected to GND, it will scan up to down.			<u> </u>
12	R/L	Left/Right scanning direction select terminal. When open, it will scan left to right. When connected to GND, it will scan right to left.			
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Pin No.	Symbol	Function		I/O Intermal Equi	va lent Circuit
13	DIM	Backlight Dimming Terminal. Input 3.3V square wave PWM for	ormat.		
14	MOD 1	Display mode select input termi	inal.	+3.3V	
15	MOD 2	See Table 6.1		\$ 23k	
16	MOD 3				\succ
17	N/P	NTSC/PAL select terminal.		+3.3V	
		Open : NTSC			
		GND : PAL			
18	TINT	Tint adjustment input terminal.		•	+5V
		Can be selected between 1~5V			
		For PAL mode, the TINT pin tied		╽╺╱┉╌ᢂ᠇ᢩᠮ᠊᠁ᠯ	
					I I I I I I I I I I I I I I I I I I I
				7	
19	R/CMP	Video signal input select termin	al.		• •••
		Open: Analog RGB		l ⊾≰ 本 ↓	4.7k
		GND: Composite Video			- <u></u>
				3	
20	YC/CMP	Synchronous signal input selec	t terminal.	v	
	,	Open: YC seperate input			
		GND: Composite			
		-			-
21	YCSYNC	Chroma input terminal.			
		Use Chroma signal 0.7Vp-p.			88
		When using composite video, c	onnect to	# 5V	
		GND.			
22	VSY	Vertical synchronous signal inp	ut terminal.	22 2 24	¥
23	HSY	Horizontal synchronous signal i	nput terminal.		
				0. haF + 2 2 + 10	ר אין
24	Vss	Signal Ground terminal.			GND
		Connect to GND.			
25	G	Analog RGB signal input termin			<u> </u>
26	В	Use Analog RGB signal standar	d level	<u>+</u> 8	ବ୍ 🖞
27	R	0.7Vp-p.			╞╌┎┍
					ו אד
					GND
28	PWR	System power supply input term	ninal.		
		Use standard 12V.			
29	VSYO	Vertical synchronous signal out	put terminal.		
		Please use for UOS(under on s			
		the screen position.		100 1	
30	HSYO	Horizontal synchronous signal c	output terminal		
50	1010	Please use for UOS(under on se	•		
			or congrito aujust		
		the screen position.			
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	Table 6 Display Image	.1 : Displa MOD1	MOD2	MOD3		
Mode	(4:3 Signal Input)	(14pin)	(15pin)	(16pin)	Re	mark
Full		GND	GND	GND	Input Signal is on screen.	displayed fully
Normal Center		OPEN	GND	GND	4:3 Image disp of display.	ayed in center
Wide		GND	OPEN	GND	4:3 Signal has sideways from	been extended center of display.
Zoom 1		OPEN	OPEN	GND	Display is fixed zoomed.	on top and then
Zoom 2		GND	GND	OPEN	The time for ga from Zoom1 mo	
Normal Left		OPEN	GND	OPEN	4:3 image shift	ed to left.
Normal Right		GND	OPEN	OPEN	4:3 image shift	ed to right.
Unfixed		OPEN	OPEN	OPEN	Invalid mode.	
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<u>7. Test</u>

7.1. Mechanical and Environmental Test

	Item	Condition	Test hour	Remark
	High temp operation	Ta=65±2°C, 45%RH Below	192 hrs	Note1
Ē	High temp & high humidity operation	Ta=65±2°C, 90±2%RH, No Condensation	192 hrs	Note1
Endurance	Low temp operation	Ta=-30±3°C	192 hrs	Note1
nce	High temp storage	Ta=85±2°C, 45%RH below	192 hrs	Note1
	Low temp storage	Ta=-40±3°C	192 hrs	Note1
	Light resistance	Sunshine carbon arc Ta=63±2°C	360 hrs	
	Drastic temp change	-30°C(60min)⇔25°C(15min)⇔80°C(60min), No electric path	20 cycle	Note2
–	Condensation	-30°C(30min non-operating), 25°C/95%RH(10min operating) Dried after specified cycle and confirm operation	10cycle	
Heat	Humidity cycle	1cycle=48hrs non-operational 65°C 25°C 2.5 3 2.5 2.5 3 2.5 0°C 90~95%RH	5cycle	
Elec	Static resistance	C=200pF, R=0 Ω , V= \pm 150V 3 discharge on electric and other terminal, non-operational	-	
Electrical	Electric discharge	C=150pF, R=150 Ω , V=±15kV Discharge ±chrages 5 times each on panel and earth, non-Operational	-	
M	Vibration	5~10Hz, width 25mm 10~30Hz, 3.7×9.8m/s ² 30~50Hz, 1.6×9.8m/s ² 50~80Hz, 0.7×9.8m/s ² 80~100Hz, 0.3×9.8m/s ²	All dir 1 hr	
lechanical	Shock	980m/s ² , t=6ms, X,Y,Z all dir,semi-sine-wave, non- operational	-	
à	External durability	Apply pressure on the center of the screen by push / pull gauge. head diameter isop12mm, pressured apply 5×9.8N(=5kgf) once, non-opreatioal	-	
	Pressure resistance	5×10 ⁴ Pa(=0.5Pressure), non-operational	2 hrs	

Note 1: High temperature operation, high temperature & high humidity operation, low temperature operation, high temperature storage and low temperature storage will test it's ability for 1000 hours to confirm. The deteriortion of plarizer is disregarded.

Note 2: Drastic tempertaure change test will continue the inspection under same condition consecutiely to confirm it's ability. Test will be done mounted in your request kit.

7.2. Reliability Test Standard

		0	ptical & Electr	ristics			
	ltem	Contrast	Surface Iuminance	Response time	Circuit	Quality of Screen	
	High temp operation*	>30	within±20%	Within±20%	Within+40%	Not to be conspicious	
Enc	High temp & high humidity operation*	>25	"	"	"	"	
Endurance	Low temp operation*	>30	"	"	"	"	
lce	High temp storage*	"	"	"	"	"	
	Low temp storage*	"	"	"	"	"	
	Light resistance*	"	"	"	"	"	
	Drastic temp change*	"	"	"	"	"	
Heat	Condensation**	>25	"	"	"	"	
	Temp.& Humidity cycle**	"	"	"	"	"	
Elec	Static resistance	No abnormalities in system and display.					
Electrical	Electric discharge	No damage should be done					
~	Vibration	No abnormalities in system and display.					
Mechanical	Shock	No abnormalities in system and display.					
anic	Exterior durability	No abno	No abnormalities in system and display.				
a	Pressure durability	No abno	rmalities in sy	stem and disp	lay.		

Note : *indicates that test was performed in room teperature, more then 2 hours after it was taken out from chamber.

**indicates that test was performed after 24 hours after it has been taken out from chamber. Luminance, circuit, response time changes are compared from the initial standard values.

8. Appearance Standard

8.1.Mechanical Testing

8.1.1. External appearance

Inspection Area	Item	Criteria	Remark
	Surface Linear Scratches	Thickness Disregard under 0.05mm Between 0.05mm-0.15mm, total length must be within 50mm Greater then 0.15mm is not acceptable	
TFT Cell Section	Surface Sport Scratches	Disregard under 1 sub-pixel Penalized for 1-3 sub-pixels Shall not exceed 3 sub-pixels	Note1 Note2
	Back Scratches	Observe from surface and judge based on criteria of surface	
	Chipped Dirt Discoloration	It must not influence surface Must be removable No irregular discoloration on screen	-
	Linear Scratches	Thickness Disregard under 3.0mm Greater then 3.0mm must have less then 90mm total	
Metal Shield case (Applies to all	Spot Scratches	Diameter Less then 3.0mmø,only 3 is allowed Greater then 3.0mmø is not accepted	Note2
surface)	Dirt	Must be removable	
	Deformation Fingerprint	Not allowed Remove as much as possible	-
Input Output Section	Crack Distortion	No crack or disconnection No noticeable distortion	
(FPC w/B/L Cable)	FPC tape	The FPC should not be coming off for more then 10mm	Note3

Note1: Cell section's area subject to quality display area. Quality display area is specified in the external appearance drawing.

Note2: If there are any other problems please follow "Precautions under operation".

Note3: This is provisional standard and applies to limited sample (Optrex standards)

8.1.2. Dimensional Outline

All standards follow the measurement designated by the dimensional outline drawing.

8.2.Quality of Display

8.2.1. Conditions for Common Inspection

Unless specified, the conditions below will be applied.

Ta= $25\pm5^{\circ}$ C,Humidity=65%,V_{PW}=V_{BL}=+12.0V,V_{SS}=0V,Backlight inverter=Our standard inverter or equivalent, measured after backlight bas been lit for more then 30 minutes.

8.2.2. Quality Display Standard and Criteria for Judgement

(A) Quality Dis	play Standard

	Item	Description	Criteria
	Line defect	Black, white and colored line	Not accepted
Quality Display	Spot defect	Lighting irregularities due to sub-pixels by the TFT and CF.White spots : Any pixel that can be seen through ND(Neutral Density) filter when black signal(Vsig=4V) is inputted under specified condition.Black sports : Any pixel that is below 50% of maximum luminance when white signal(Vsig=0V) is inputted.	
	Stain	Luminance irregularities and discoloration spots.	
Quality	Irregularity Same as stain with more area.		
Quality Screen	Line	Same as stain but in linear shape.	Note 1,2
Stripe		Same as stain, but in arc, spirals, or moiré shape.	
		Others that are formed from concentration of irregular patterns.	

Note1: The quality of screen is set at V_{sig}=0V(white),2V(middle),4V(black)screen display and it may not be seen through 2.5% ND filter. However, for few exceptions, Sample under Optrex standard will be used for inspection.

Note2: When questions arise concerning this specifications or new problems that are not specified, it will be discussed for solution.

(B) Spot Defect Inspection Standards and Criteria.

White spots inspection criteria

Include below with the 8.2.1.conditions for common inspection

Luminance : 200 ~ 250	[lx]
Distance : 45 ~ 50	[cm] (Perpendicular from panel surface)
Time : 5	[S] (After ND filter has been placed)
Surface Brightness : 300~320	[cd/m ²]

Standard

Туре	Acceptabl e No.	Criteria
Level 1 White Spots	0	Visible through 0.3%ND filter
Level 1-2 White Spots	3	Visible through 1.0%ND filter, and not visible through 0.3%ND filter
Level 1-3 White Spots	5	Visible through 2.5%ND filter, and not visible through 1.0%ND filter
Black spots	6	Any visible black spots on V_{sig} =1V(All white cluster)
Total	7	Total white and black spots in level 1-3

Any spots that are not visible through 2.5% ND filters and not within level 1-3 is disregarded.

Density:

Only up to 2(white or black) are allowed for diameter within 10mm.

If it is continuos, then continuos spot is applied.

Continuos spot :

Any spot that continues for more then 3 pixels is not acceptable.

For 2 continuos white spots, treat as one spot for the table above.

For 2 continuos black spots, treat as two spots for the table above.

For 2 continuos white and black spot, treat as one each.

White & black spot:

If the pixel holds both white and black spot (level 1-3), treat as white spot for the table above.

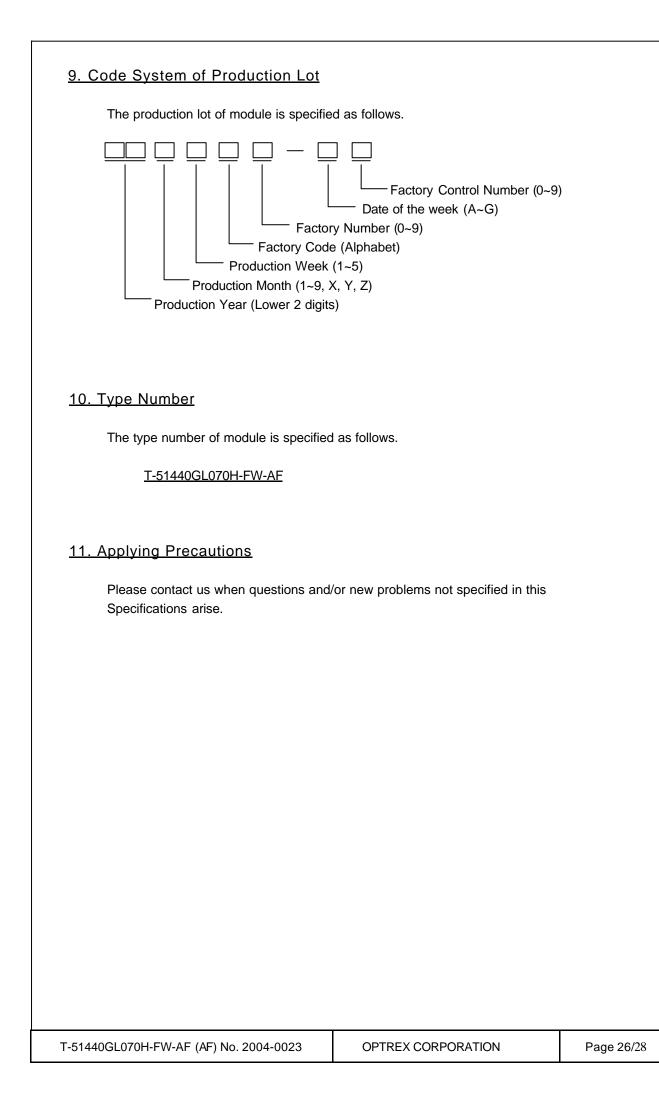
Foreign Substance(within Cell):

Foreign substance shall not be more then 3-pixel size.

If the size of substance is 2 to 3-pixel, then count as 2 in acceptable value.

If the size of substance is 1 to 2-pixel, then count as 2 in acceptable value.

If the size of substance is smaller then 1 pixel, then disregard.



12. Precautions Relating Product Handling

The Following precautions will guide you in handling our product correctly.

- 1) Liquid crystal display devices
- 1. The liquid crystal display device panel used in the liquid crystal display module is made of plate glass. Avoid any strong mechanical shock. Should the glass break handle it with care.
- 2. The polarizer adhering to the surface of the LCD is made of a soft material. Guard against scratching it.
- 2) Care of the liquid crystal display module against static electricity discharge.
- 1. When working with the module, be sure to ground your body and any electrical equipment you may be using. We strongly recommend the use of anti static mats (made of rubber), to protect work tables against the hazards of electrical shock.
- 2. Avoid the use of work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.
- 3. Slowly and carefully remove the protective film from the LCD module, since this operation can generate static electricity.
- 3) When the LCD module alone must be stored for long periods of time:
 - 1. Protect the modules from high temperature and humidity.
- 2. Keep the modules out of direct sunlight or direct exposure to ultraviolet rays.
- 3. Protect the modules from excessive external forces.
- 4) Use the module with a power supply that is equipped with an overcurrent protector circuit, since the module is not provided with this protective feature.
- 5) Do not ingest the LCD fluid itself should it leak out of a damaged LCD module. Should hands or clothing come in contact with LCD fluid, wash immediately with soap.
- 6) Conductivity is not guaranteed for models that use metal holders where solder connections between the metal holder and the PCB are not used. Please contact us to discuss appropriate ways to assure conductivity.
- 7) For models which use CFL:
- 1. High voltage of 1000V or greater is applied to the CFL cable connector area. Care should be taken not to touch connection areas to avoid burns.
- 2. Protect CFL cables from rubbing against the unit and thus causing the wire jacket to become worn.
- 3. The use of CFLs for extended periods of time at low temperatures will significantly shorten their service life.
- 8) For models which use touch panels:
- 1. Do not stack up modules since they can be damaged by components on neighboring modules.
- 2. Do not place heavy objects on top of the product. This could cause glass breakage.
- 9) For models which use COG,TAB,or COF:
- 1. The mechanical strength of the product is low since the IC chip faces out unprotected from the rear. Be sure to protect the rear of the IC chip from external forces.
- 2. Given the fact that the rear of the IC chip is left exposed, in order to protect the unit from electrical damage, avoid installation configurations in which the rear of the IC chip runs the risk of making any electrical contact.

- 10) Models which use flexible cable, heat seal, or TAB:
 - 1. In order to maintain reliability, do not touch or hold by the connector area.
- 2. Avoid any bending, pulling, or other excessive force, which can result in broken connections.
- 11)In case of buffer material such as cushion / gasket is assembled into LCD module, it may have an adverse effect on connecting parts (LCD panel-TCP / HEAT SEAL / FPC / etc., PCB-TCP / HEAT SEAL / FPC etc., TCP-HEAT SEAL, TCP-FPC, HEAT SEAL-FPC, etc.,) depending on its materials.

Please check and evaluate these materials carefully before use.

12) In case of acrylic plate is attached to front side of LCD panel, cloudiness (very small cracks) can occur on acrylic plate, being influenced by some components generated from polarizer film..

Please check and evaluate those acrylic materials carefully before use.

13. Warranty

This product has been manufactured to your company's specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

- 1. We cannot accept responsibility for any defect, which may arise from additional manufacturing of the product (including disassembly and reassembly), after product delivery.
- 2. We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.
- 3. We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed your company's acceptance inspection procedures.
- 4. When the product is in CFL models, CFL service life and brightness will vary According to the performance of the inverter used, leaks, etc. We cannot accept responsibility for product performance, reliability, or defect, which may arise.
- 5. We cannot accept responsibility for intellectual property of a third party, which may arise through the application of our product to your assembly with exception to those issues relating directly to the structure or method of manufacturing of our product.
- 6. Optrex will not be held responsible for any quality guarantee issue for defect products judged as Optrex-origin longer than 2 (two) years from Optrex production or 1(one) year from Optrex, Optrex America, Optrex Europe delivery which ever comes later.