# T-51513D104JU-FW-A-AC-V406D

TFT-LCD with LED Backlighting & transflective upgrade

Rev. 4 2010-03-26

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# 1. Revision History

Date	Rev.	Author	Description	Page
26-Mar-10	4	wc1	Initial Release	

# 2. General Specifications

The T-51513D104JU-FW-A-AC-V406D is a 10.4" color TFT-LCD (Thin Film Transistor Liquid Crystal Display) module composed of an LCD panel, driver ICs, control circuits, a LED backlight unit and a transflective upgrade. By applying 6-bit digital data, 640×480, 262,144-color images are displayed on a 10.4" diagonal screen. Input power is 3.3/5.0VDC for the LCD. Digital type data and control signals are transmitted via a CMOS interface having a (typical) 25MHz clock frequency. T-51513D104JU-FW-A-AC-V406D features white LED backlighting. The power supply for the backlight unit is not integrated into this module.

Specifications are summarized in the following table:

ltem	Specification
Display Area (mm)	211.2 × 158.4 (10.39-inch diagonal)
Number of Dots	640 × 3 (H) × 480 (V)
Pixel Pitch(mm)	0.33 (H) × 0.33 (V)
Color Pixel Arrangement	RGB vertical stripe
Display Mode	Normally white TN
Number of Colors	262,144
Brightness (cd/m²)	1600
Wide Viewing Angle Technology	Optical Compensation Film
	-65~65°(H) -45~65°(V)
Surface Treatment	Anti-glare and hard-coating 3H, transflective upgrade
Electrical Interface	CMOS
Optimum Viewing Angle (Contrast ratio)	12 o'clock
Operating Temperature Range	-20~70℃
Module Size (mm)	243.0 (W) × 181.6 (H) × 12.2 (D)
Module Mass (g)	540
Backlight Unit	LED, dual edge-lit, replaceable

Note: A characteristic value shown without any note is a typical value.

# 3. Electrical Specifications

#### 3.1. Absolute Maximum Ratings

ITEM	SYMBOL	MIN.	MAX	UNIT
Power Supply Voltage for LCD	VCC	-0.3	6.5	V
Logic Input Voltage	VI	0	6.5	V
LED Current	IL	0	1.1	Α
Operation Temperature (Panel) Note 1,2)	Top(Panel)	-20	70	C
Operation Temperature (Ambient) Note 2)	Top(Ambient)	-20	70	${\mathbb C}$
Storage Temperature Note 2)	Tstg	-20	80	C

#### [Note]

- 1) Measured at the center of active area and at the center of panel back surface
- 2) Top,Tstg  $\leq$  40°C : 90%RH max. without condensation Top,Tstg > 40°C : Absolute humidity shall be less than the value of 90%RH at 40°C without condensation.

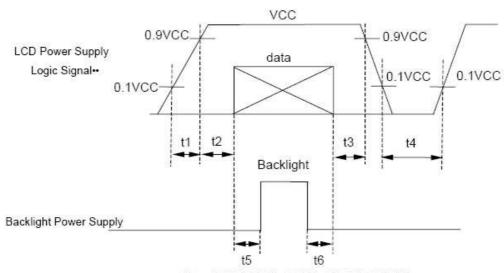
#### 3.2. DC Characteristics

Ambient temperature: Ta = 25°C

ITEM		SYMBOL	MIN.	TYP.	MAX.	UNIT	Remarks
Power Supply Voltage	3.3V	VCC	3.0	3.3	3.6	V	*1)
for LCD	power						
	5.0V	VCC	4.75	5.0	5.25	V	*1)
	power						
Power Supply Current	3.3V	ICC		300	400	mA	VCC=3.3V, *2)
for LCD	power						
	5.0V	ICC		200	280	mA	VCC=5.0V, *2)
	power						
Permissible Input Ripple	e Voltage	VRP			100	mVp-p	VCC=+3.3V/5.0V
Logic Input Voltage		VIH	2.0		5.25	V	
		VIL	0		0.8	V	

#### \*1) Power and signals sequence:

 $t1 \le 10 \text{ ms}$   $400 \text{ ms} \le t4$   $0 < t2 \le 50 \text{ ms}$   $200 \text{ ms} \le t5$  $0 < t3 \le 50 \text{ms}$   $0 \le t6$ 

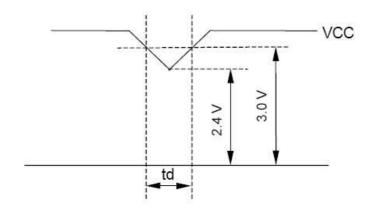


data: RGB DATA, DCLK, HD, VD, DENA

#### VCC-dip conditions:

- a) 3.3V powered:
  - 1) When 2.4  $V \le VCC < 3.0 V$ , td  $\le 10 ms$
  - 2) When VCC < 2.4 V

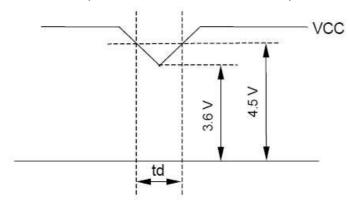
VCC-dip conditions should also follow the power and signals sequence.



#### (b) 5.0V powered:

- 1) When 3.6 V  $\leq$  VCC < 4.5 V, td  $\leq$  10 ms
- 2) When VCC < 3.6 V

VCC-dip conditions should also follow the power and signals sequence.



#### \*2) Typical current condition:

64-gray-bar pattern

480 line mode

 $VCC = +3.3V / 5.0V, f_H=31.5 \text{ kHz}, fv=60 \text{ Hz}, f_{CLK}= 25 \text{ MHz}$ 

#### 3.3. AC Characteristics

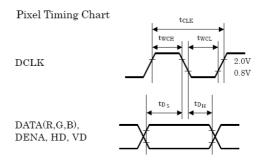
Interface Timing

	ITEM	SYMBOL	MIN	TYP	MAX	UNIT
DCLK	Frequency	f <sub>CLK</sub>	-	25	29	MHz
*1), *4)	Period	t <sub>CLK</sub>	34.5	40		ns
	Low Width	t <sub>WCL</sub>	12			ns
	High Width	t <sub>wch</sub>	12			ns
DATA *1)	Setup time	t <sub>DS</sub>	5			ns
(R,G,B,DENA,	Hold Time	t <sub>DH</sub>	5			ns
HD, VD)						
	Horizontal Active Time	t <sub>HA</sub>	640	640	640	t <sub>CLK</sub>
	Horizontal Front Porch	t <sub>HFP</sub>	10	16		t <sub>CLK</sub>
	Horizontal Back Porch	t <sub>HBP</sub>	2	138		t <sub>CLK</sub>
DENA	Vertical Active Time	t <sub>VA</sub>	480	480	480	t <sub>H</sub>
*3)	Vertical Front Porch	t <sub>VFP</sub>	1	13		$t_H$
	Vertical Back Porch	t <sub>VBP</sub>	2	33		t <sub>H</sub>
	Frequency	f <sub>H</sub>	27	31.5	38	kHz
HD	Period	t <sub>H</sub>	26.3	31.7	37.0	μs
*2), *4)	Low Width	t <sub>WHL</sub>	5	96		t <sub>CLK</sub>
	Frequency	f <sub>V</sub>	55	60	70	Hz
VD *2)	Period	t <sub>V</sub>	14.3	16.7	18.2	ms
	Low Width	t <sub>WVL</sub>	3			t <sub>H</sub>

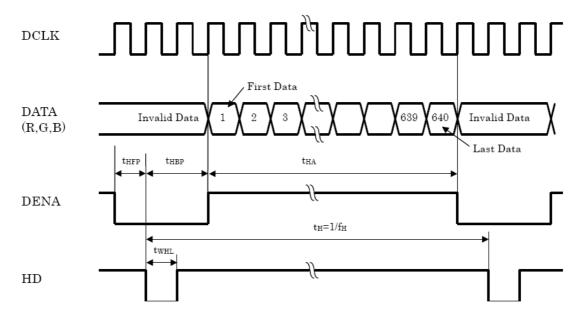
[Note]

- 1) Data is latched at the falling edge of DCLK in this specification.
- 2) Polarities of HD and VD are negative in this specification.
- 3) DENA (Data Enable) should always be positive polarity as shown in the timing specification.
- 4) DCLK should appear during all invalid periods, and HD should appear during invalid period of frame cycle.
- 5)  $t_{HFP} + t_{HBP} \ge 20 T_{clk}$

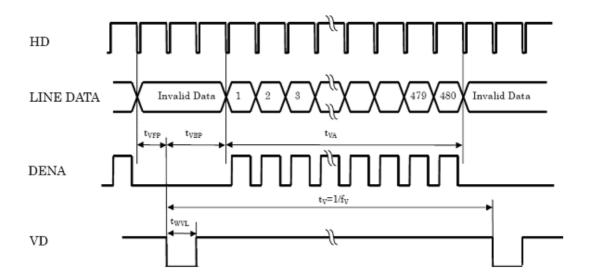
#### 3.3.1. Pixel Timing



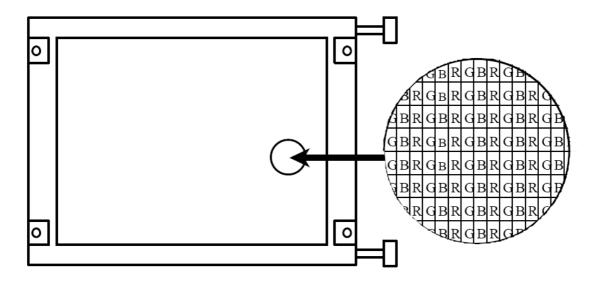
#### 3.3.2. Horizontal Timing



#### 3.3.3. Vertical Timing

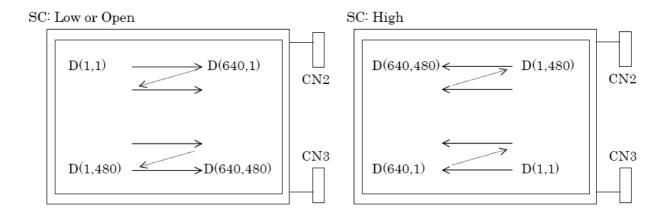


#### 3.4. Pixel Alignment



#### 3.5. Inverted Scan Capability

D(X,Y) shows the data number of input signal for LCD panel signal processing PCB.



#### 3.6. **Color Data Assignment**

									INPU	JT DA	λΤΑ								
				R DA	ATA			G DATA						B DATA					
COLOF	₹	R	R	R	R	R	R	G	G	G	G	G	G	В	В	В	В	В	В
		5	4	3	2	1	0	5	4	3	2	1	0	5	4	3	2	1	0
		М					L	М					L	М					L
		S					S	S					S	S					S
		В					В	В					В	В					В
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	CYAN	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	MAGENTA	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	GREEN(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	GREEN(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
BLUE	BLUE(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	BLUE(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	BLUE(62	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

[Note]
1) Definition of gray scale
Color (n) --- n indicates gray scale level.
Higher n means brighter level.
2) Data
1: Light 0: Low

1:High, 0: Low

#### 3.7. Backlight Specifications

Ta = 25°C

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	Remarks
LED Voltage	VL		8.46	9.36	V	*1), IL = 400mA
LED Current	IL			400	mA	*2),
Power Consumption	WL		3.39		W	IL = 400mA
Backlight Lifetime	BL	50,000		1	h	*3), *4), *5), IL = 400mA

<sup>\*1)</sup> VL is specified as the sum of the white LED forward voltages.

#### \*5) Recommended **LED backlight converter**:

PART NUMBER	MANUFACTURER
CDS-CCR2-800M-R	Apollo Display Technologies, LLC.

<sup>\*2)</sup> LEDs are best driven using a constant current source. To avoid chromaticity shifts while dimming pulse-width modulation (PWM) techniques may be employed (0-100% duty cycle).

<sup>\*3)</sup> Backlight lifetime is defined as the time when the brightness becomes 50% of the initial value.

<sup>\*4)</sup> The lifetime of the backlight depends on the ambient temperature. The lifetime will decrease under high temperature.

# 4. Optical Specifications

Conditions: Ta = 25°C, VCC = 3.3V, Input Signals: t ypical values, IL = 400 mA

ITE	M	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	Remarks
Contrast Ratio	)	CR	$\theta = \phi = 0^{\circ}$	350	500	-		*1)*2)*6
Luminance		Lw	$\theta = \phi = 0^{\circ}$	1400	1600		cd/m²	*1)*3)*6)
Luminance U	niformity	ΔLw	$\theta = \phi = 0^{\circ}$			28	%	*1)*4)*6)
Response Tin	ne	tr	$\theta = \phi = 0^{\circ}$		10	-	ms	*1)*5)*6)
		tf	$\theta = \phi = 0^{\circ}$	ł	30	ł	ms	*1)*5)*6)
Viewing	Horizontal	φ		-60~60	-70~70	ı	0	*1)*6)
Angle	Vertical	θ	CR ≥ 10	-50~40	-60~50	ı	0	*1)*6)
	Horizontal	φ		-70~70	-80~80	ı	0	*1)*6)
	Vertical	θ	CR ≥ 5	-70~55	-80~65	ı	0	*1)*6)
Image sticking	9	tis	2 h			2	S	*7)
Chromaticity	White	Wx	$\theta = \phi = 0^{\circ}$		0.325			*1)*6)
		Wy			0.350			

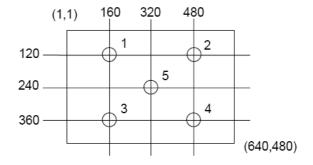
#### [Note]

Measured items are measured using CS1000 (MINOLTA) for color coordinates, EZContrast (ELDIM) for viewing angle, and CS1000 or BM-5A (TOPCON) for others under the dark room condition (no ambient light) after more than 30 minutes from turning on the lamp unless noted.

#### \*1) Measurement Points:

Contrast Ratio, Luminance, Response Time, Viewing Angle, Color Coordinates: Display Center

Luminance Uniformity: points 1-5 shown in a figure below



#### \*2) Definition of Contrast Ratio:

CR=ON (White) Luminance / OFF(Black) Luminance

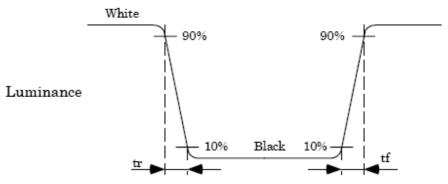
\*3) Definition of Luminance:

Lw= ON (White) Luminance

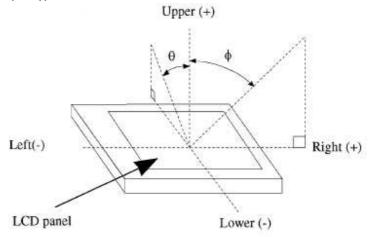
\*4) Definition of Luminance Uniformity:

 $\Delta$ Lw=[Lw(MAX)/Lw(MIN)-1] × 100

#### \*5) Definition of Response Time



#### \*6) Definition of Viewing Angle( $\theta$ , $\phi$ )



#### \*7) Image sticking:

Continuously display the test pattern shown in the figure below for two-hours. Then display a completely white screen. The previous image shall not persist more than two seconds at 25℃.

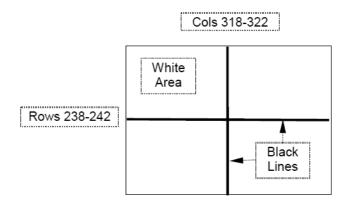


Figure: Test pattern for image sticking test

# 5. I/O Interface

## 5.1. Pin Assignments

(1) CN 1(Interface Signal)

Connector used: DF9B-31P-1V (HIROSE)

Mating connector: DF9-31S-1V (HIROSE)

Pin No.	Symbol	Function
1	GND	
2	DCLK	Clock signal for sampling data signal
3	HD	Horizontal sync signal
4	VD	Vertical sync signal
5	GND	• •
6	R0	Red data signal (LSB)
7	R1	Red data signal
8	R2	Red data signal
9	R3	Red data signal
10	R4	Red data signal
11	R5	Red data signal (MSB)
12	GND	
13	G0	Green data signal (LSB)
14	G1	Green data signal
15	G2	Green data signal
16	G3	Green data signal
17	G4	Green data signal
18	G5	Green data signal (MSB)
19	GND	
20	B0	Blue data signal (LSB)
21	B1	Blue data signal
22	B2	Blue data signal
23	B3	Blue data signal
24	B4	Blue data signal
25	B5	Blue data signal
26	GND	
27	DENA	Data enable signal (to settle the viewing area)
28	VCC	3.3 / 5.0V LCD Power Supply
29	VCC	3.3 / 5.0V LCD Power Supply
30	TEST	This pin should be open. Test signal output for internal use.
31	SC	Reverse scan control ( Low or open: Normal , High : Reverse )

<sup>\*)</sup> The shielding case is connected with GND.

#### (2) CN2, CN3 (Backlight)

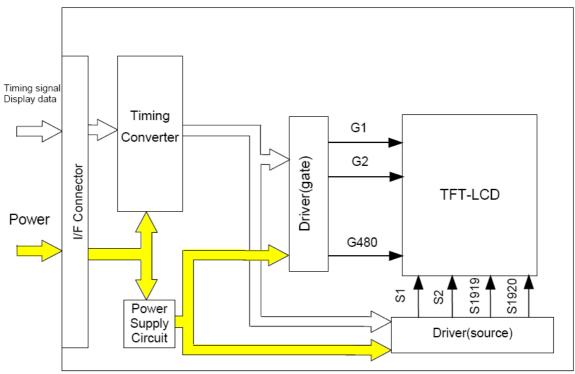
Backlight-side connector: 22-01-3027 (Molex)

Mating connector: 22-05-3021 (Molex)

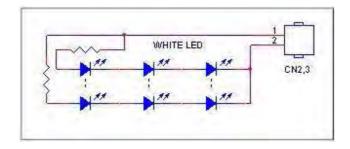
Pin No.	Symbol	Function
1	LRH	VBLH (High voltage)
2	LRL	VBLL (Low voltage)

Note] VBLH - VBLL = VL

#### 5.2. Block Diagram



**BACKLIGHT** 



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#### 6. Test Criteria

#### (1) Temperature and Humidity

TEST ITEM	CONDITIONS		
HIGH TEMPERATURE	40℃, 90%RH, 240 ·h		
HIGH HUMIDITY OPERATION	(No condensation)		
HIGH TEMPERATURE STORAGE	80℃, 240 h		
LOW TEMPERATURE STORAGE	-20℃, 240 h		
THERMAL SHOCK (NON-OPERATION)	BETWEEN -20℃ (1h) an d		
	80℃(1h),		
	100 CYCLES		

#### (2) Shock and Vibration

ITEM	CONDITIONS		
	Shock level: 1470 m/s² (150G)		
SHOCK	Waveform: half sinusoidal wave, 2 ms		
(NON-OPERATION)	Number of shocks: one shock input in each direction of three		
	mutually perpendicular axes for a total of six shock inputs.		
	Vibration level: 9.8 m/s <sup>2</sup> (1.0G)		
VIBRATION	Waveform: sinusoidal		
(NON-OPERATION)	Frequency range: 5 to 500 Hz		
	Frequency sweep rate: 0.5 octave /min		
	Duration: one sweep from 5 to 500 Hz in each of three		
	mutually perpendicular axis(each x,y,z axis: 1 hour, total 3		
	hours)		

#### (3) Judgement Standard

Judgment of the above tests should be made as follows:

Pass: Normal display image, no damage of the display function (ex. no line defect). Partial transformation of the module parts should be ignored.

Fail: No display image, damage of the display function (ex. line defect).

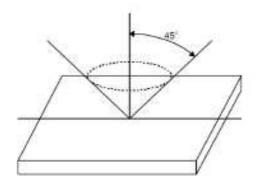
# 7. Appearance Standards

#### 7.1. Inspection Conditions

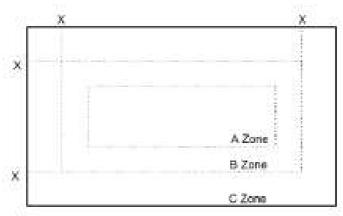
The LCD shall be inspected under 40W white fluorescent light.

The distance between the eyes and the sample shall be more than 30cm.

All directions for inspecting the sample should be within 45° against perpendicular line.



#### 7.2. Definition of Applicable Zones



X : Maximum Seal Line

Zone A: Active display area

Zone B: Out of active display area up to viewing area

Zone C: Remaining parts

Zone A + Zone B = Viewing area

#### 7.3. Standards

No.	Parameter	Criteria				
1	Polarizer Scratches					
			Zone	Acceptable Nu	umber	
		X (mm)	Y (mm)	A B	C	
		L ≤ 15	0.01 <w≤0.05< td=""><td>4</td><td>*</td></w≤0.05<>	4	*	
		L > 15	W > 0.01	0	*	
		-	W > 0.05	0	*	
		X : Length, Y : Width * : Disregard				
2	DENT				10	
		Zone		Acceptable Nu	ımber	
		Dimension (ı		A B	C	
		0.30 < D ≤	0.50	4	*	
		0.50 < D		0	*	
		D : Average Diameter = (long+short)/2 *: Disregard				
3	BLACK and WHITE SPOT BUBBLE	BUBBLE				
			Zone	Acceptable Nu	ımber	
		Dimension (		A B	С	
		0.30 < D ≤	0.50	5	*	
		0.50 < D		0	*	
	8		3	į.	**	
4	LINT					
			Zone	Acceptable Nu	ımber	
		X (mm)	Y (mm)	A B	C	
		L ≤ 3.0	W ≤ 0.15	4	*	
		L > 3.0	W ≤ 0.15	0	*	
		4	W > 0.15	According to BLACK	*	
				SPOT		
		X:Length, Y	: Width *: Disr	egard	'n	
5	(a) Bright Dot					
	(b) Dark Dot		Zone	Acceptable Nu	ımber	
	100000	Dimension (	mm)	A B	С	
		Bright Dot		7 (G ≤ 3)	*	
		Dark Dot Total		7		
				10		
6	TWO Adjacent Dot	1	0		5.	
U	TWO Adjacent Dot		Zone	Acceptable Nu	ımbor	
		Dimension (		A B	C	
				3 PAIRS	*	
		Bright Dot Dark Dot		3 PAIRS	*	
		Dark Dot		3 PAIR3	L.	
7	Three or More Adjacent Dot	NOT ALLOWED				
8			F-500			
4000		Zone Dimension (mm)		Acceptable Number		
				A B	С	
		Bright Dot		5 mm	*	
		Dark Dot		5 mm	*	
		, literature and the second	9	2100	1	
9	Line Defect	NOT ALLOW	ED			

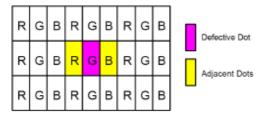
#### Note 1: Bright Dot is defined as follows:

Visible through 5% transmission ND filter under the condition that black image (color 0) is on the display.

#### Note 2: Dark Dot is defined as follows:

Recognizable darker than around under the condition that each R(63), G(63), B(63) image is on the display.

#### Note 3: Definition of adjacent



# 8. Ordering Information

The ordering code for this module is: T-51513D104JU-FW-A-AC-V406

# 9. Application Precautions

Please contact us when questions and/or new problems not specified in this specification arise.

## 10. Precautions Relating to Product Handling

The Following precautions will guide you in handling this product correctly.

- 1) Liquid crystal display devices:
  - a) The liquid crystal display device panel used in the liquid crystal display module is made of plate glass. Avoid any strong mechanical shock. Should the glass break, handle it with care.
  - b) The polarizer adhering to the surface of the LCD is made of a soft material. Guard against scratching it.
- 2) Take care to guard the liquid crystal display module against static electricity discharge.
  - a) When working with the module, be sure to ground your body and any electrical equipment you may be using. We strongly recommend the use of anti-static mats (made of rubber) to protect sensitive circuits against the hazards of static electricity.
  - b) Avoid the use of work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity treated fibers.
  - c) Slowly and carefully remove the protective film from the LCD module, since this operation can generate static electricity.
- 3) When the LCD module must be stored for long periods of time:
  - a) Protect the module from high temperature and humidity.
  - b) Keep the module out of direct sunlight or direct exposure to ultraviolet rays.
  - c) Protect the module from excessive external forces.
- 4) Use the module with a power supply that is equipped with an over current protection circuit, since the module is not provided with this protective feature.
- 5) Do not ingest the LCD fluid itself should it leak out of a damaged LCD module. Should hands or clothing come in contact with LCD fluid, wash immediately with soap.
- 6) Conductivity is not guaranteed for models that use metal holders where solder connections between the metal holder and the PCB are not used. Please contact us to discuss appropriate ways to assure conductivity.
- 7) For models which use touch panels:
  - a) Do not stack up modules since they can be damaged by components on neighboring modules.
  - b) Do not place heavy objects on top of the product. This could cause glass breakage.
- 8) For models which use COG, TAB, or COF:
  - a) The mechanical strength of the product is low since the IC chip faces out unprotected from the rear. Be sure to protect the rear of the IC chip from external forces.
  - b) Given the fact that the rear of the IC chip is left exposed, in order to protect the unit from electrical damage avoid installation configurations in which the rear of the IC chip runs the risk of making any electrical contact.
- 9) For models which use flexible cable, heat seal, or TAB:
  - a) In order to maintain reliability, do not touch or hold by the connector area.
  - b) Avoid any bending, pulling, or other excessive force, which can result in broken connections.
- 10) Mechanical stress can have an adverse effect on connecting parts (LCD panel-TCP / HEAT SEAL / FPC / etc., PCB-TCP / HEAT SEAL / FPC etc., TCP-HEAT SEAL, TCP-FPC, HEAT SEAL-FPC, etc.) depending on the materials used. Please check and evaluate these materials carefully before use.
- 11) In case an acrylic plate is attached to front side of LCD panel, cloudiness (very small cracks) can occur in the acrylic plate, being influenced by some components generated from polarizer film. Please check and evaluate the acrylic materials carefully before use.

### 11. Warranty

Apollo Display Technologies, LLC. offers a standard one year parts and labor guarantee on all products sold against manufacturers defect.

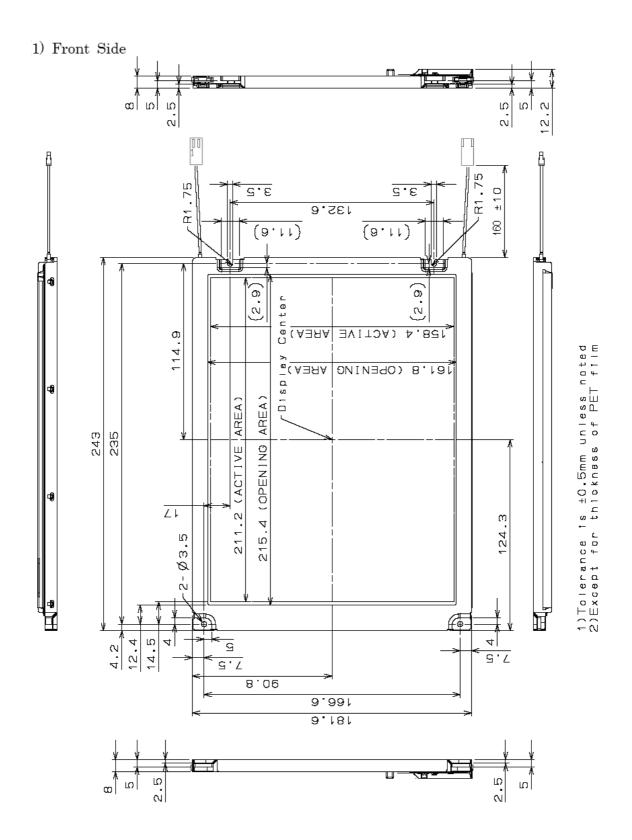
Customer is liable for shipping charges associated with shipment of product back to Apollo for return analysis. If product is found to be defective Apollo will repair or replace the part free of charge. Apollo will cover the shipping cost of all warranty items back to the customer (via standard ground service).

For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life.

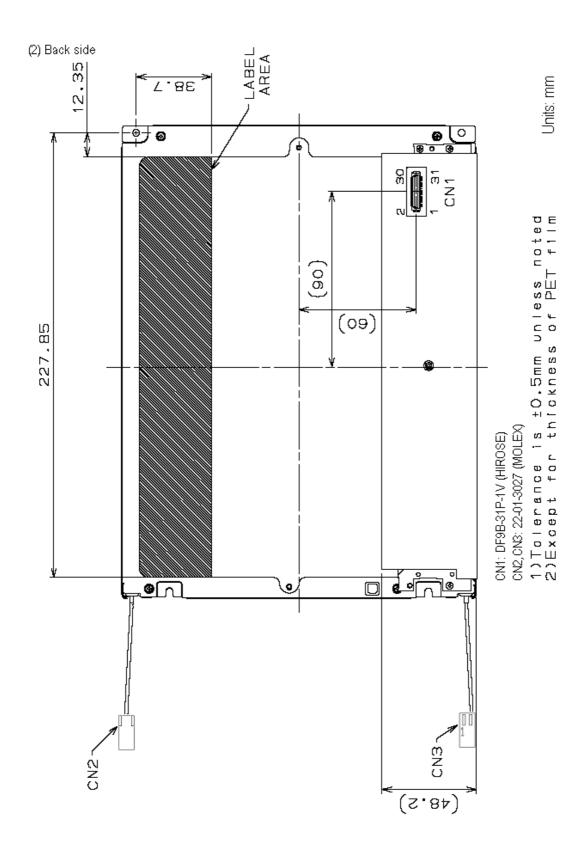
- 1. We cannot accept responsibility for any defect, which may arise from additional manufacturing of the product (including disassembly and reassembly), after product delivery.
- 2. We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.
- 3. We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed your company's acceptance inspection procedures.
- 4. We cannot accept responsibility for intellectual property of a third party, which may arise through the application of our product to your assembly with exception to those issues relating directly to the structure or method of manufacturing of our product.

# 12. Mechanical Specifications

Mechanical specifications are found on the following page(s).



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