PRIME VIEW

PA035XSL

Version : **5.0**

TECHNICAL SPECIFICATION

MODEL NO. : PA035XSL

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☐Customer's Approved

Customer _____

Date _____

By _____

☐PVI's Confirmation

Confirmed By Tomy Chang

Prepared By 曾耀德

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Revision History

Rev.	Issued Date	Revised Contents
0.1	Mar,30 2004	NEW
0.2	Jul,5 2004	Modify Page 3: Outline Dimension From 83.7(W)×68.6(H)×6.6 (D) to 83.5(W)×63.1(H)×3.6 (D)
1.0	Aug,13 2004	Updata Page 21:E/S Optics data10-1) Specification Page27,28: Packing drawing
2.0	Oct,21 2004	Modify Page 10: 8-5) Timing Characteristics Of Input Signals Page 13: Fig.8-2 Horizontal Start Pixel
3.0	Dec,23 2005	1.Page 3: Outline Dimension Modify: 1.1 Add FPC PI Tape outline . 1.2 Add tolerance of Active Area and Bezel opening . 2.Page 25. 12. Reliability Test Add High Temperature Operation Test=60℃,240hrs. 3.Page 5 : 5.Input / Output Terminals Modify FPC pin No.23 Form V_{EE} to No connection 4.Add Appendix.
4.0	July,24,2006	Modify Page 20 10. Optical Characteristics LED Life Time 10000 hrs modify to 30000 hrs
5.0	July,30,2007	Modify Page 12. Reliability Test Low Temperature Storage Test:-20℃ change to -30℃ High Temperature Operation Test:60℃ change to 70℃ Low Temperature Operation Test:0℃ change to -20℃

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TECHNICAL SPECIFICATION

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1. Application

This technical specification applies to 3.5” color TFT-LCD panel. The 3.5” color TFT LCD panel is designed for camcorder, digital camera application and other electronic products which require high quality flat panel displays.

2. Features

. Compatible with NTSC or PAL system

. High Resolution：224,640 Dots

. Up/Down and Left/Right Image Reversion

3. Mechanical Specifications

Parameter	Specifications	Unit
Screen Size	3.5 (diagonal)	inch
Surface Treatment	Anti-Glare	
Display Format	320×(R,G,B) ×234	dot
Active Area	71.6×52.65	mm
Pixel Pitch	0.2235 (H)×0.225 (V)	mm
Pixel Configuration	Delta	
Outline Dimension	83.5(W)×63.1(H)×3.6 (D)	mm
Weight	32±5	g
Gray scale inversion direction	6 o’clock [ref to Page 22 viewing angle]	

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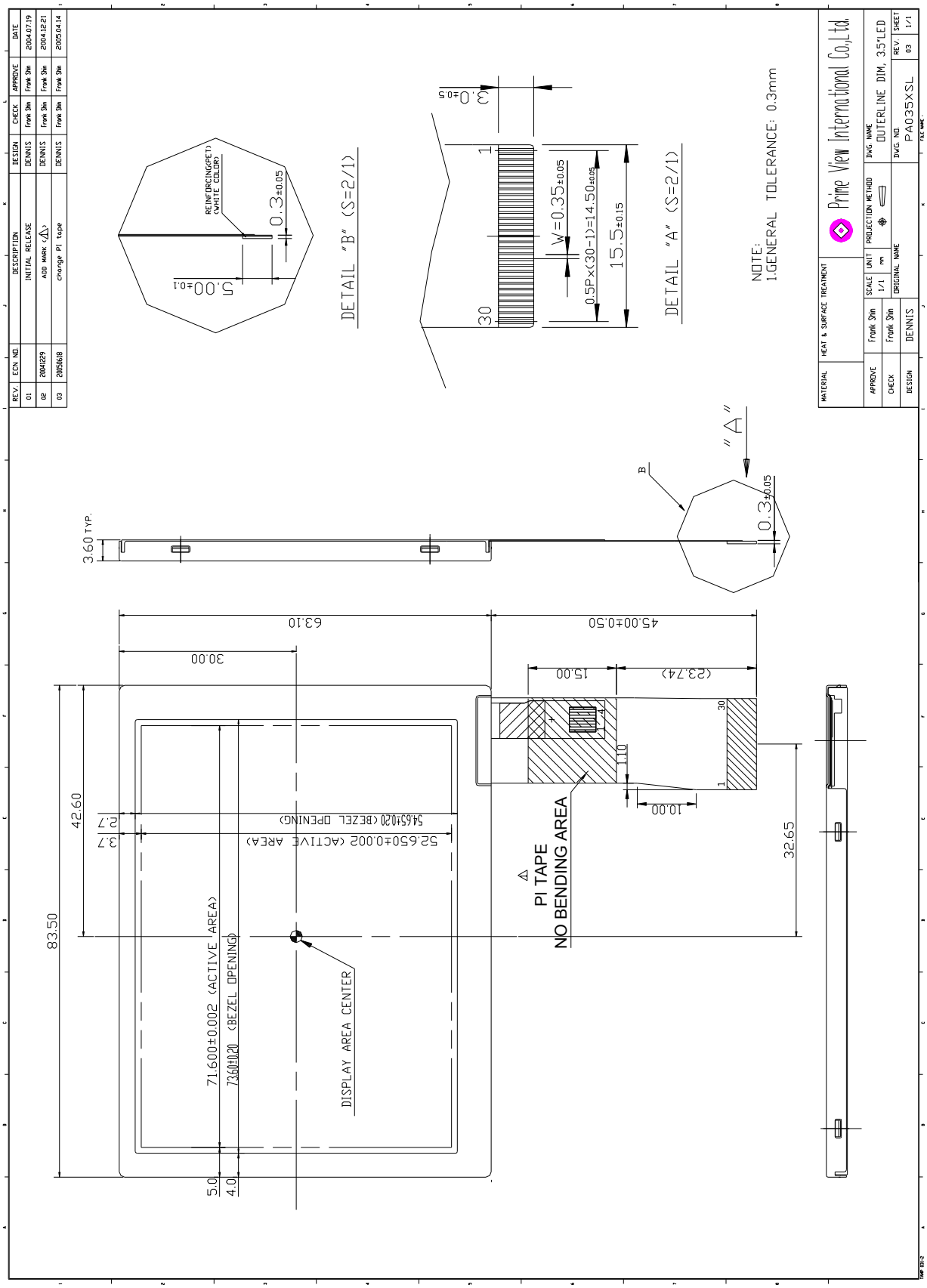
4. Mechanical Drawing of panel:



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5.Input / Output Terminals

Pin No	Symbol	I/O	Description	Remark
1	STH1	I/O	Start pulse for source driver	Note 5-1
2	AV_{SS}	I	Analog GND for source driver	
3	AV_{DD}	I	Analog power input for source driver	Note 5-2
4	V_B	I	Video Input B	Note 5-3
5	V_G	I	Video Input G	
6	V_R	I	Video Input R	
7	V_{SS}	I	Digital GND	
8	V_{DD}	I	Digital power input	Note 5-2
9	CPH1	I	Sampling and shift clock for source driver	
10	CPH2	I	Sampling and shift clock for source driver	
11	CPH3	I	Sampling and shift clock for source driver	
12	STH2	I/O	Start pulse for source driver	Note 5-1
13	Q2H	I	Video input rotation control	
14	INH	I	Output enable for source driver	
15	R/L	I	Left/Right Control for source driver	Note 5-1
16	V_{COM}	I	Common electrode voltage	Note 5-3
17	XOE	I	Output enable for gate driver	
18	CPV	I	Clock input for gate driver	
19	U/D	I	Up/Down Control for gate driver	Note 5-1
20	DIO2	I/O	Vertical start pulse	Note 5-1
21	DIO1	I/O	Vertical start pulse	
22	V_{GL}	I	Gate off voltage(alternative every 1-H)	Note 5-4
23	NO CONNECTION			
24	V_{SS}	I	GND	
25	V_{CC}	I	Logic power for gate driver	Note 5-2
26	V_{GH}	I	Gate on voltage	Note 5-5
27	NO CONNECTION			
28	GLED2	I	Supply current for LED B/L	
29	GLED3	I	Supply current for LED B/L	
30	VLED	I	Supply voltage for LED B/L	

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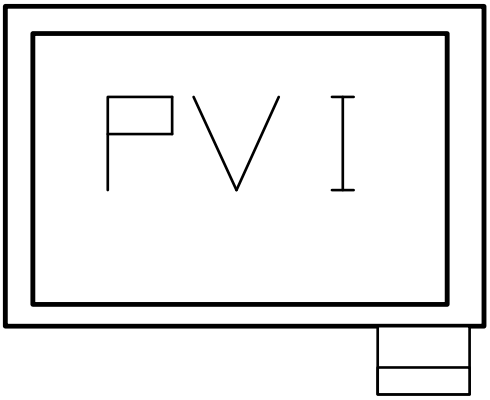
Note 5-1 : STH1, STH2 and R/L mode

R/L	STH1	STH2	Remark
High(V_{DD})	Input	Output	Left to Right
Low(0 Volt.)	Output	Input	Right to Left

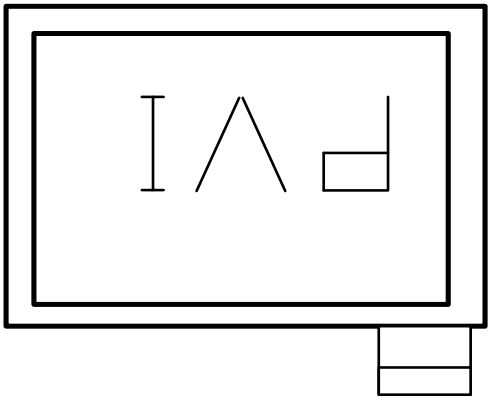
DIO1,DIO2,and U/D mode

U/D	DIO1	DIO2	Remark
High (V_{CC})	Input	Output	Down to Up
Low (0 Volt.)	Output	Input	Up to Down

U/D(PIN 19)=High R/L(PIN 15)=High



U/D(PIN 19)=Low R/L(PIN 15)=Low



Note 5-2 : V_{DD}, $V_{CC(TYP.)}$ = +3.3V , $AV_{DD\ (TYP.)}$= +5.0V

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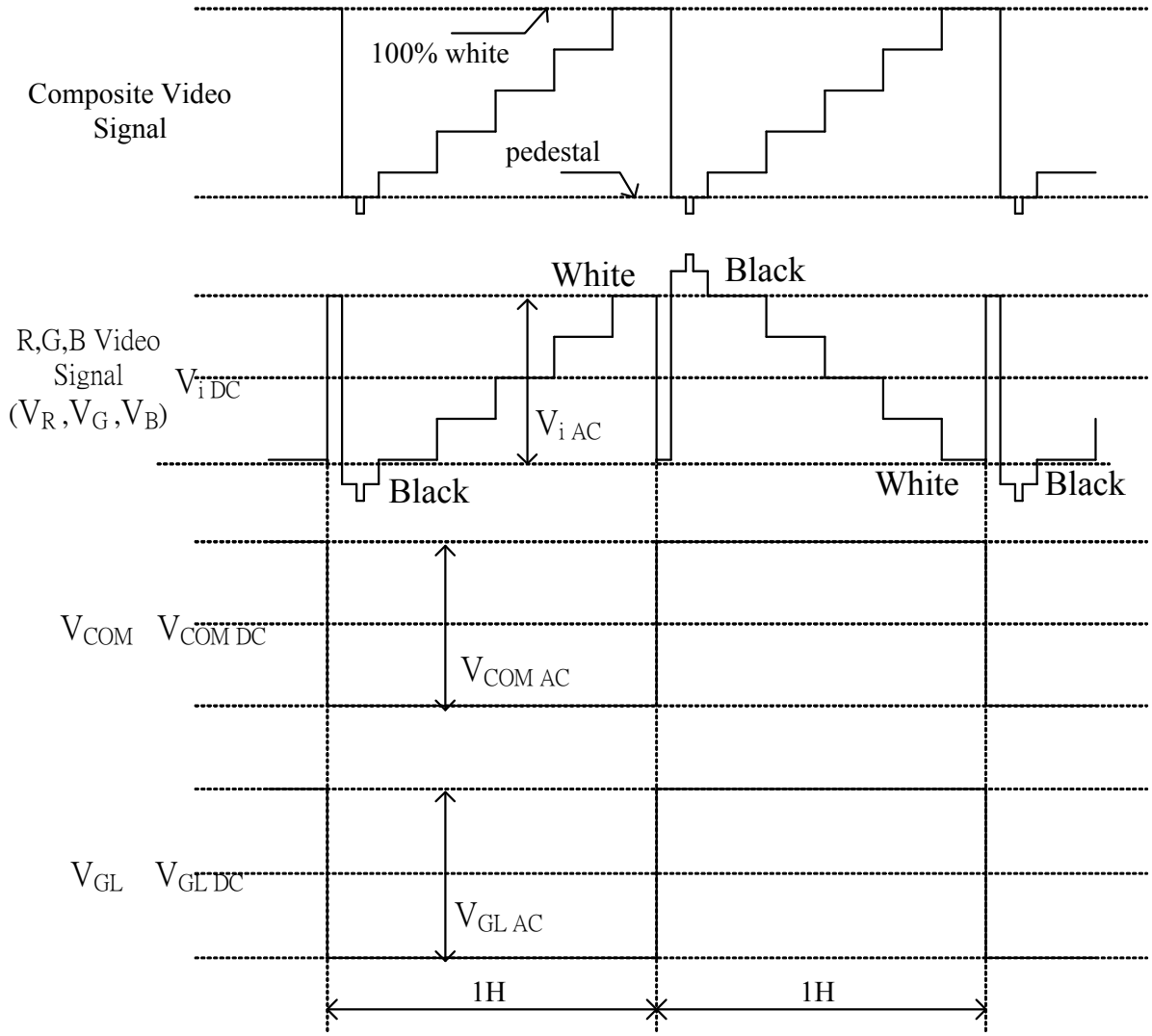
Note 5-3 : $V_{COM\ (TYP.)} = 6\ V_{PP}$.

Phase of the video signal input and V_{COM}
The relation between these values could refer to 8-1 Operating condition.

Fig.1



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Liquid crystal transmission of the video signal input, V_{COM} and timing

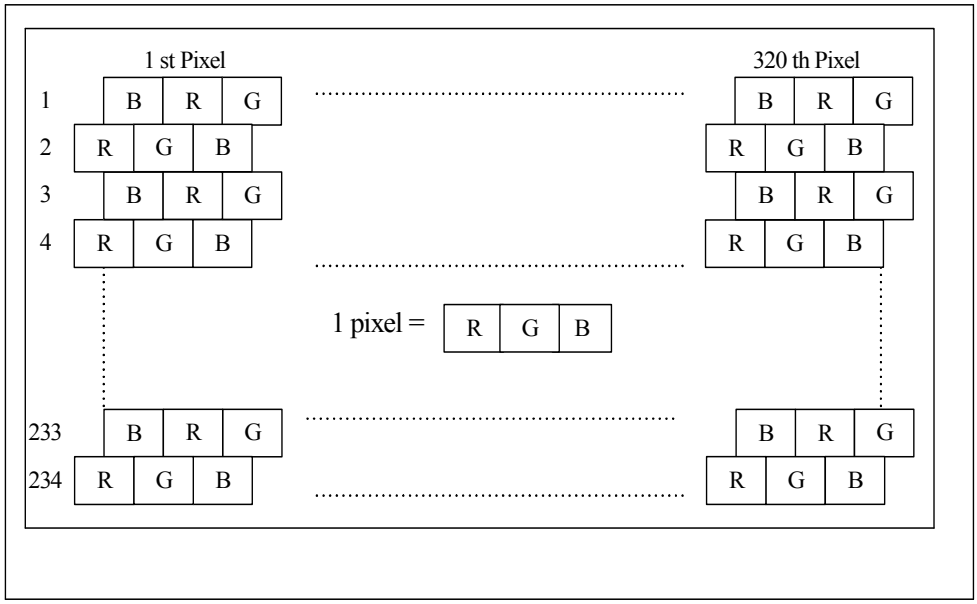
	V_{COM}	
	H Level	L Level
Video Signal Input Maximum	Black	White
Video Signal Input Minimum	White	Black

White : maximum transmission / Black : minimum transmission

Note 5-4: $V_{GL(TYP.)}$ = -12V

Note 5-5 : $V_{GH(TYP.)}$ = +17V .

6. Pixel Arrangement



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7. Absolute Maximum Ratings:
The followings are maximum values , which if exceeded, may cause faulty operation or damage to the unit.

Parameter		**Symbol**	**MIN.**	**MAX.**	**Unit**	**Remark**
Supply Voltage For Source Driver		AV_{DD}	-0.3	+7.0	V	
		V_{DD}	-0.3	+7.0	V	
Supply Voltage For Gate Driver		V_{CC}	-0.3	+7.0	V	
		V_{GH}-V_{GI}	-0.3	+40	V	
	H Level	V_{GH}	-0.3	+45	V	
	L Level	V_{EE}	-23	+0.3	V	

8. Electrical Characteristics

8-1) Operating Condition

Parameter		**Symbol**	**MIN.**	Typ.	MAX.	Unit	Remark
Supply Voltage For Source Driver	Analog	V_{DD}	+3.0	+3.3	+3.6	V	
	Logic	AV_{DD}	+3.0	+3.3	+3.6	V	Depend on T/C signal voltage
			+4.5	+5.0	+5.5	V	
Supply Voltage For Gate Driver	V_{GH}		+15.0	+17.0	+19.0	V	
	$V_{GL\ DC}$		-13.5	-12	-9.5	V	DC Component of V_{GL}
	$V_{GL\ AC}$		-	6.0	-	V_{P-P}	AC Component of V_{GL}
	Logic	V_{CC}	+3.0	+3.3	+3.6	V	Depend on T/C signal voltage
			+4.5	+5.0	+5.5	V	
Analog Signal input Level (VR , VG , VB)	V_{IAC}		-	+4.0	+4.2	V	Note 8-1
	V_{IDC}		-	+2.5	-	V	
Digital input voltage	H level	V_{IH}	0.7 V_{DD1}	---	V_{DD1}	V	
	L level	V_{IL}	-0.3	---	0.3 V_{DD1}	V	
Digital output voltage	H level	V_{OH}	0.7 V_{DD1}	---	V_{DD1}	V	
	L level	V_{OL}	-0.3	---	0.3 V_{DD1}	V	
V_{COM}		$V_{COM\ AC}$	-	+6.0	-	V_{P-P}	AC Component of V_{COM}
		$V_{COM\ DC}$	-	1.0	-	V	DC Component of V_{COM} Note 8-2

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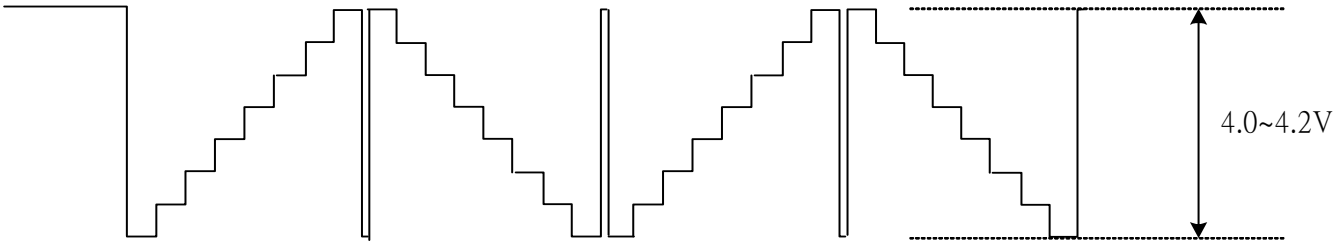
Note 8-1：Both NTSC and PAL system Video Signal input waveform is based on 8 steps gray scale.



Note 8-2 : PVI strongly suggests that the $V_{COM\ DC}$ level shall be adjustable, and the adjustable level range is 1.0V±1V,every module's $V_{COM\ DC}$ level shall be carefully adjusted to show a best image performance.

8-2)Current Consumption (GND=AV_{SS}=0V)

Ta= 25 ℃

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Current for Driver	I_{GH}	V_{GH}=+17V	-	0.055	0.083	mA	
	I_{GL}	V_{GL}=-12V	-	0.067	0.087	mA	V_{GL} center voltage
	I_{CC}	V_{CC}=+3.3V	-	0.441	0.563	mA	
	AI_{DD}	AV_{DD}=+5V	-	7	10	mA	
	I_{DD}	V_{DD}=+3.3V	-	1.2	3	mA	

8-3) Backlight driving & Power Consumption

Pin No	Symbol	Description	Remark
28	GLED2	Supply current for LED	
29	GLED3	Supply current for LED	
30	VLED	Supply voltage for LED	Note 8-3

Note 8-3 : Supply voltage for LED would depend on supply current.

GND = 0 V Ta= 25 ℃

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
LED voltage	VLED	9	9.6	11.4	V	I_L=20mA
LED current	ILED1	-	20	-	mA	Note 8-4
	ILED2					
Backlight Power Consumption	PLED	360	384	456	mW	Note 8-5

Note 8-4 : LED B/L applied information , please refer to the appendix at the end .

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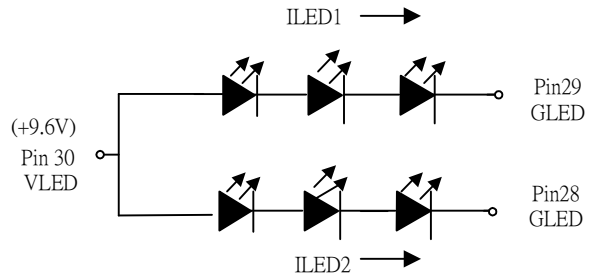
Note 8-5 : $P_{LED} = V_{LED} * I_{LED1} + V_{LED} * I_{LED2}$



8-4) Power Consumption

Ta= 25 ℃

Parameter	Symbol	Conditions	TYP.	MAX	Unit	Remark
LCD Panel Power Consumption	-	-	48.7	72.4	mW	
Backlight Power Consumption	P_{LED}	-	384	456	mW	
Total Power Consumption	-	-	432.7	528.4	mW	

8-5) Timing Characteristics Of Input Signals

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Remark
1Field Scanning Period	t1V	-	262.5	-	H	
1Line Scanning Period	t1H	-	63.5	-	μs	
Source Driver Operating Frequency	fhc	4	6.4	8	MHz	
Signal Sampling Pulse Width	tchw	125	156.25	250	ns	
Signal Sampling Pulse Delay	tchd	47.65	52	58.25	ns	tchd 12,23
Signal Sampling Pulse Width(H)	tchwh	71.45	78.12	87.35	ns	
Signal Sampling Pulse Delay(L)	tchwl	71.45	78.12	87.35	ns	
Source Start Signal Pulse Width	tshw	45	156.25	315	ns	*tshset=tshhld
Source Start Signal Setup Time	tshset	20	78.12	-	ns	
Source Start Signal Hold Time	tshhld	20	78.12	-	ns	
Source Output Enable Pulse Width	tohw	1.0		-	μs	
Source Start Signal Rising Time	tss	-	9.8	-	μs	
Video Input Signal Start Point	tvs	-	10.0	-	μs	
Phase Difference Between OEH&CPV	toc	1.5	2.3	-	μs	
Gate Clock Period	tcvw	10	63.5	-	μs	
Gate Clock Pulse Width(H)	tcvwh	10	31.7	48	μs	
Gate Clock Pulse Width(L)	tcvwl	10	31.7	48	μs	
Gate Start Signal Pulse Width	tsvw	5	63.5	126**	μs	**tsvset=tsvhld
Gate Start Signal Setup Time	tsvset	5	53.2	-	μs	
Gate Start Signal Hold Time	tsvhld	5	10.3	-	μs	
Phase Difference Between OEH&STH	tosp	-	4	-	μs	
Phase Difference Between SYNC&OEH	tohs	-	1.4	-	μs	
Gate Output Enable Pulse Width	toev	-	2.5	-	μs	
V_{COM} Delay Time	t_{DCOM}	-	-	3	μs	
RGB Delay Time	t_{DRGB}	-	-	2	μs	
Vertical Display Start	tsv	-	3	-	tH	

8-6) Signal Timing Waveforms

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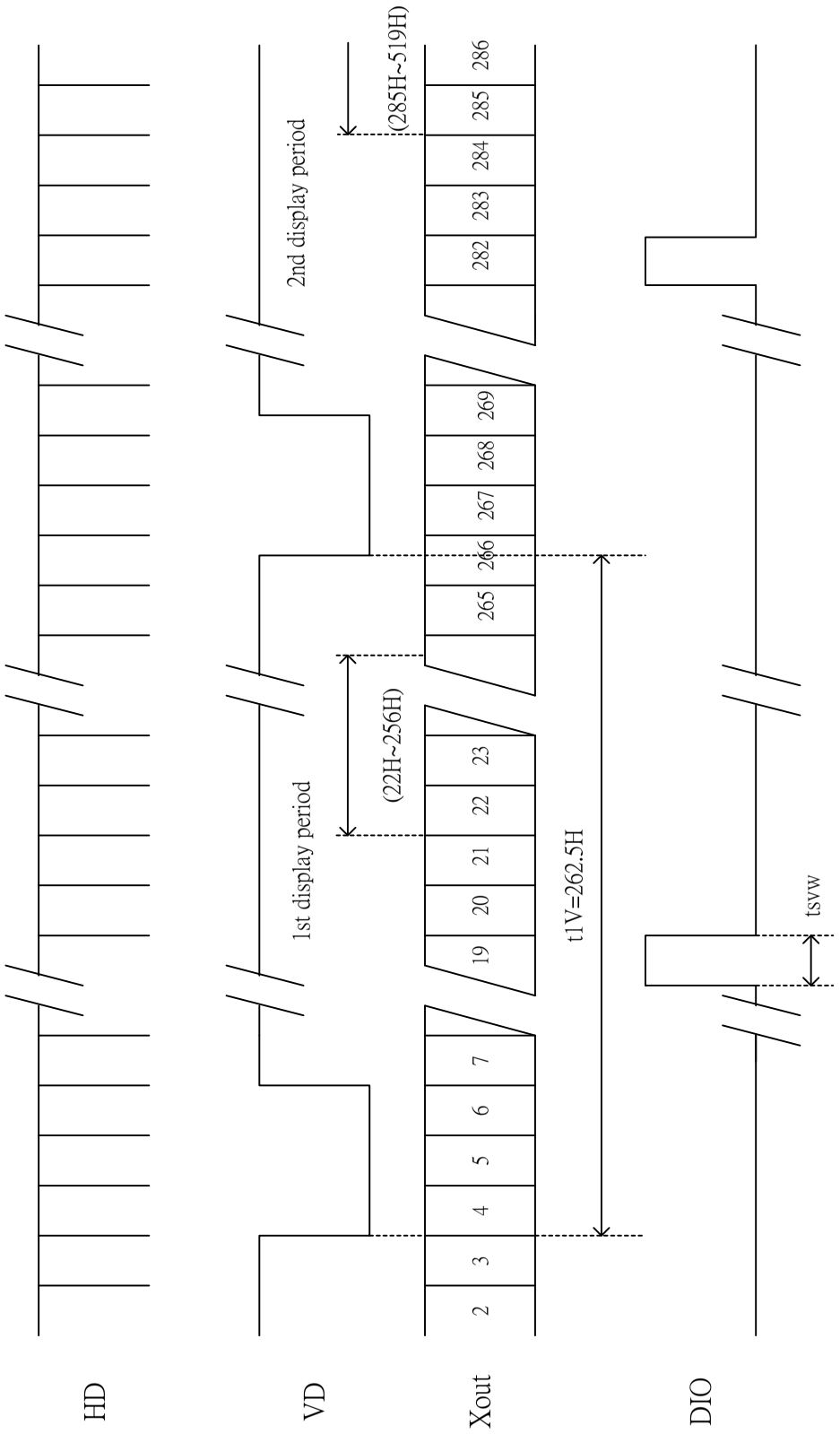

Fig. 8-1 Vertical Start Line for NTSC

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** odd field : Scan lines 14n+6 14n+12 (n = 2, 3, 4..) are not displayed.
even field : Scan lines 14n+12 14n+20 (n = 2, 3, 4..) are not displayed.

Fig. 8-2 Vertical Start Line for PAL

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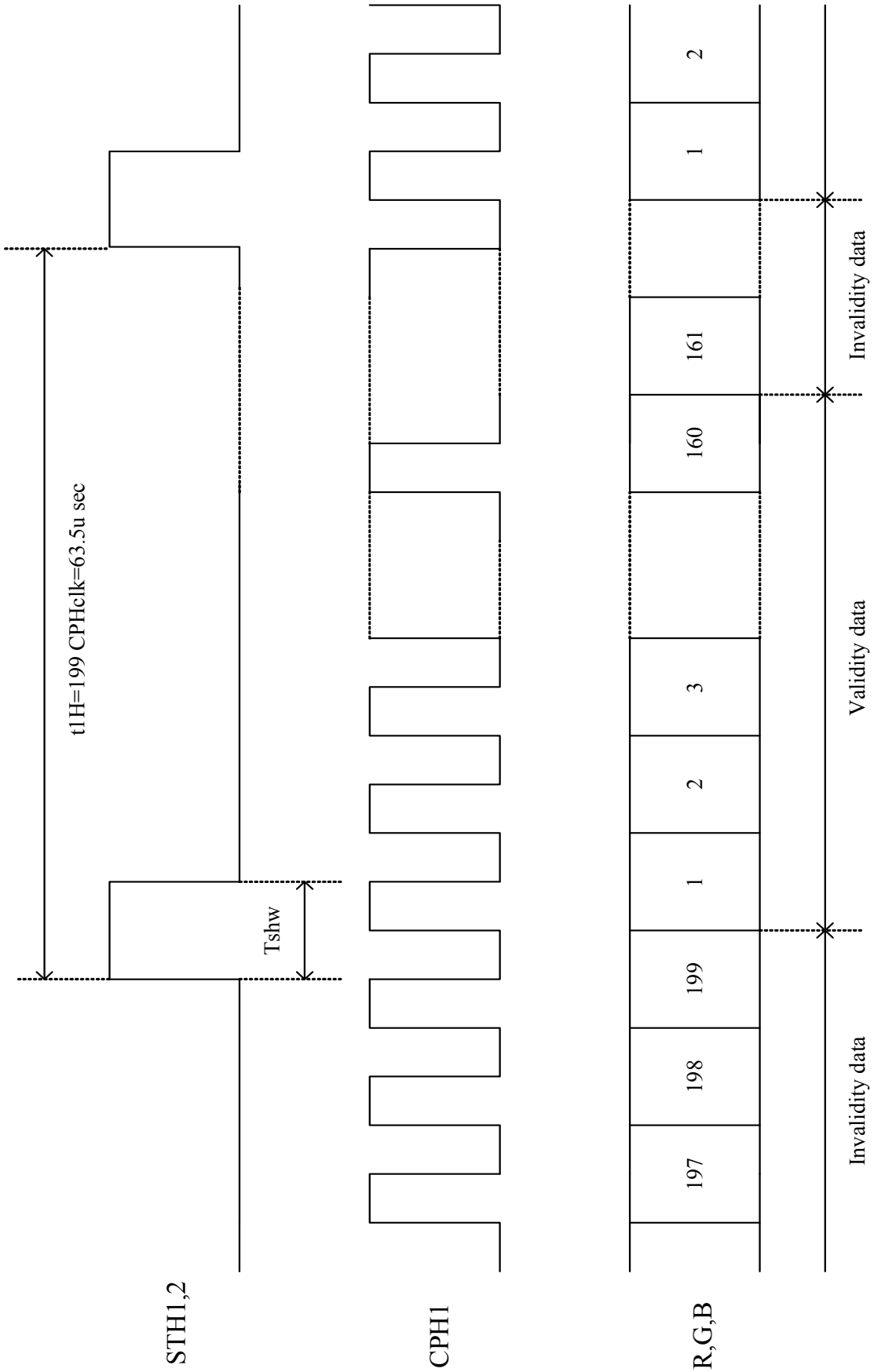

Fig. 8-3 Horizontal Start Pixel

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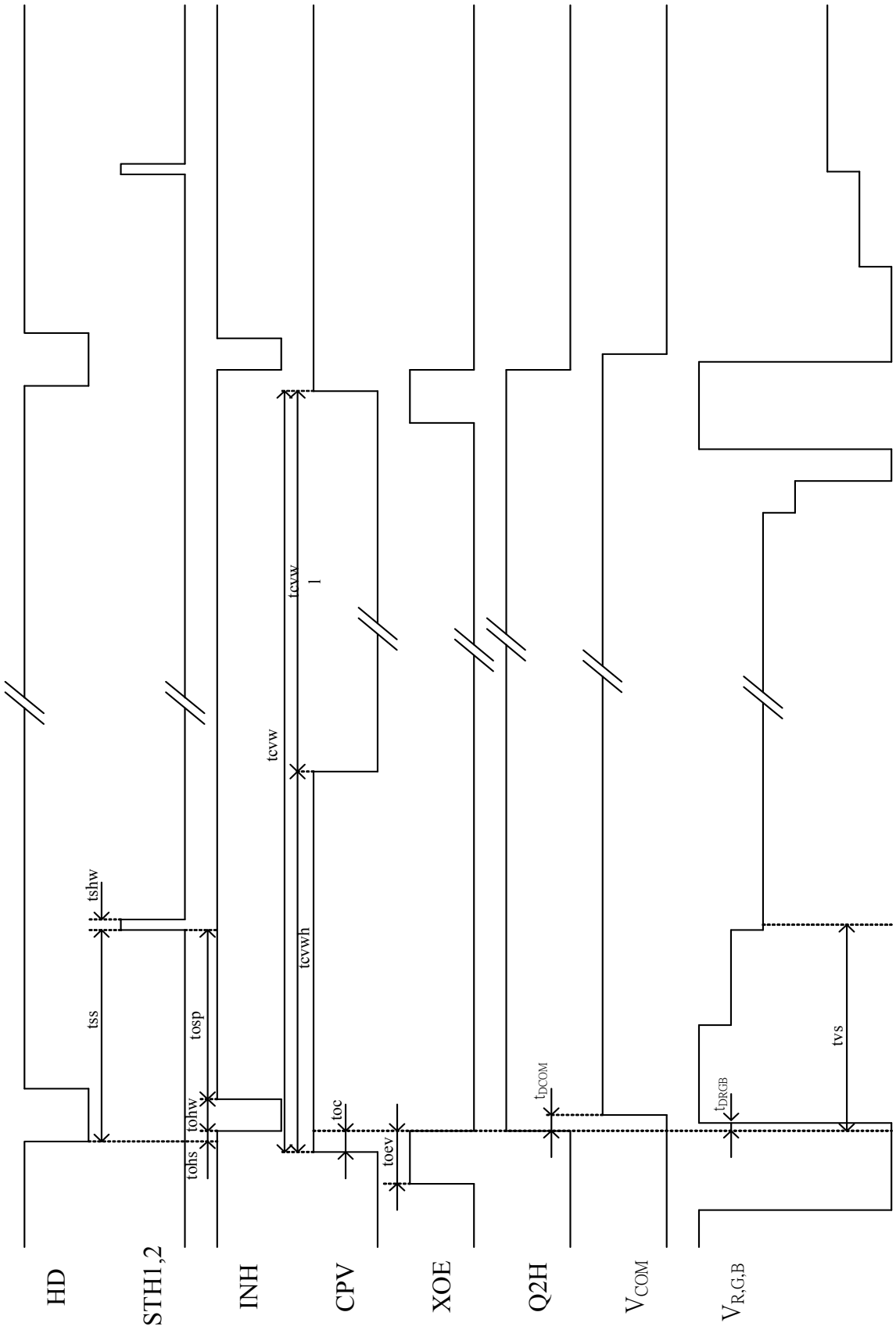

Fig. 8-4 Detail Horizontal Timing

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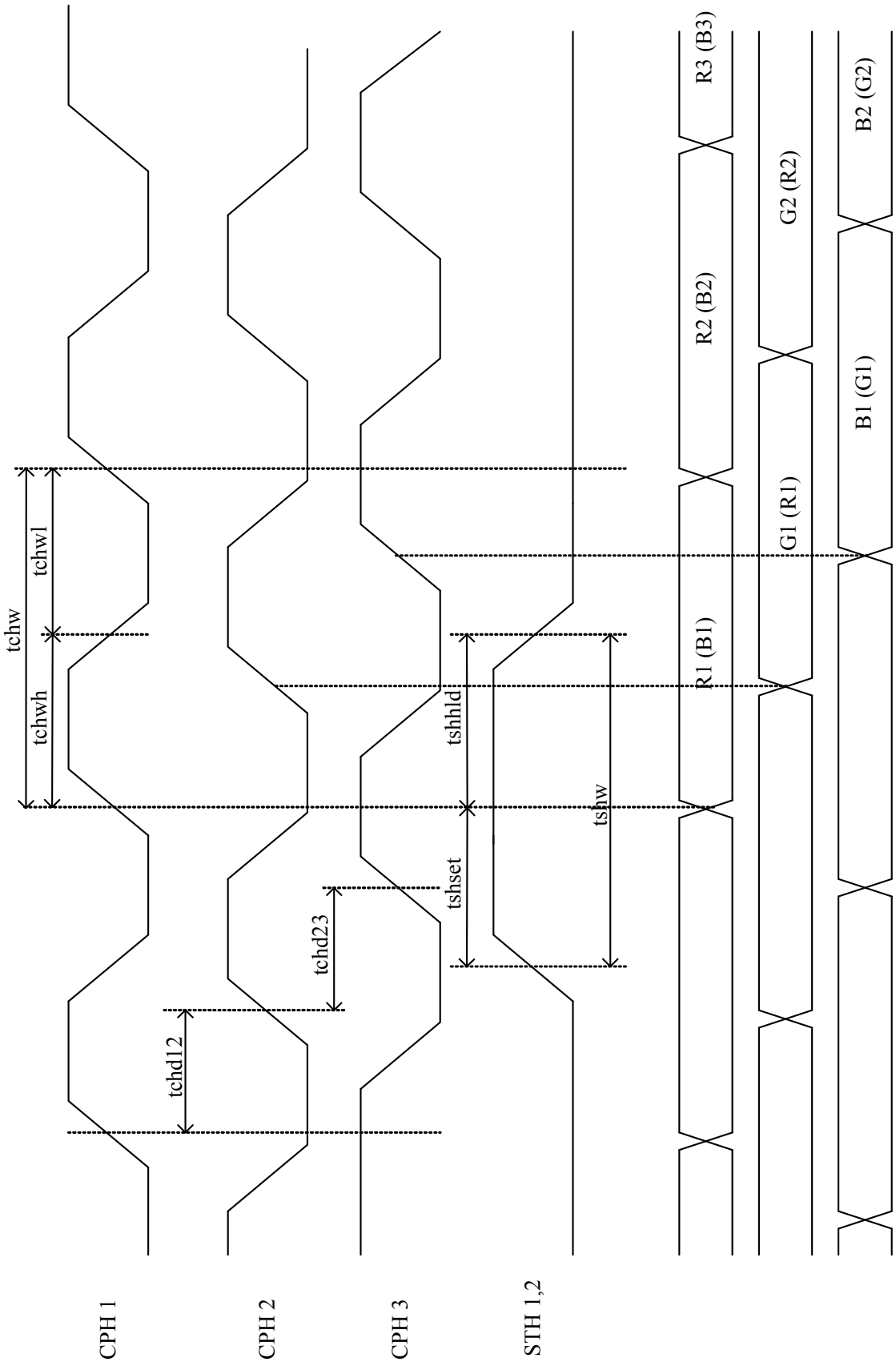

Fig. 8-5 Sampling Clock Timing

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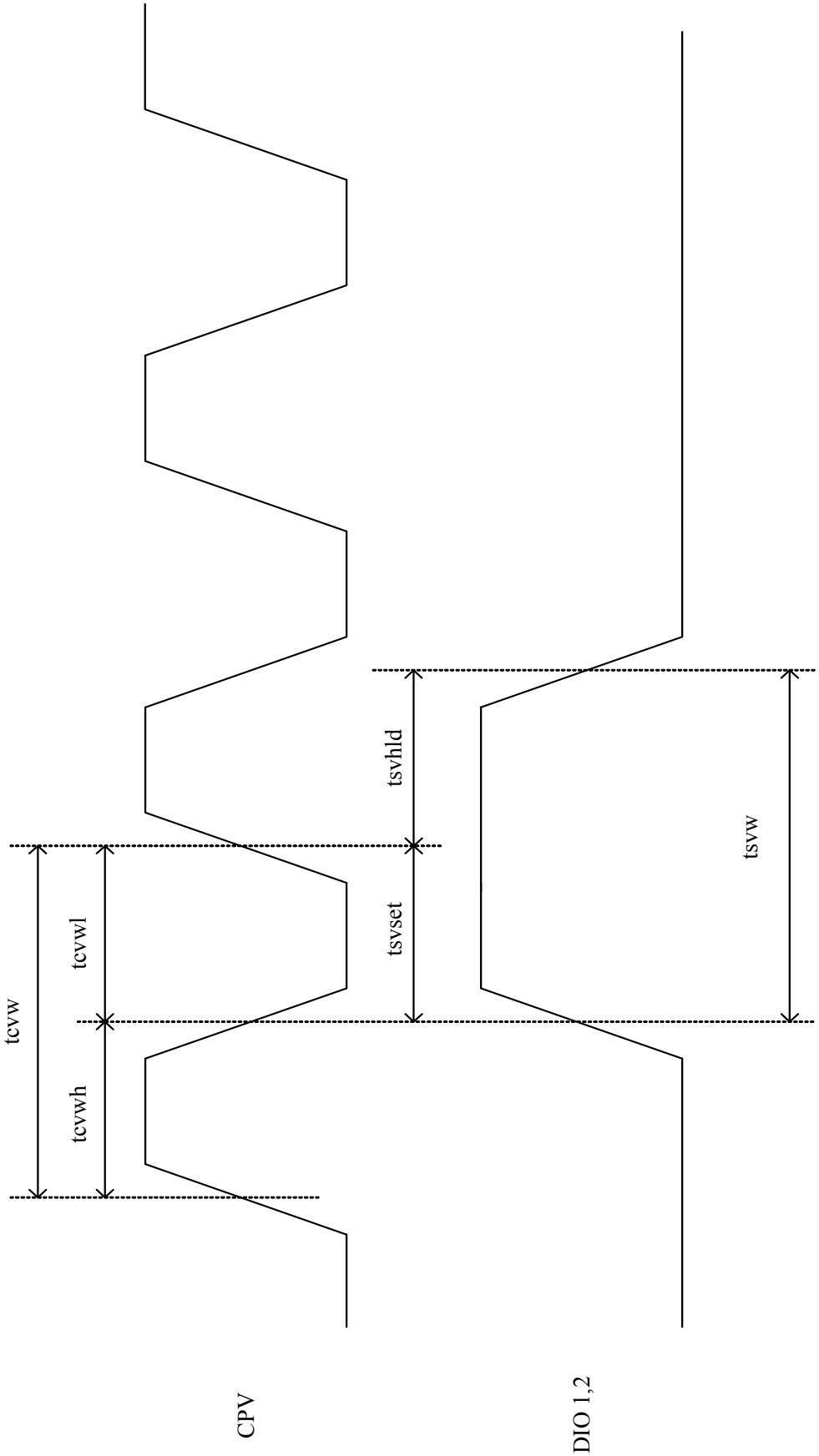

Fig. 8-6 Vertical Shift Clock Timing

Vertical timing (From up to down)

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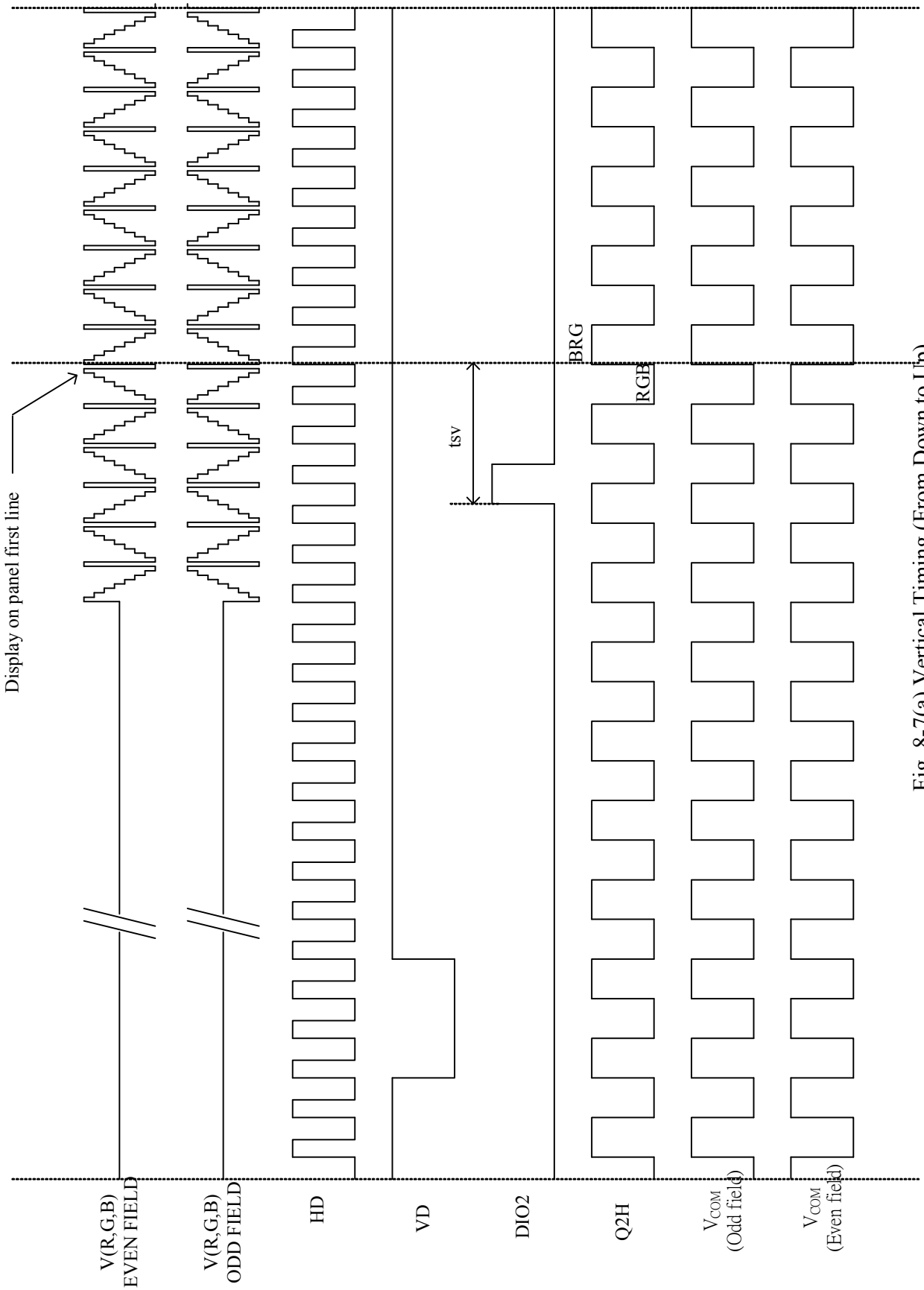

Fig. 8-7(a) Vertical Timing (From Down to Up)

Vertical timing (From down to up)

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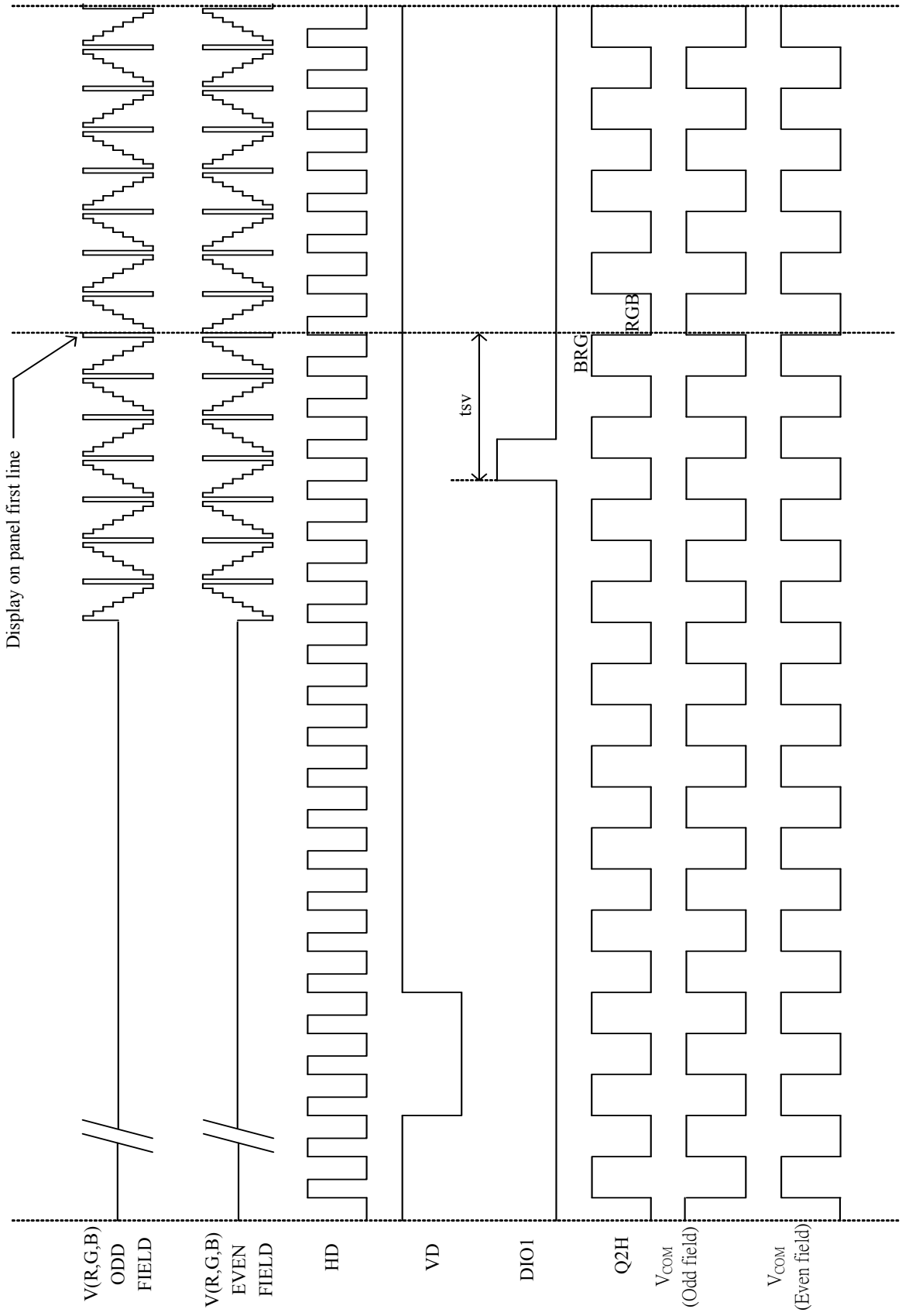

Fig. 8-7(b) Vertical Timing (From Up to Down)

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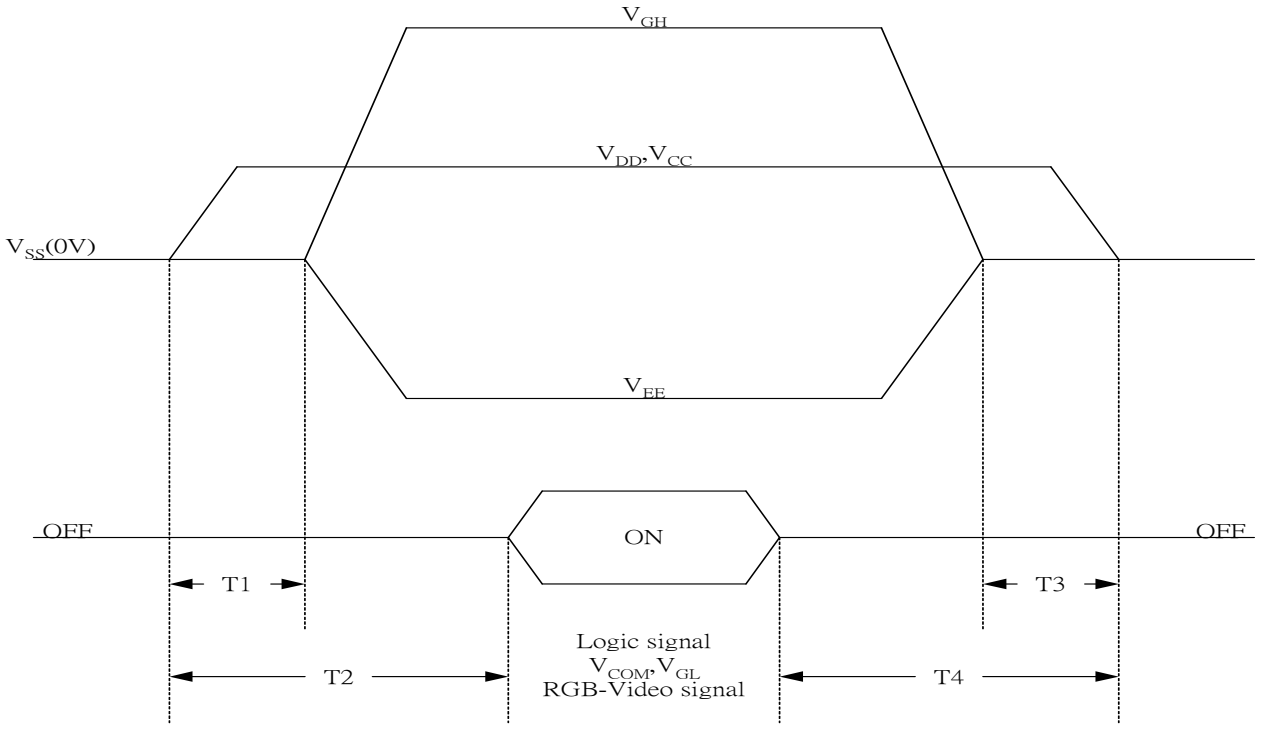
9. Power on Sequence(Voltage source)

The Power on Sequence only effect by V_{CC},V_{SS},V_{DD},V_{EE} and $V_{GH,}$ the others do not care.



1) 10ms≦T1<T2

2) 0ms<T3≦T4≦10ms

10. Optical Characteristics

10-1) Specification

Ta = 25℃

Parameter		Symbol	Condition	MIN.	TYP.	MAX.	Unit	Remarks
Viewing Angle	Horizontal	θ21, θ22	CR≧10	±45	±50	-	deg	Note 10-1
	Vertical	θ (to 12 o'clock)		10	15	-	deg	
		θ (to 6 o'clock)		30	35	-	deg	
Contrast Ratio		CR	At optimized Viewing angle	200	350	-		Note 10-2
Response time	Rise	Tr	θ=0°	-	15	30	ms	Note 10-3
	Fall	Tf		-	25	50	ms	
Uniformity		U	9 point	70	75			Note 10-4
Brightness			θ=0°	200	250		cd/㎡	
White Chromaticity		X	θ=0°	0.280	0.310	0.340		
		y		0.300	0.330	0.360		
LED Life Time		-	20mA	-	30000	-	hr	

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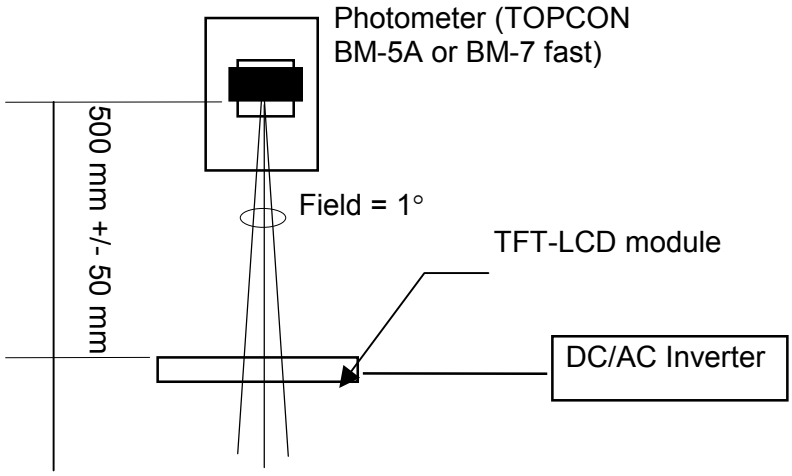
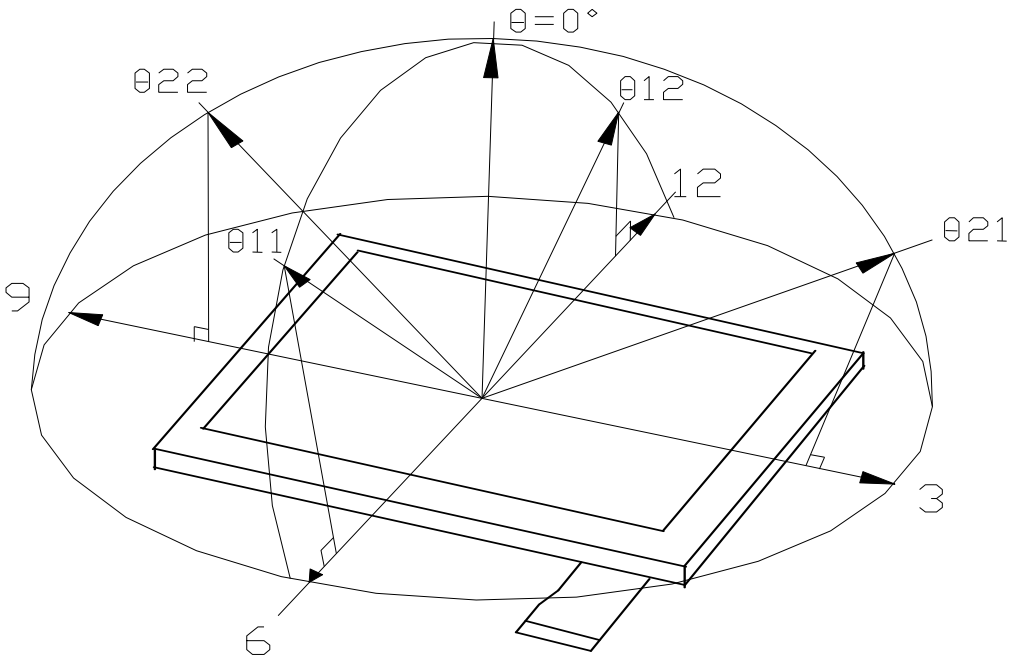


Optical characteristics measuring configuration

1.Topcon BM-7(fast) luminance meter 1.0°field of view is used in the testing (after 5 minutes operation).
2. LED current : 20 mA .
3.Conditions:The brightness of center point becomes 50% of initial brightness.

Note 10-1：The definition of viewing angle diagrams :



Note 10-2 : CR = $\frac{\text{Luminance when Testing point is White}}{\text{Luminance when Testing point is Black}}$

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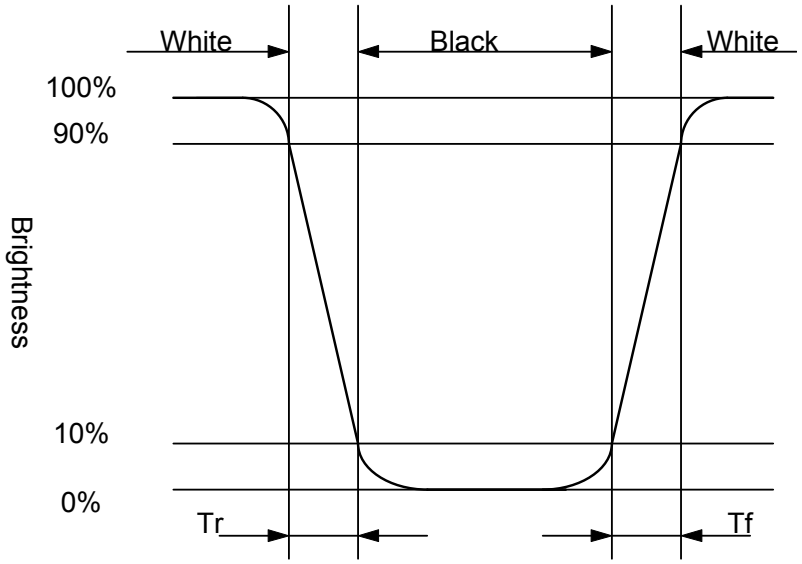
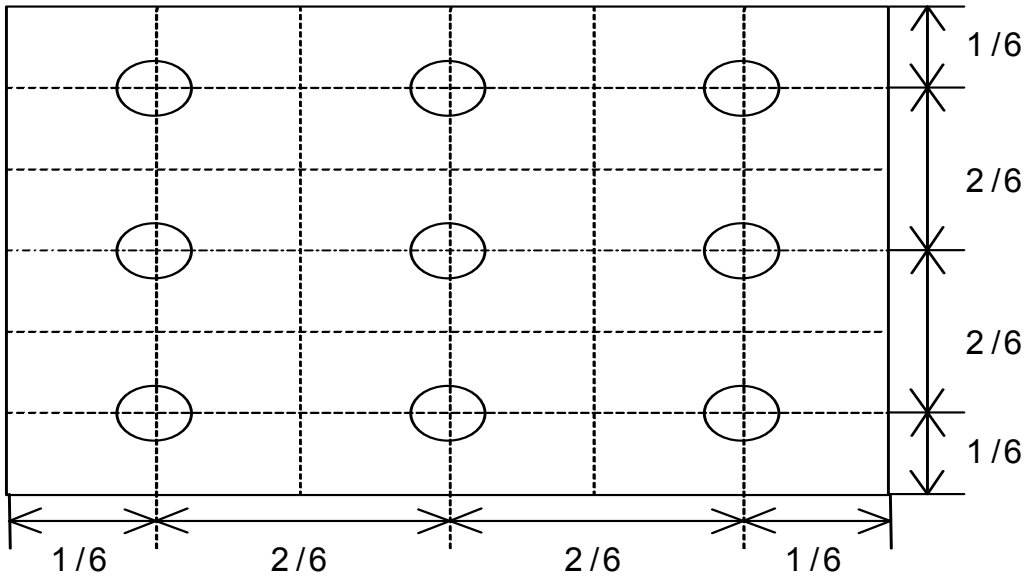
Note 10-3 : The definitions of response time:



Note 10-4: The uniformity of LCD is defined as

$$U = \frac{\text{The Minimum Brightness of the 9 testing Points}}{\text{The Maximum Brightness of the 9 testing Points}}$$

Luminance meter : BM-5A or BM-7 fast(TOPCON)
Measurement distance : 500 mm +/- 50 mm
Ambient illumination : < 1 Lux
Measuring direction : Perpendicular to the surface of module
The test pattern is white (Gray Level 63).



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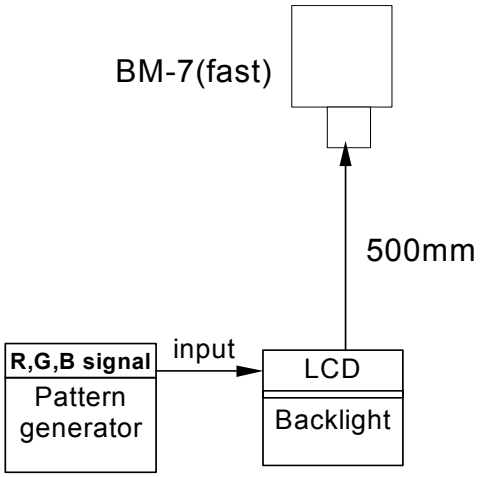
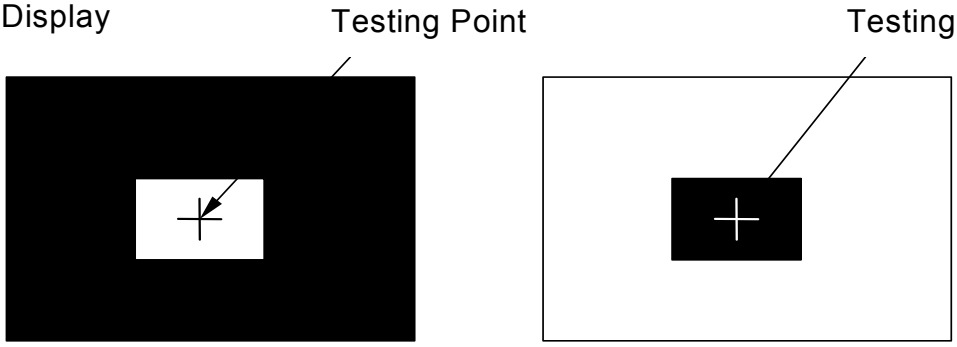
10-2) Test Configuration



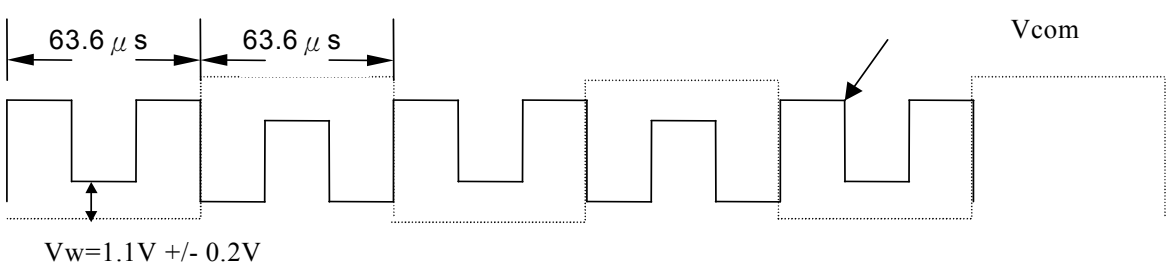
Caution: 1. Environmental illumination ≦ 1 lux
2. Before test CR, Vcom voltage must be adjusted carefully to get the best CR.

- LCD Display

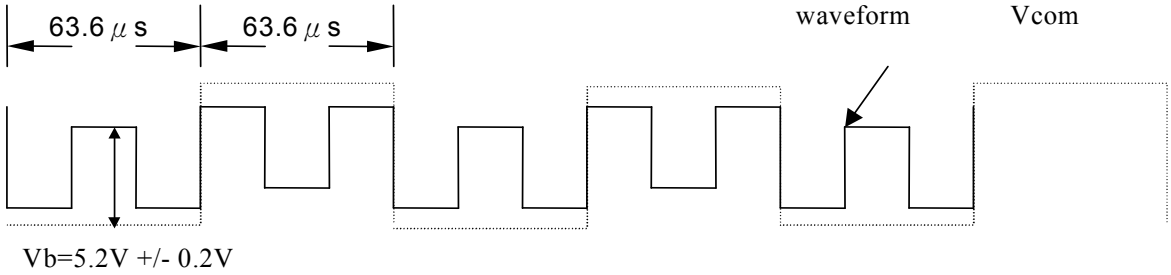


Pattern A Pattern B

- R, G, B Waveform of Pattern A at Testing Point



- R, G, B Waveform of Pattern B at Testing Point



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11. Handling Cautions

11-1) Mounting of module

a) Please power off the module when you connect the input/output connector.

0. Please connect the ground pattern of the backlight power circuit and case surely. If the connection is not perfect, some following problems may happen possibly.

1. The noise from the backlight unit will increase.

1. The output from power circuit will be unstable.
2. In some cases a part of module will heat.
3. Polarizer which is made of soft material and susceptible to flaw must be handled

carefully.

b) Protective film (Laminator) is applied on surface to protect it against scratches and dirt. It is recommended to peel off the laminator before use and taking care of static electricity.

11-2) Precautions in mounting

a) When metal part of the TFT-LCD module (shielding lid and rear case) is soiled, wipe it with soft dry cloth.
b) Wipe off water drops or finger grease immediately. Long contact with water may cause discoloration or spots.
c) TFT-LCD module uses glass which breaks or cracks easily if dropped or bumped on hard surface. Please handle with care.
d) Since CMOS LSI is used in the module. So take care of static electricity and earth yourself when handling.

11-3) Adjusting module

a) Adjusting volumes on the rear face of the module have been set optimally before shipment.
b) Therefore, do not change any adjusted values. If adjusted values are changed, the specifications described may not be satisfied.

11-4) Others

a) Do not expose the module to direct sunlight or intensive ultraviolet rays for many Hours.
b) Store the module at a room temperature place.
c) The voltage of beginning electric discharge may over the normal voltage because of leakage current from approach conductor by to draw lump read lead line around.
d) If LCD panel breaks, it is possibly that the liquid crystal escapes from the panel. Avoid putting it into eyes or mouth. When liquid crystal sticks on hands, clothes or feet. Wash it out immediately with soap.
e) Observe all other precautionary requirements in handling general electronic components.
f) Please adjust the voltage of common electrode as material of attachment by 1 module.

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12. Reliability Test

No.	Test Item	Test Condition
1	High Temperature Storage Test	Ta = +70 ℃, 240 hrs
2	Low Temperature Storage Test	Ta = -30℃, 240 hrs
3	High Temperature Operation Test	Ta = +70 ℃, 240 hrs
4	Low Temperature Operation Test	Ta = -20℃, 240 hrs
5	High Temperature & High Humidity Operation Test	Ta = +60℃, 90%RH, 240 hrs
6	Thermal Cycling Test (non-operating)	-20℃ →+70℃, 200 Cycles 30 min 30 min
7	Vibration Test (non-operating)	Frequency：10 ~ 55 H_Z Amplitude：1.0 mm Sweep time: 11 mins Test Period: 6 Cycles for each direction of X, Y, Z
8	Shock Test (non-operating)	100G, 6ms Direction: ±X, ±Y, ±Z Cycle: 3 times
9	Electrostatic Discharge Test (non-operating)	±200V, 0Ω 200pF, 1 time / each terminal

Ta: ambient temperature
Note : The protective film must be removed before temperature test.

[Criteria]

1. In the standard conditions, there is not display function NG issue occurred. (including :line defect ,no image) All the cosmetic specification is judged before the reliability stress

2. After the vibration and shock test , can't be find chip broken.

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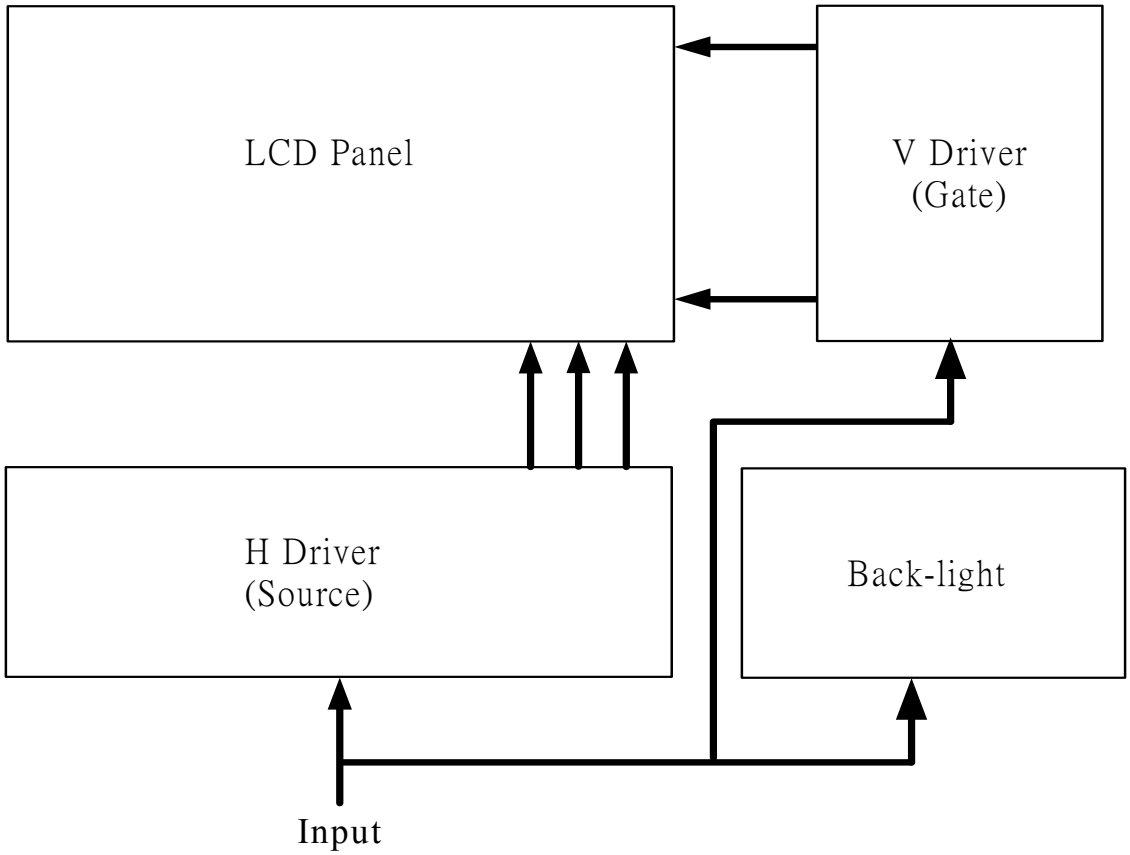
13 Block Diagram

131 LCD Module Diagram



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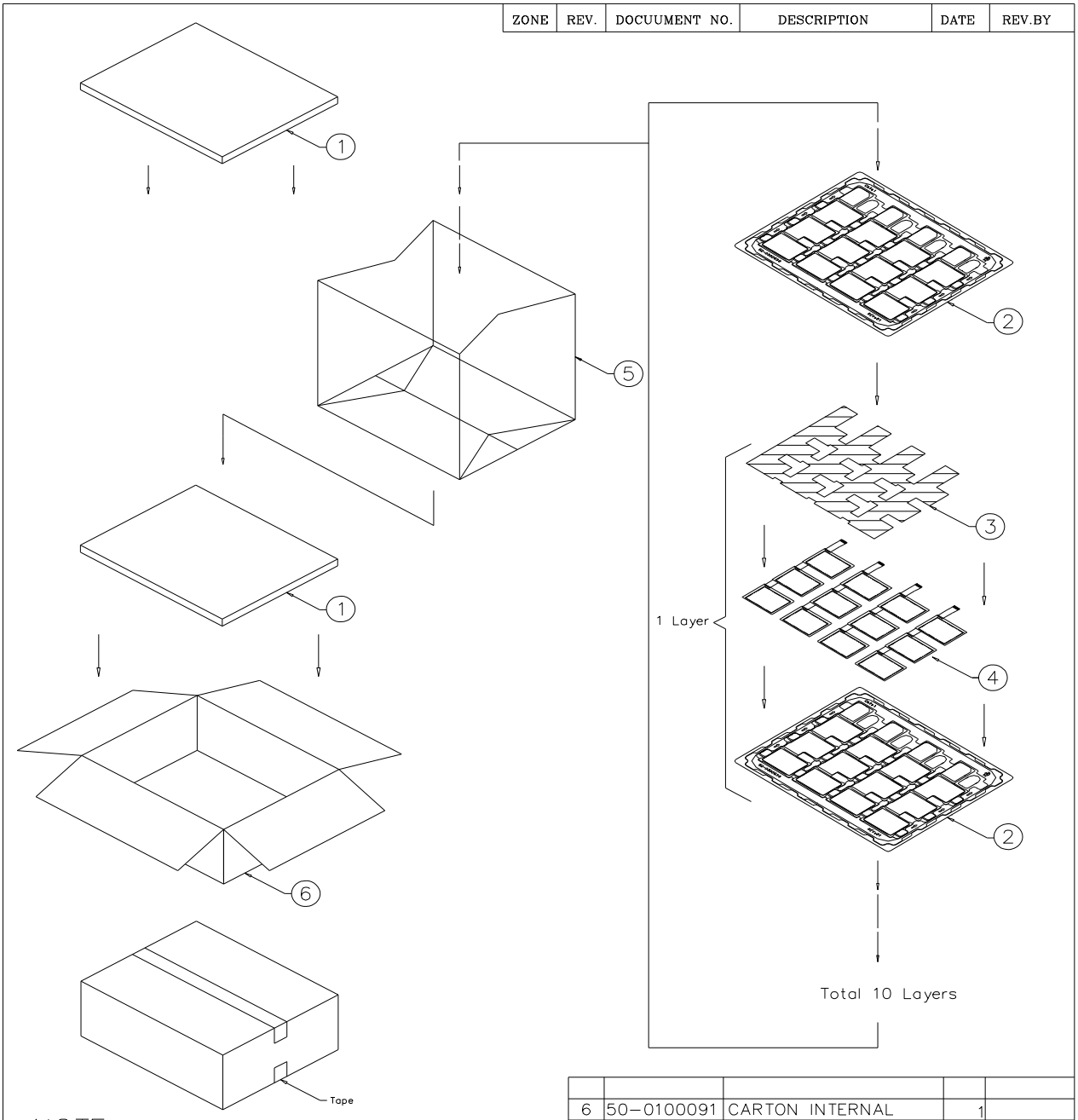
14 Packing

ZONE	REV.	DOCUUMENT NO.	DESCRIPTION	DATE	REV.BY



NOTE:
1. One layer include: 1 piece of cushion sheet, 12pcs panel & 1 piece of tray.
2. Q'TY: 120 pcs panel/carton.
3. Dimension: 455*375*190mm
4. Weight: 8.3 KG

ITEM	PART NO.	DESCRIPTION	QTY	REMARK
6	50-0100091	CARTON INTERNAL	1	
5	50-0500041	摺口袋450*380*700mm	1	抗靜電
4		PA035XSL	120	
3	50-0200018	EPE CUSHION SHEET	10	抗靜電
2	50-0300533	TRAY	11	抗靜電
1	50-0300491	EPE FOAM	2	

MTL.SPEC.	UNSPECIFIED TOL'S	REMARK	元太科技股份有限公司 Prime View Internation Co.,ltd.
	ANGLE		
	ROUGHNESS		

			SCALE	UNIT	SHEET	DWG.TITLE
APPROVE	Frank Shin	'05.06.06	1:1	mm	1 OF 2	PA035XSLN1 PACKING Dim
CHECK	Frank Shin	'05.06.06	MTL.NO.		DWG.NO.	REV. 01 / A4 SIZE
DESIGN	Dennis	'05.06.06				

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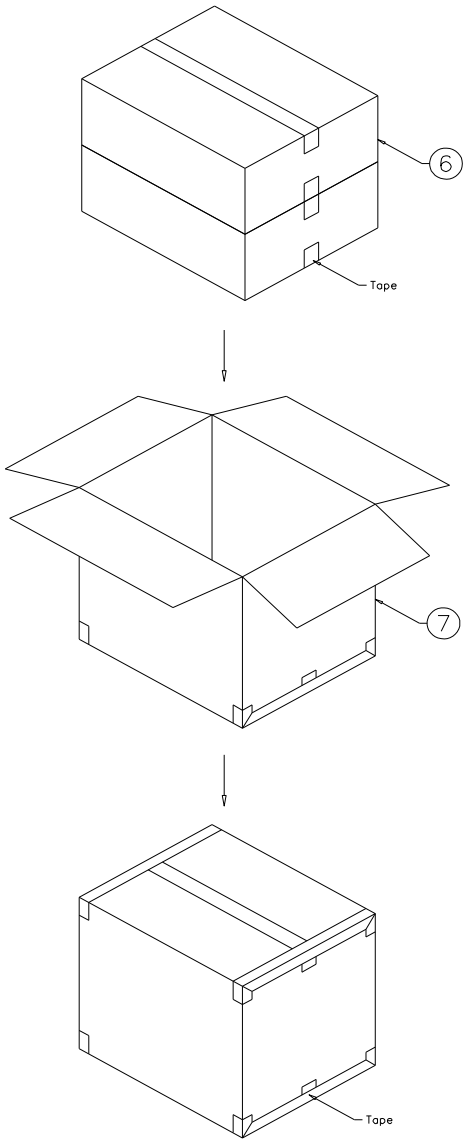
ZONE	REV.	DOCUUMENT NO.	DESCRIPTION	DATE	REV.BY



NOTE:

1.Q'TY: 240 pcs panel/carton.
2.Dimension: 480*396*405mm
3.Weight: 17.5 KG

ITEM	PART NO.	DESCRIPTION	QTY	REMARK
7	50-0100101	CARTON EXTERNAL	1	

MTL.SPEC.	UNSPECIFIED TOL'S ANGLE ROUGHNESS	REMARK	元太科技股份有限公司 Prime View Internation Co.,ltd.

			SCALE	UNIT	SHEET	DWG.TITLE
APPROVE	Frank Shin	'05.06.06	1:1	mm	2 OF 2	PA035XSLN1 PACKING Dim
CHECK	Frank Shin	'05.06.06				
DESIGN	Dennis	'05.06.06	MTL.NO.		DWG.NO.	REV. 01 / A4 SIZE

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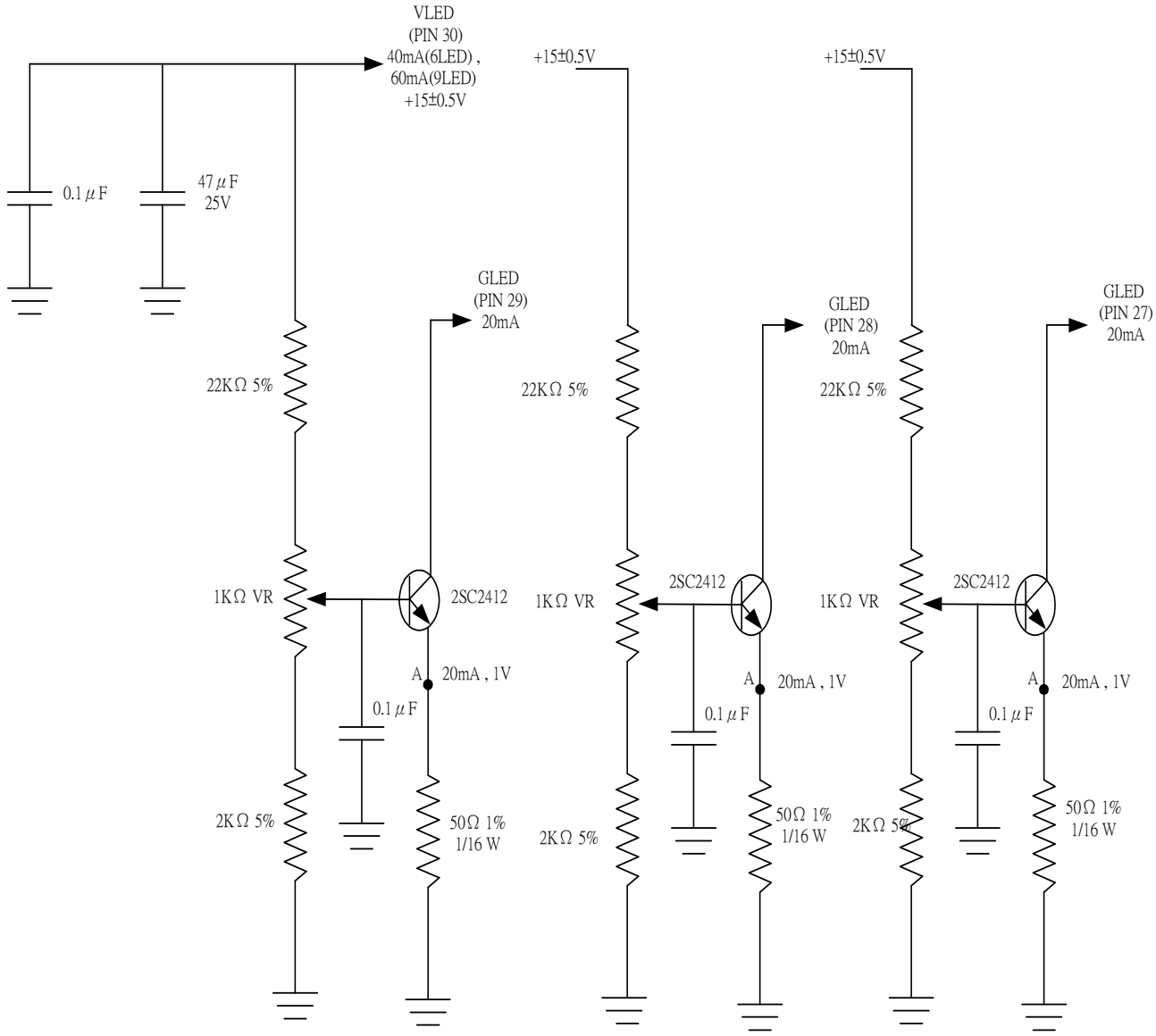
Appendix



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