

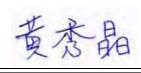
Version : <u>1.0</u>

TECHNICAL SPECIFICATION MODEL NO: PD050QT2

The content of this information is sub	ject to be changed without notice.
Please contact PVI or its agent for fu	rther information.
-	
Customer's Confirmation	
Customer	
Date	
	•
D	
Ву	
☐PVI's Confirmation	

Confirmed By

Prepared By







PD050QT2

Revision History

Rev.	Eng.	Issued Date	Revised Contents
0.1	Sarah	July 09, 2007	Preliminary
1.0	Sarah	Oct 24, 2007	Add Page 13
			12. Optical Characteristics data



TECHNICAL SPECIFICATION CONTENTS

NO.	ITEM	PAGE
-	Cover	1
-	Revision History	2
-	Contents	3
1	Application	4
2	Features	4
3	Mechanical Specifications	4
4	Mechanical Drawing of TFT-LCD module	5
5	Input / Output Terminals	6
6	Pixel Arrangement	7
7	Absolute Maximum Ratings	7
8	Electrical Characteristics	8
9	Display Color and Gray Scale Reference	9
10	Interface Timing	10
11	Power On Sequence	13
12	Optical Characteristics	13
13	Handling Cautions	16
14	Reliability Test	17
15	Block Diagram	18
16	Packing	19



1. Application

This data sheet applies to a color TFT LCD module, PD050QT2. This module applies to OA product, car TV (must use Analog to Digital driving board), which requires high quality flat panel display. If you must use in severe reliability environment, please don't extend over PVI's reliability test conditions.

2. Features

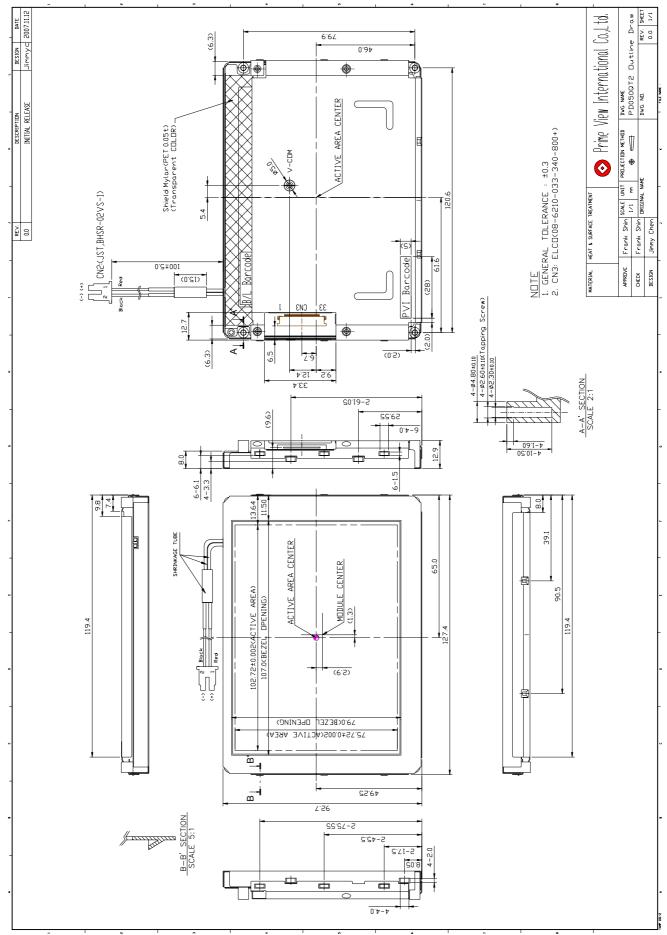
- . Amorphous silicon TFT LCD panel with LED back-light unit
- . Pixel in stripe configuration
- . Slim and compact, designed for O/A application
- . TTL transmission interface

3. Mechanical Specifications

Parameter	Specifications	Unit
Screen Size	5 (diagonal)	inch
Display Format	320 XRGB) ×240	dot
Active Area	102.72 (H) ¾5.72 (V)	mm
Pixel Pitch	0.3210(H) × 0.3155 (V)	mm
Pixel Configuration	Stripe	
Display Colors	262K	
Surface Treatment	Anti-Glare +SWV	
Back-light	24-LED	
Outline Dimension	127.4 (W) >92.7 (H) ≥ 12.9 (D)(typ.)	mm
Weight	172±10	g
Display mode	Normally white	
Gray scale inversion direction	6 (ref to Page 13 viewing angle)	o'clock



4. Mechanical Drawing of TFT-LCD Module





5. Input / Output Terminals

5-1) TFT-LCD Panel Driving

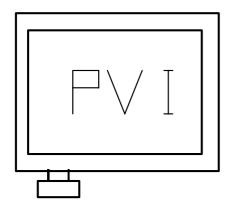
Connector type: ELCO 08-6210-033-340-800A+, PIN No 33 pins, pitch=0.5mm

Pin No	Symbol	I/O	Description	Remark
1	GND	1	GND	
2	CK		Clock signal for sampling each data signal	
3	Hsync	I	Horizontal synchronous signal(negative)	
4	Vsync	I	Vertical synchronous signal(negative)	
5	GND	-	GND	
6	R0		RED data signal(LSB)	
7	R1	-	RED data signal	
8	R2	I	RED data signal	
9	R3	-	RED data signal	
10	R4	_	RED data signal	
11	R5	I	RED data signal(MSB)	
12	GND	-	GND	
13	G0	ı	GREEN data signal(LSB)	
14	G1	I	GREEN data signal	
15	G2	_	GREEN data signal	
16	G3		GREEN data signal	
17	G4		GREEN data signal	
18	G5	ı	GREEN data signal(MSB)	
19	GND	-	GND	
20	В0	_	Blue data signal(LSB)	
21	B1		Blue data signal	
22	B2		Blue data signal	
23	В3	I	Blue data signal	
24	B4	ı	Blue data signal	
25	B5	-	Blue data signal(MSB)	
26	GND	-	GND	
27	ENAB	I	Signal to settle the horizontal display position(positive)	Note5-1
28	V _{CC}	-	+3.3V power supply	
29	V_{CC}	-	+3.3V power supply	
30	R/L	-	Horizontal display mode select signal	Note5-2
	IVL	•	H:Normal ,L:Left /Right reverse mode	110100 2
31	U/D	1	Vertical display mode select signal	Note5-3
		•	L: Normal ,H: Up/Down reverse mode	1.0.000
32	N/C	-	NC	
33	GND	-	GND	

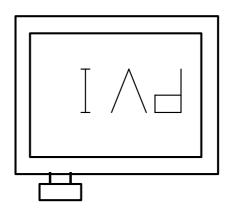
Note5-1: The horizontal display start timing is settled in accordance with rising of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in 10-2. Don't keep ENAB "High" during operation.



Note 5-2/5-3: The definitions of U/D & R/L







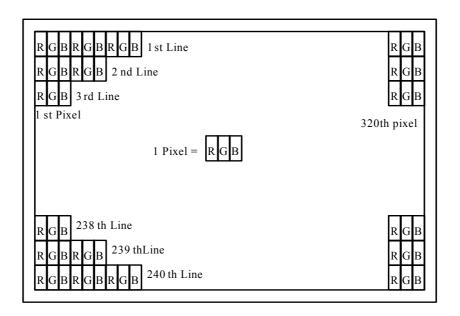
R/L(PIN 30) = High , U/D(PIN 31) = Low

5-2) Backlight driving

Connector type: JST BHSR-02VS-1, PIN No 2 pin

Pin No	Symbol	Description	Remark
1	+	Input terminal (Anode)	Wire color : Red
2	-	Input terminal (Cathode)	Wire Color : Black

6. Pixel Arrangement



7. Absolute Maximum Ratings:

GND=0V, Ta=25℃

Parameters	Symbol	Condition	MAX.	Unit	Remark
Input Voltage	Vı	Ta=25℃	-0.3~+4	V	Note7-1
+3.3V supply voltage	V_{CC}	Ta=25°C	0~+3.6	V	

Note 7-1:CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync,ENAB,R/L,U/D

PD050Q



8. Electrical Characteristics

8-1) Recommended Operating Conditions:

GND=0V, Ta=25°C

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Supply Voltage	V_{CC}	+3.0	+3.3	+3.6	V	
Current Dissipation	I _{cc}	-	42.8	-	mA	Note 8-1
Total power consumption	Pcc	128.4	141.24	154.08	mW	
Permissive input ripple voltage	V_{RF}	-	-	100	mVp-p	V _{CC} =3.3V
Input voltage(Low)	V_{IL}	0	-	0.3 V _{CC}	V	
Input voltage(High)	V_{IH}	0.7 V _{CC}	-	V_{CC}	V	
V _{com} Voltage	V_{com}	-	3.55	-	V	

Note 8-1: To test the current dissipation of V_{CC} , using the "color bars" testing pattern shown as below.

1	2	3	4	5	6	7	8
---	---	---	---	---	---	---	---

I_{DD} current dissipation testing pattern

- White
- 2. Yellow

- Cyan
 Green
 Magenta
 Red
- 7. Blue
- **Black**

8-2) Recommended Driving Condition for Back Light

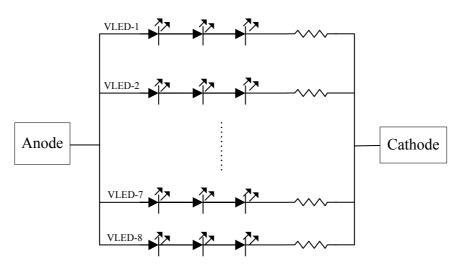
GND = 0 V , Ta = 25°C

Parameter	Symbol	Min	TYP	MAX	Unit	Remark
Supply voltage of LED backlight	V _{LED1~8}	-	-	(11.0)	V	Note 8-2
Supply current of LED backlight	I _{LED1~8}	-	20	-	mA	Note 8-3
Backlight Power Consumption	P _{LED}	-	-	1.76	W	Note 8-4

Note 8-2: ILED1~8=20mA, Constant Current

Note 8-3: The LED driving condition is defined for each LED module. (3 LED Serial) Input current = 20mA * 8 = 160mA

Note 8-4 :
$$P_{LED-1} * I_{LED-1} * I_{LED-2} * I_{LED-2} * I_{LED-2} * I_{LED-7} * I_{LED-8} * I_{LE$$







8-3) Power Consumption:

Parameters	Symbol	Max.	Unit	Remark
LCD Panel Power Consumption	-	0.16	W	Note 8-5
Backlight Power Consumption	-	1.76	W	Note 8-6
Total Power Consumption	-	1.92	W	

Note 8-5: The power consumption for backlight is not included. Note 8-6: Back light power consumption is calculated by $I_L \times I_L$.

9. Display Color and Gray Scale Reference

									npu	t Co	lor	Data	ì						
C	olor			Re	ed					Gre	een					BI	ue		
Plack		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Colors	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red (02)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker						_												
Red	<u> </u>	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	↓	\downarrow											
	Brighter																		
	Red (61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green (02)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	Darker																		
Green	\downarrow	↓	↓	↓	\downarrow														
	Brighter																		
	Green (61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue (02)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	Darker																		
Blue	\downarrow	↓	\downarrow	\downarrow															
	Brighter																		
	Blue (61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1



10. Interface Timing

10-1) Timing Parameters

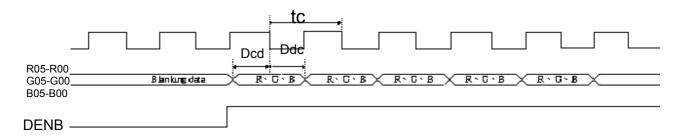
AC Electrical Characteristics (V_{CC} =+3.3V,GND=0V,Ta=25 $^{\circ}$ C)

		Symbol	Min.	Тур.	Max.	Unit
Power supply		VCC	3.0	3.3	3.6	V
CLK		1/tc	-	6.3	7.0	MHz
	Frequency	tc	-	158	-	ns
	Period	Нр	-	63.6	-	us
	reliou		-	400	-	tc
	Display period	Hdp	-	320	-	tc
HSYNC	Pulse width	Hpw	5	30	-	tc
HOTING	Back-porch	Hbp	-	38	-	tc
	Front-porch	Hfp	-	12	-	tc
	Hpw+Hbp		-	68	-	tc
	Hsync-CLK	Hhc	10	-	Tc-10	ns
	Period	Vp	-	16.8	-	ms
	renou		-	262	288	Нр
	Display period	Vdp	-	240	-	Нр
VSYNC	Pulse width	Vpw	-	3	-	Нр
	Back-porch	Vbp	-	15	-	Нр
	Front-porch	Vfp	-	6	-	Нр
	Vpw+Vbp		-	18	-	Нр
DENB	Horizontal scanning period	T1	-	400	-	tc
	Horizontal display period	T2	-	320	-	tc
	Vertical display period	Т3	-	240	ı	T1
	Frame cycling period	T4	-	262	288	T1
ВСВ	CLK-DATA	Dcd	10	-	-	ns
R,G,B	DATA-CLK	Ddc	8	-	-	ns

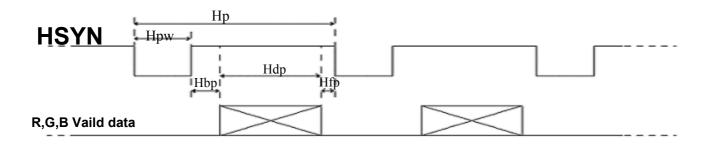


10-2) Timing Diagram

a.1 Input signal range



a.2 HSYNC timing



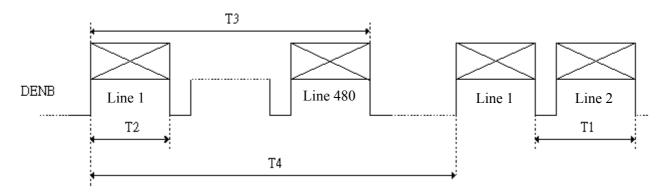
a.3 CLK, HSYNC relationship





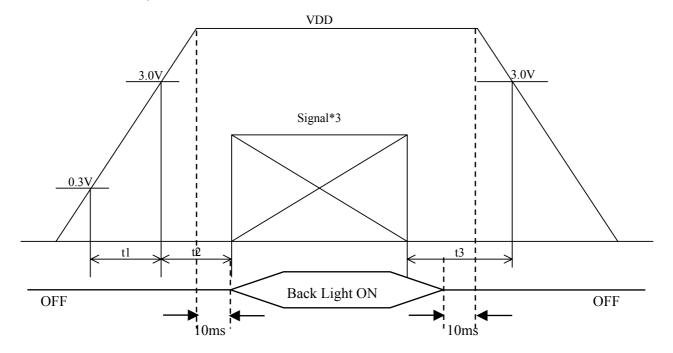
a.4 VSYNC timing Vp Vp Vdp Vdp Vdp Vdp Vdp

a.5 DENB timing





11. Power On Sequence



- 1. 0<t1≦20ms
- 2. 0<t2≦50ms
- 3. 0<t3≦1s

12. Optical Characteristics

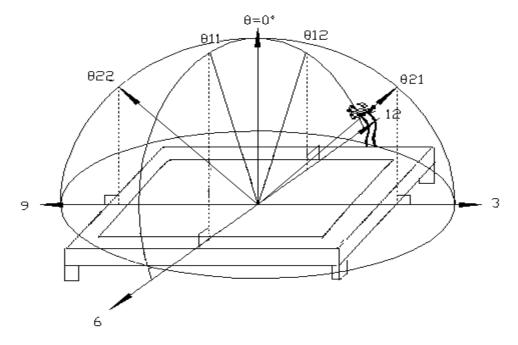
12-1) Specification:

Ta = 25℃

Parameter		Symbol	Condition	MIN.	TYP.	MAX.	Unit	Remarks
	Horizontal	θ21, θ22	CR≧10	55	60		deg	Note 12-1
Viewing Angle	Vertical	<i>θ</i> 12		35	40		deg	
		<i>θ</i> 11		50	55		deg	
Contrast Ratio		CR	At optimized Viewing angle	400	600			Note 12-2
Luminance		L	$\theta = 0^{\circ}$	500	550		cd/m²	
White Chromaticity		х	$\theta = 0^{\circ}$	0.270	0.310	0.350		Note 12-3
		у	θ =0°	0.300	0.340	0.380		
Response time	Rise	Tr	θ =0°		15	30	ms	Note 12-5
	Fall	Tf		-	25	50	ms	
Uniformity		U	-	70	80		%	Note 12-6
Cross Talk Ratio		CTK	-			3.5	%	Note 12-7
LED Life Time			₊₂₅ ℃	20000	30000		hrs	Note 12-4



Note 12-1: The definitions of viewing angles



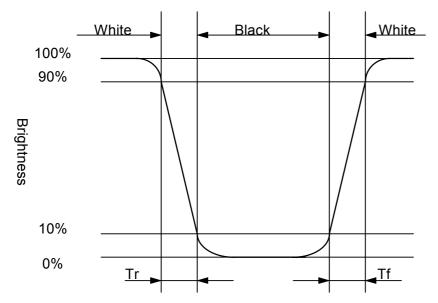
Note 12-2: CR = Luminance when Testing point is White Luminance when Testing point is Black

Contrast Ratio is measured in optimum common electrode voltage.

Note 12-3: Topcon BM-7(fast) luminance meter 1° field of view is used in the testing (after 20~30 minutes operation).

Note 12-4: The "LED Life time " is defined as the module brightness decrease to 50% original Brightness that the ambient temperature is 25° C and I_{LED} =20mA.

Note 12-5: The definition of response time:





Note 12-6: The uniformity of LCD is defined as

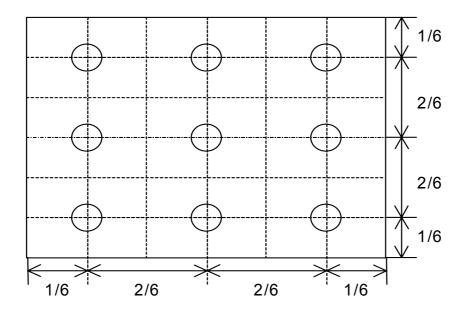
U = The Minimum Brightness of the 9 testing Points
The Maximum Brightness of the 9 testing Points

Luminance meter: BM-5A or BM-7 fast(TOPCON)
Measurement distance: 500 mm +/- 50 mm

Ambient illumination: < 1 Lux

Measuring direction: Perpendicular to the surface of module

The test pattern is white (Gray Level 63).



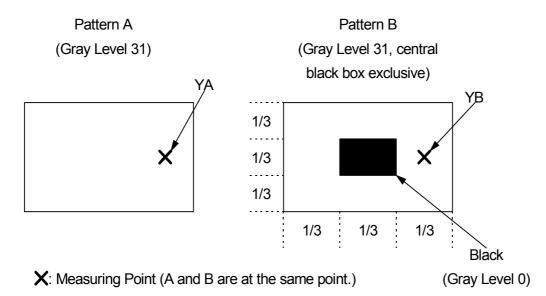
Note 12-7: Cross Talk (CTK) =
$$\frac{|YA-YB|}{YA} \times 100\%$$

YA: Brightness of Pattern A YB: Brightness of Pattern B

Luminance meter: BM 5A (TOPCON)
Measurement distance: 500 mm +/- 50 mm

Ambient illumination: < 1 Lux

Measuring direction: Perpendicular to the surface of module





PD0500



13. Handling Cautions

- 13-1) Mounting of module
 - A) Please power off the module when you connect the input/output connector.
 - B) Polarizer which is made of soft material and susceptible to flaw must be handled carefully.
 - C) Protective film (Laminator) is applied on surface to protect it against scratches and dirt. It is recommended to peel off the laminator before use and taking care of static electricity.
- 13-2) Precautions in mounting
 - A) When metal part of the TFT-LCD module (shielding lid and rear case) is soiled, wipe it with soft dry cloth.
 - B) Wipe off water drops or finger grease immediately. Long contact with water may cause discoloration or spots.
 - C) TFT-LCD module uses glass, which breaks or cracks easily if dropped or bumped on hard surface. Please handle with care.
 - D) Since CMOS LSI is used in the module. So take care of static electricity and earth yourself when handling.
- 13-3) Adjusting module
 - A) Adjusting volumes on the rear face of the module have been set optimally before shipment.
 - B) Therefore, do not change any adjusted values. If adjusted values are changed, the specifications described may not be satisfied.
- 13-4) Others
 - A) Do not expose the module to direct sunlight or intensive ultraviolet rays for many hours.
 - B) Store the module at a room temperature place.
 - C) The voltage of beginning electric discharge may over the normal voltage because of leakage current from approach conductor by to draw lump read lead line around.
 - D) If LCD panel breaks, it is possibly that the liquid crystal escapes from the panel. Avoid putting it into eyes or mouth. When liquid crystal sticks on hands, clothes or feet. Wash it out immediately with soap.
 - E) Observe all other precautionary requirements in handling general electronic components.
 - F) Please adjust the voltage of common electrode as material of attachment by 1 module.
- 13-5) Polarizer mark

The polarizer mark is to describe the direction of view angle film how to mach up with the rubbing direction.



14. Reliability Test

No	Test Item	Test Condition				
1	High Temperature Storage Test	Ta = +80°C, 240 hrs				
2	Low Temperature Storage Test	Ta = -30°C, 240 hrs				
3	High Temperature Operation Test	Ta = +70°C, 240 hrs				
4	Low Temperature Operation Test	Ta = -25℃, 240 hrs				
5	High Temperature & High Humidity Operation Test	Ta = +60°C, 90%RH, 240 hrs				
6	Thermal Cycling Test	-25°C→ +70°C, 200 Cycles				
0	(non-operating)	30 min 30 min				
		Frequency: 10 ~ 55 H _Z				
7	Vibration Test	Amplitude: 1.5 mm				
,	(non-operating)	Sweep time: 11 min				
		Test Period: 6 Cycles for each direction of X, Y, Z				
8	Shock Test	100G, 6ms				
	(non-operating)	Direction: ±X, ±Y, ±Z				
	(non-operating)	Cycle: 3 times				
9	Floetrostatic Dischargo Tost	150pF, 330Ω				
	Electrostatic Discharge Test	Air: ±15KV; Contact: ±8KV				
	(non-operating)	10 times/point, 5 points/panel face				

Ta: ambient temperature

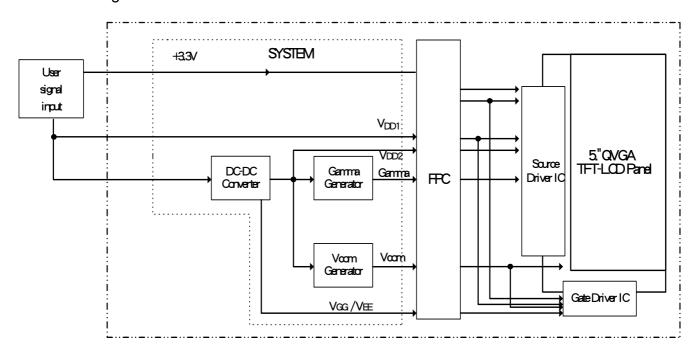
Note: The protective film must be removed before temperature test.

[Criteria]

In the standard conditions, there is not display function NG issue occurred. (Including: line defect, no image). All the cosmetic specification is judged before the reliability stress.



15. Block Diagram





16. Packing

