

Customer :

DATE : Nov. 19. 2009

*SAMSUNG TFT-LCD***MODEL NO. : LMS480JC04**Customer Approval

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*Any Modification of Spec is not allowed without SMD's permission.*

Approved by : Se chun, Oh**Samsung Mobile Display Co.**

**Revision History**

Data	Rev. No.	Page	Summary
2009.11.11	000		First issued.
2009.11.19	001	p21	Packing spec. inserted

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## General Description

### \* Description

LMS480JC04 is a transmissive type color active matrix TFT (Thin Film Transistor) LCD (Liquid Crystal Display) that uses LTPS (Low Temperature Poly-Silicon) TFT as a switching devices. This model is composed of a TFT-LCD module (TFT-LCD panel and FPC). The resolution of a 4.8" contains 1024 x RGB x 600 dots and can display up to 262k colors.

### \* Features

- LTPS SOG (System-On-Glass) technology
- 4.8 inches 15.4:9 aspect ratio
- WSVGA (1024 x RGB x 600) resolution
- 262k colors (for production only)
- LVDS interface
- TMR (Transmissive with micro-reflectance) display mode
- TN mode with wide viewing angle film
- LED backlight unit

### \* Applications

- Display terminals for Ultra mobile PC (UMPC), Potable CNS

### \* General Information

Items	Specification	Unit	Note
Display area	104.45(H) x 61.20(V)	mm	-
Driver element	LTPS TFT active matrix	-	-
Display colors	262k	colors	-
Number of dots	1024(H) x 600(V)	dots	-
Pixel arrangement	RGB land	-	-
Dot size	34 x 102	um	-
Display mode	Normally white, TMR mode	-	-
Viewing Direction	12	o'clock	-

### \* Mechanical Information

Item		Min.	Typ.	Max.	Unit	Note
Model size	Horizontal(H)	111.75	112.05	112.35	mm	with Component
	Vertical(V)	72.96	73.26	73.56	mm	with Component
	Depth(D)	4.78	4.98	5.18	mm	with Component
Weight		-	51	56	g	w/o TSP, w Component

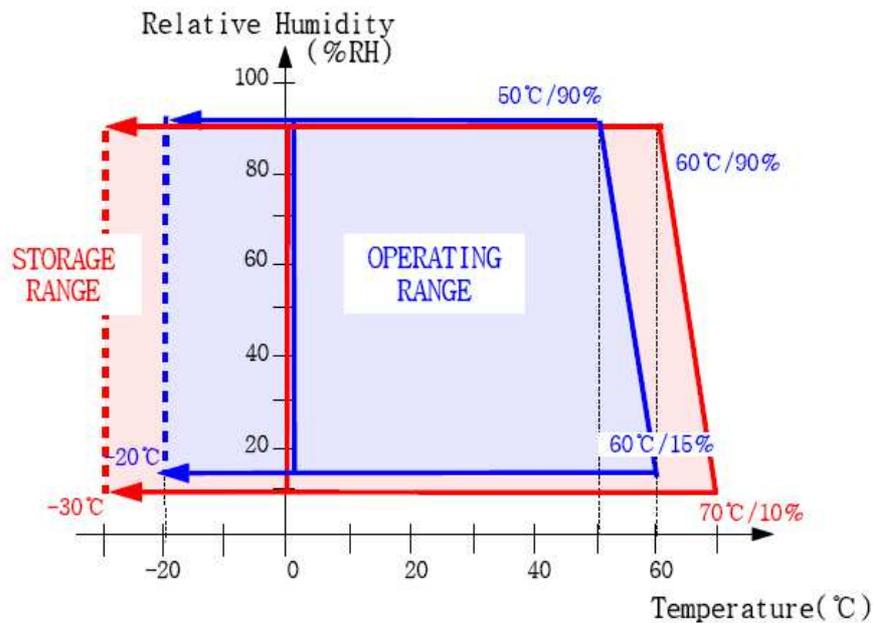
## 1. Absolute Maximum Ratings

### 1.1 Absolute Ratings of Environment

Item	Symbol	Min.	Max.	Unit	Note
Storage temperature	T <sub>STG</sub>	-30	70	°C	(1)
Operating temperature (Ambient temperature)	T <sub>OPR</sub>	-20	60	°C	(1), (2)

Note (1) 90%RH maximum humidity, 60°C maximum wet-bulb temperature

(2) When operated at a temperature lower than 0°C, the LCD worked slowly and the screen slowed appearing dim images due to the characteristics of LC (Liquid Crystal).



Temperature & Humidity Graph at Absolute Environment

## 1.2 Electrical Absolute Ratings

### (1) TFT-LCD Module

(Ta = 25 ± 2°C, VSS=GND=0V)

Characteristics	Symbol	Min.	Max.	Unit	Note
External power supply	VDD	2.2	3.6	V	

### (2) Back-light Unit

(Ta = 25 ± 2°C)

Characteristics	Symbol	Min.	Max.	Unit	Note
Current	I <sub>B</sub>	–	25	mA	(1)

Note (1) Permanent damage to the device may occur if maximum values are exceeded or reverse voltage is loaded. Functional operation should be restricted to the conditions described under normal operating conditions.

## 2. Optical Characteristics

### 2.1 Transmissive Mode

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room or equivalent state with the methods shown in Note (2).

Measuring equipment: BM-5A, BM-7, PR-650, EZ-Contrast

(Ta = 25 ± 2°C)

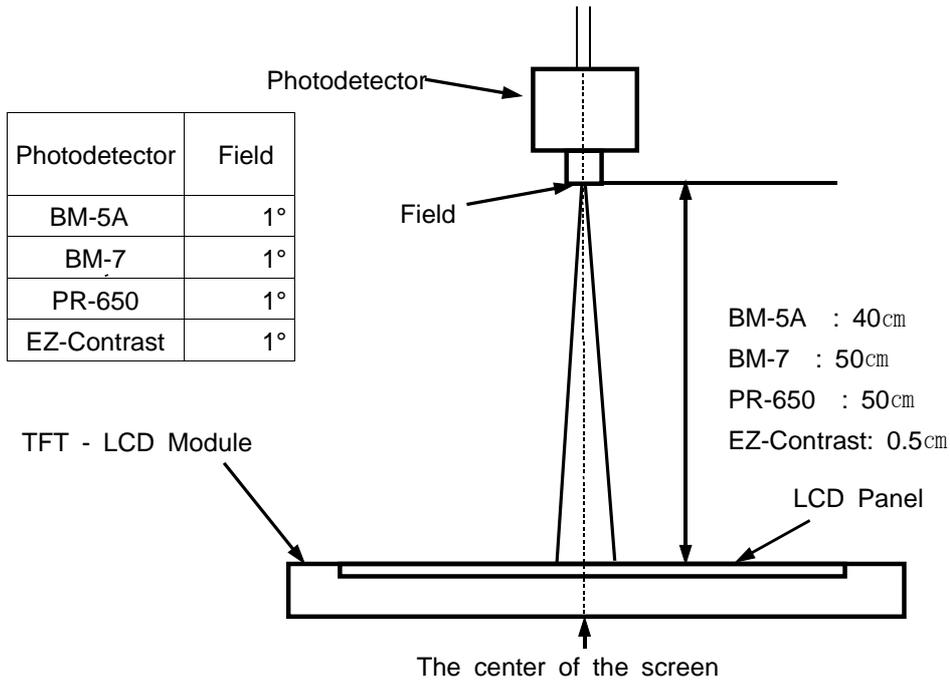
Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast ratio (Center point)	C/R		300	350	–	–	(3) BM-5A
Luminance of white (Center point)	YL	with TSP	247	330	–	cd/m <sup>2</sup>	(4) BM-5A
Color Reproducibility	Cre		45	50	–	%	
Crosstalk			–	–	5	%	(8) SR-3
Response time	Rising:Tr	Tr+Tf	–	28	35	msec	(5) BM-7
	Falling:Tf						
Color chromaticity (CIE 1931)	White	Wx	0.264	0.314	0.364		(6) PR-650
		Wy	0.305	0.355	0.405		
	Red	Rx	0.532	0.582	0.632		
		Ry	0.280	0.330	0.380		
	Green	Gx	0.294	0.344	0.394		
		Gy	0.565	0.615	0.665		
	Blue	Bx	0.101	0.151	0.201		
		By	0.043	0.093	0.143		
Viewing angle	Hor.	θL	60	65	–	Degrees	(7) EZ- Contrast
		θR	60	65	–		
	Ver.	φH	45	50	–		
		φL	50	55	–		

Note (1) If product is exposed to high temperatures for extended time, there is a possibility of the polarizer film damage which could degrade the optical characteristics.

Note (2) Test Equipment Setup

After stabilizing and leaving the panel alone at a given temperature for 30min, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. 30min after lighting light source. This should be measured in the center of screen.

- Environment condition :  $T_a = 25 \pm 2 \text{ }^\circ\text{C}$
- Back-Light On condition



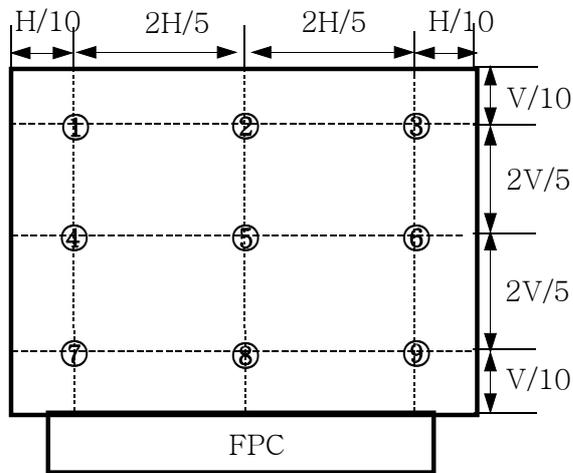
Note (3) Definition of Contrast Ratio (C/R) : Ratio of gray max (Gmax) & gray min (Gmin) at the center point of the panel.

$$C/R = \frac{G_{max}}{G_{min}}$$

\* Gmax : Luminance with all pixels white

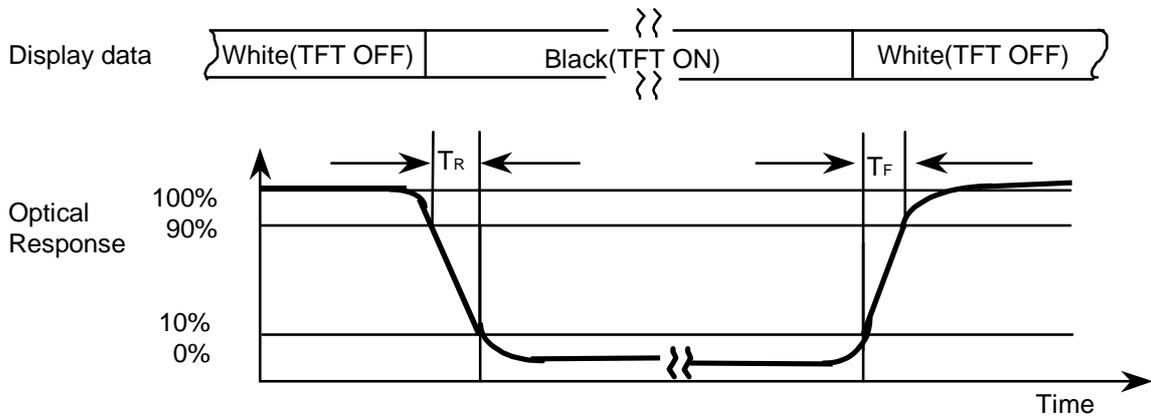
\* Gmin : Luminance with all pixels black

Note (4) Definition of Luminance of White : Luminance of white at the center point.



The spot locations for luminance measurement

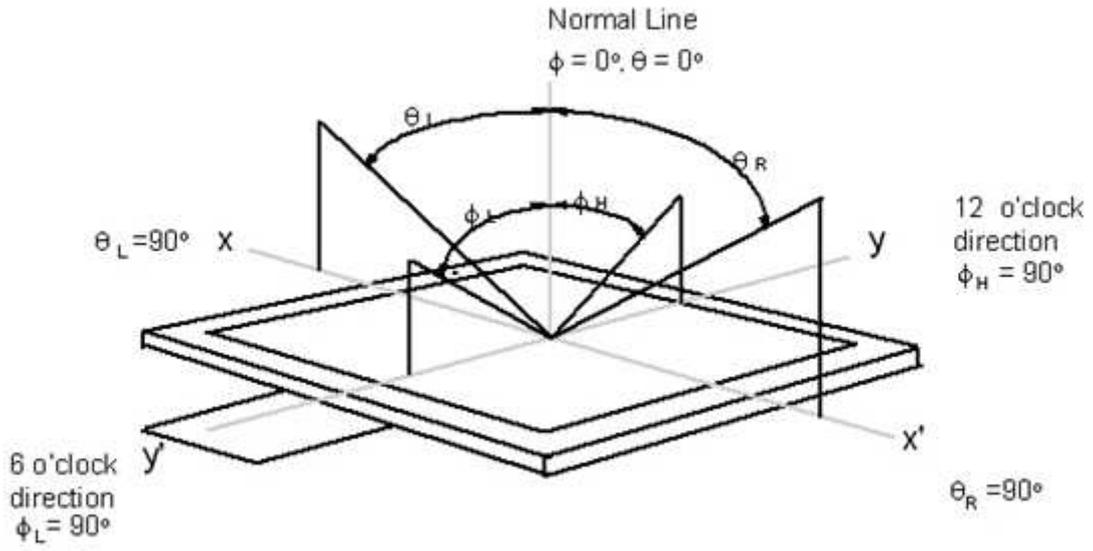
Note (5) Definition of Response time : Sum of  $T_r$ ,  $T_f$



Note (6) Definition of Color Chromaticity (CIE 1931)

Color coordinate of white & red, green, blue at center point.

Note (7) Definition of Viewing Angle



Note (8) Crosstalk

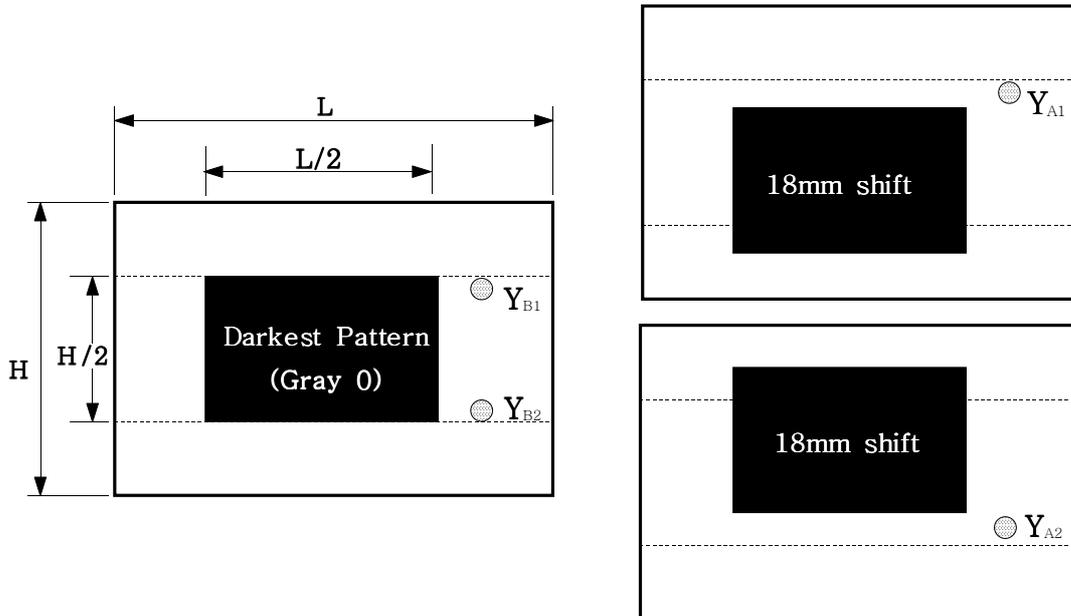
$$\text{Crosstalk Modulation Ratio}(D_{\text{SHA}}) = \frac{|Y_A - Y_B|}{Y_A} \times 100 (\%)$$

Where

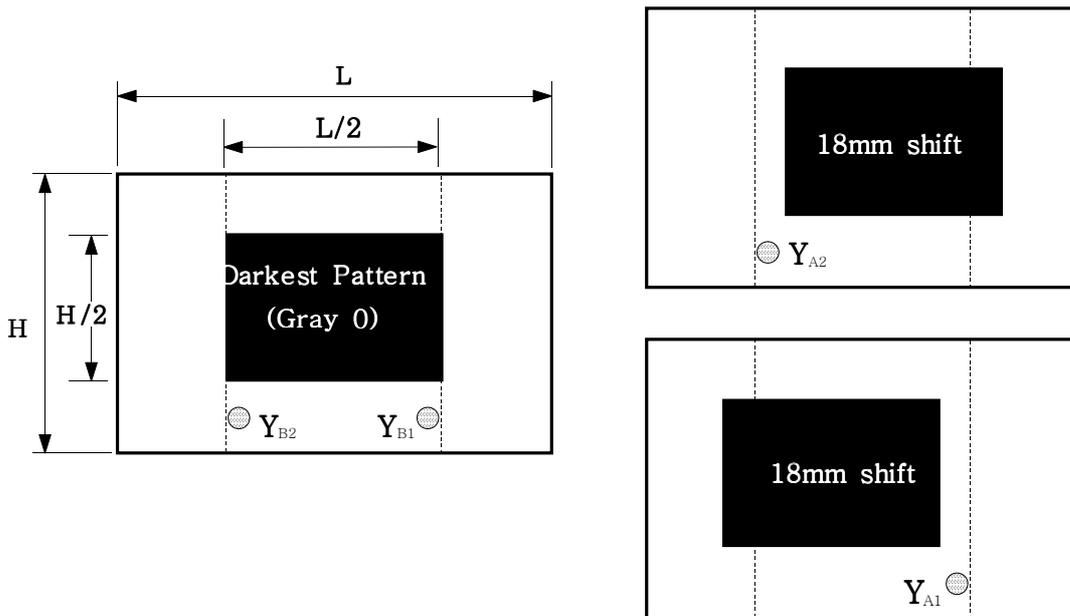
$Y_A, Y_B$  measurement = 2° Viewing Angle (Measurement area  $\varphi 12\text{mm}$ )

The pattern except the Black Bar is a gray 32.

① Horizontal-Crosstalk measurement method



② Vertical-Crosstalk measurement method



### 3. Electrical Characteristics

#### 3.1 TFT-LCD Module

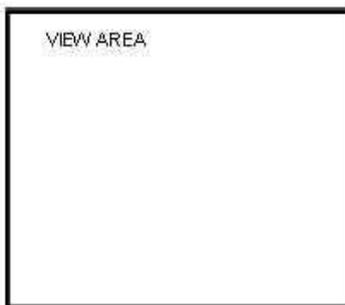
\* To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the section 7.3 Power On/Off Sequence.

(Ta = 25 ± 2°C)

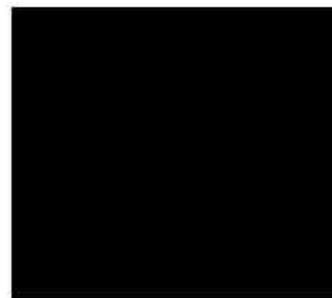
Characteristics		Symbol	MIN.	TYP.	MAX.	Unit	Note
Logic power supply		VDD	3.2	3.3	3.4	V	
Power Dissipation	White	P <sub>w</sub>		250	320	mW	frame freq : 75hz
	Black	P <sub>B</sub>		330	390		

\* Power Dissipation check pattern

a) White Pattern



b) Black Pattern



### 3.2 Back-Light Unit

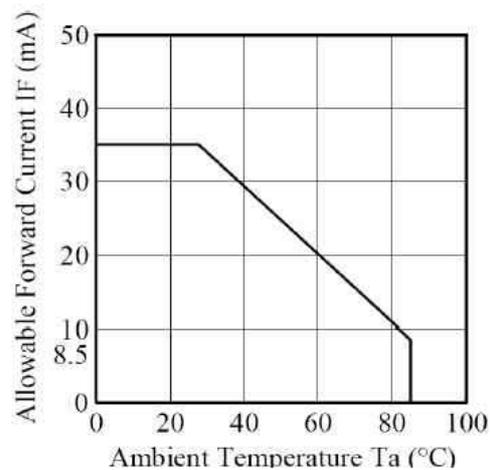
The back-light system is an edge-lighting type with 11 white LED(Light Emitting Diode)s.

( $T_a = 25 \pm 2^\circ\text{C}$ )

Characteristics	Symbol	MIN.	TYP.	MAX.	Unit	Note
LEDs current	$I_{BL}$		20	22	mA	(1)
Power Dissipation	$P_{BL}$		726	800	mW	(2)

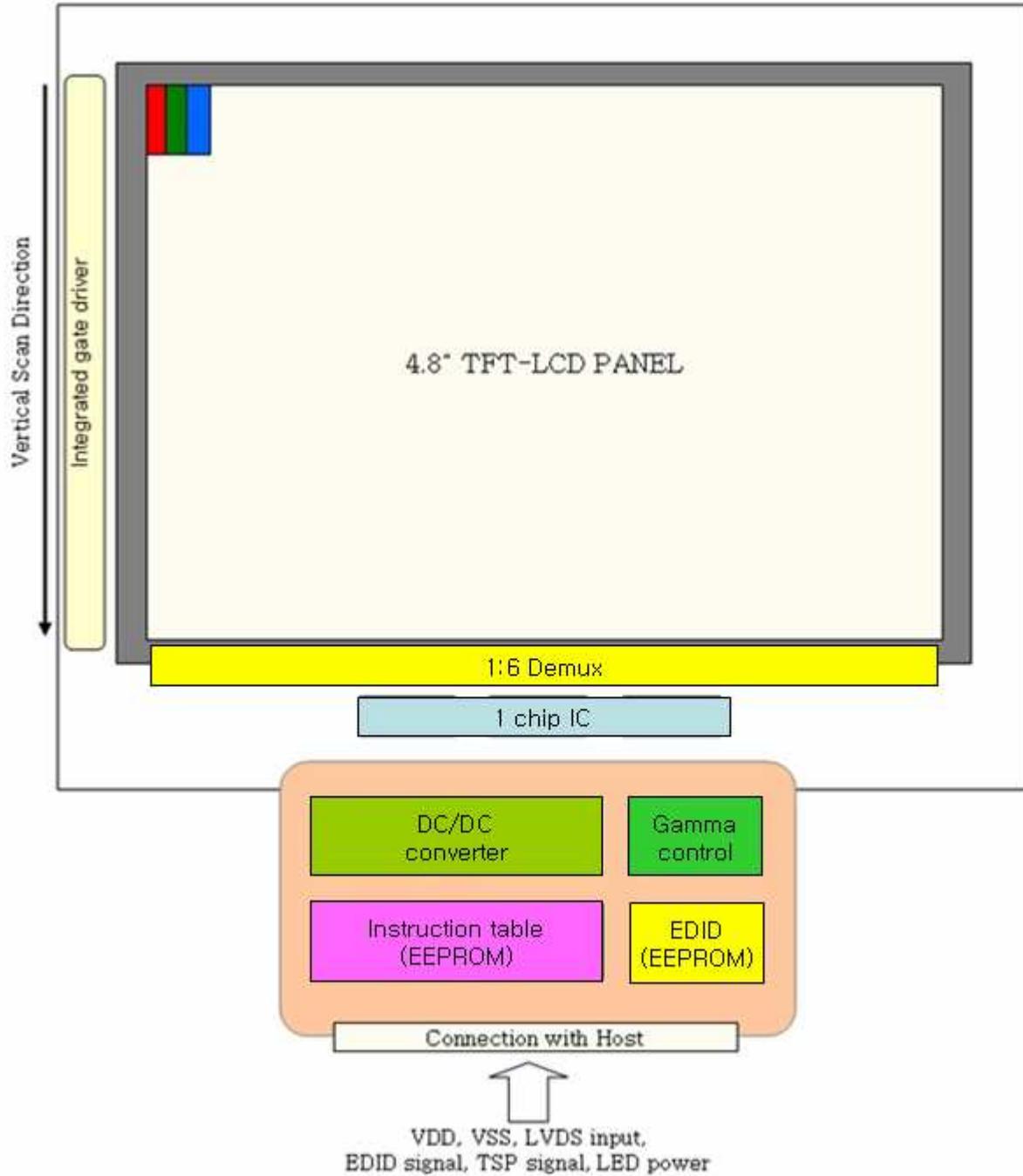
Note (1) 11 white LEDs

Note (2) Where  $I_B=20\text{mA}$

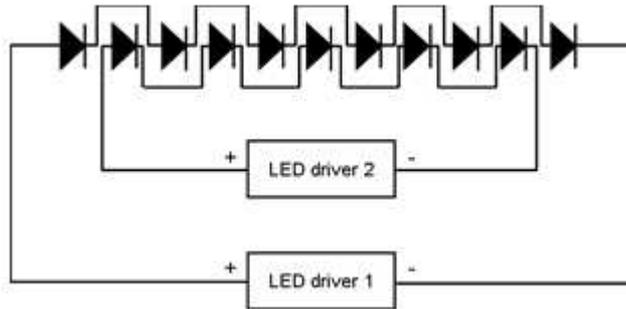


### 4. Block Diagram

#### 4.1 TFT-LCD Module



4.2 Back-Light Unit (Connector : 4 pin FPC solder type)



Pin No.	Symbol	I/O
1	VCD1	LED Power supply (cathode-1)
2	VAD1	LED Power supply (anode-1)
3	VAD2	LED Power supply (anode-2)
4	VCD2	LED Power supply (cathode-2)

## 5. Input Terminal Pin Assignment

### 5.1 Input Signal & Power (Connector type : 28 pin/ 0.5mm pitch/ Bottom contact)

Pin No.	Symbol	I/O
1	Vdd	LCD Power supply : 3.3V
2	NC	Not connected
3	NC	Not connected
4	NC	Not connected
5	NC	Not connected
6	Vdd	LCD Power supply : 3.3V
7	Vss	Ground
8	RxIN0-	Negative LVDS differential data inputs (R0-R5, G0)
9	RxIN0+	Positive LVDS differential data inputs (R0-R5, G0)
10	Vss	Ground
11	RxIN1-	Negative LVDS differential data inputs (G1-G5, B0- B1)
12	RxIN1+	Positive LVDS differential data inputs (G1-G5, B0- B1)
13	Vss	Ground
14	RxIN2-	Negative LVDS differential data inputs (B2-B5, Hsync, Vsync, DE)
15	RxIN2+	Positive LVDS differential data inputs (B2-B5, Hsync, Vsync, DE)
16	Vss	Ground
17	CLK-	Negative LVDS differential clock inputs
18	CLK+	Positive LVDS differential clock inputs
19	Vss	Ground
20	EDID CLK	EDID clock
21	EDID DATA	EDID data
22	EDID PWR	EDID power supply : 3.3V
23	Vss	Ground
24	Vss	Ground
25	VCD1	LED Power supply (cathode-1)
26	VCD2	LED Power supply (cathode-2)
27	VAD1	LED Power supply (anode-1)
28	VAD2	LED Power supply (anode-2)



## 6. Interface Timing

### 6.1 Timing Parameters of TFT-LCD Module Input Signals

(a) Vertical timing

Signal	Symbol	Min.	Typ.	Max.	Unit	Note
Frame Frequency	$f_{FRM}$	60	75	80	Hz	(1)
VSYNC (Frame) Period	VCYC	-	608	-	H	(1)
VSYNC Low Width	VLW	-	1	-	H	(1)
Vertical Display Period	VDP	-	600	-	H	(1)
Vertical Back Porch	VBP	-	4	-	H	(1)
Vertical Front Porch	VFP	-	3	-	H	(1)

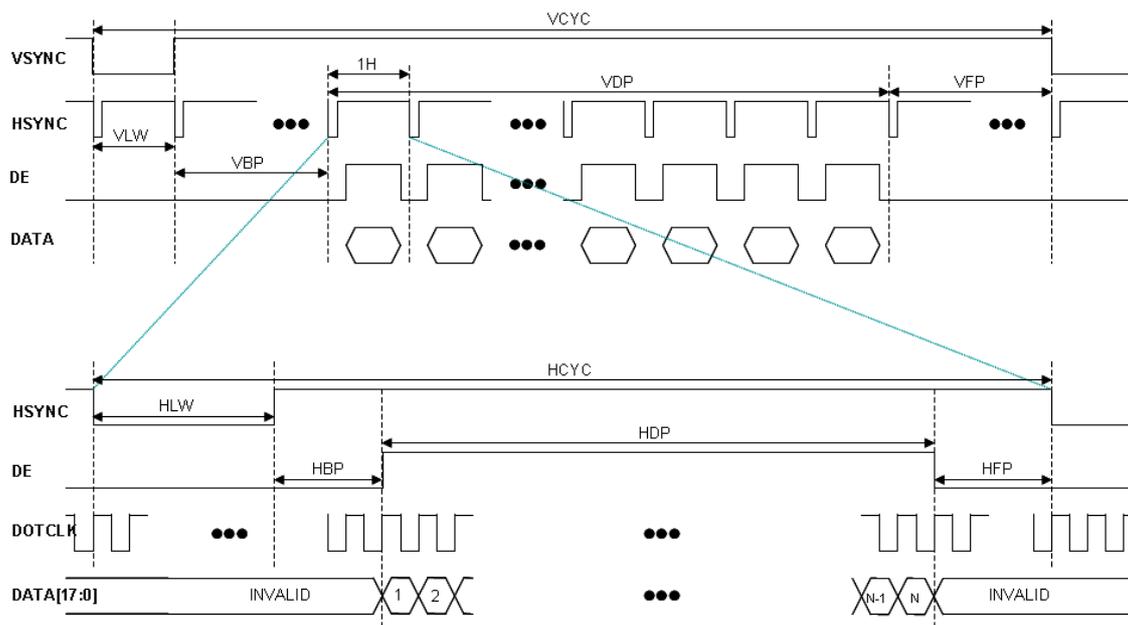
(a) Horizontal timing

Signal	Symbol	Min.	Typ.	Max.	Unit	Note
DotCLK Frequency	$f_{CLK}$	-	53.99	-	MHz	(1)
HSYNC (Frame) Period	HCYC	-	1184	-	DotCLK	(1)
HSYNC Low Width	HLW	-	32	-	DotCLK	(1)
Horizontal Display Period	HDP	-	1024	-	DotCLK	(1)
Horizontal Back Porch	HBP	-	48	-	DotCLK	(1)
Horizontal Front Porch	HFP	-	80	-	DotCLK	(1)

<sup>(1)</sup> Timing-combinations have been preset and memorized in EEPROM.

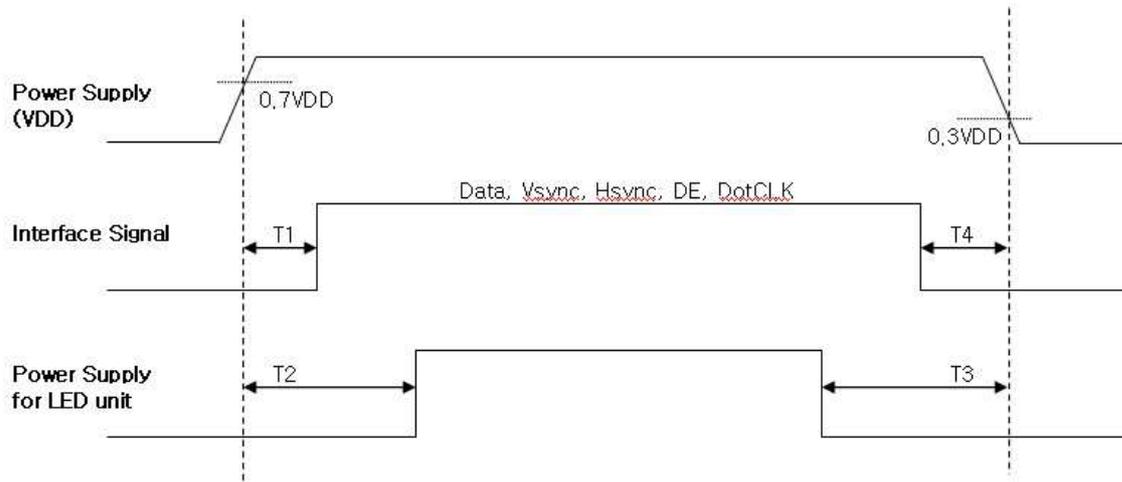
When recommended timings are put into LCD, LCD can be displayed without EDID

### 6.2 Timing Diagrams of TFT-LCD Module Input Signals



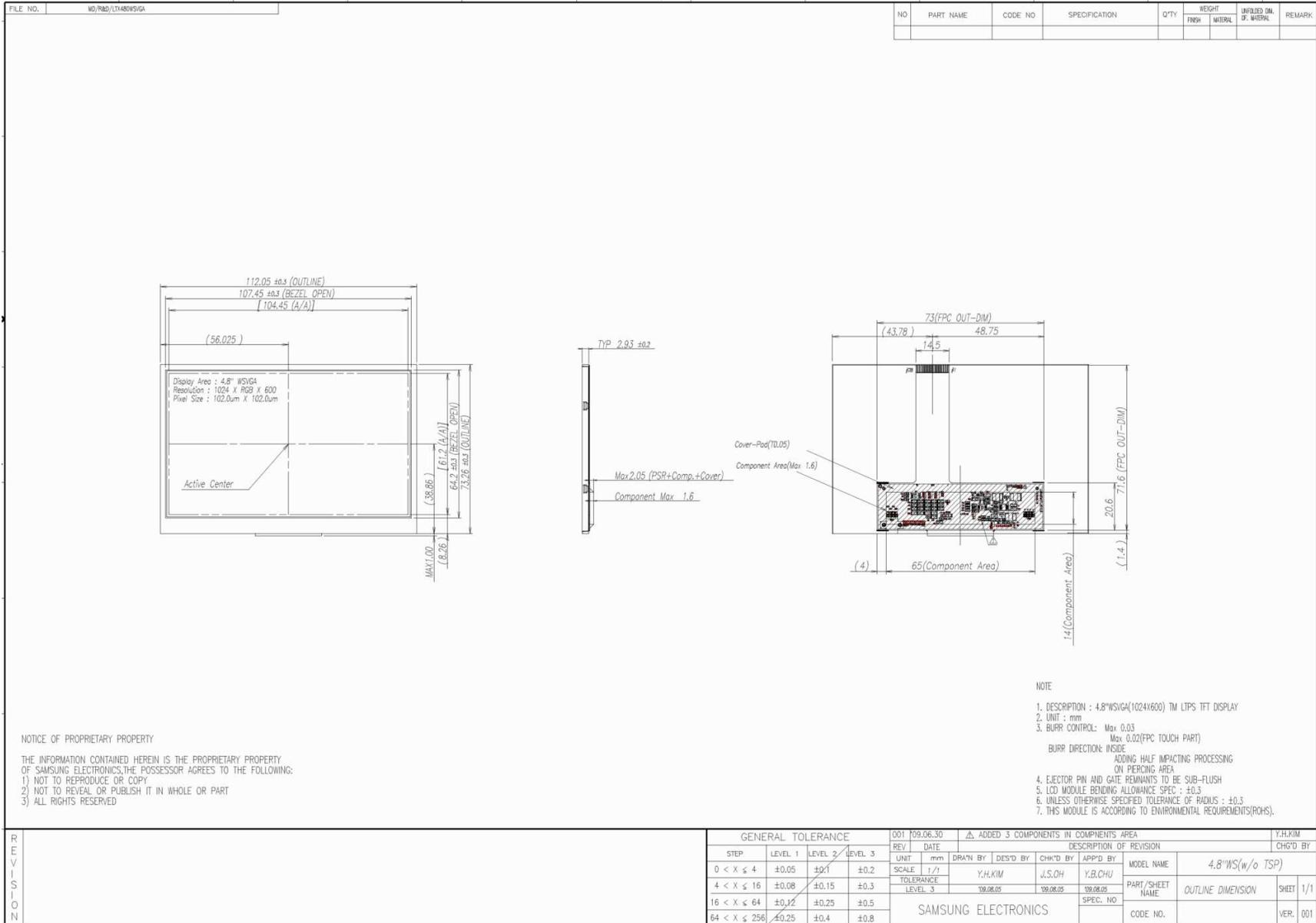
### 6.3 Power ON/OFF Sequence

– To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.

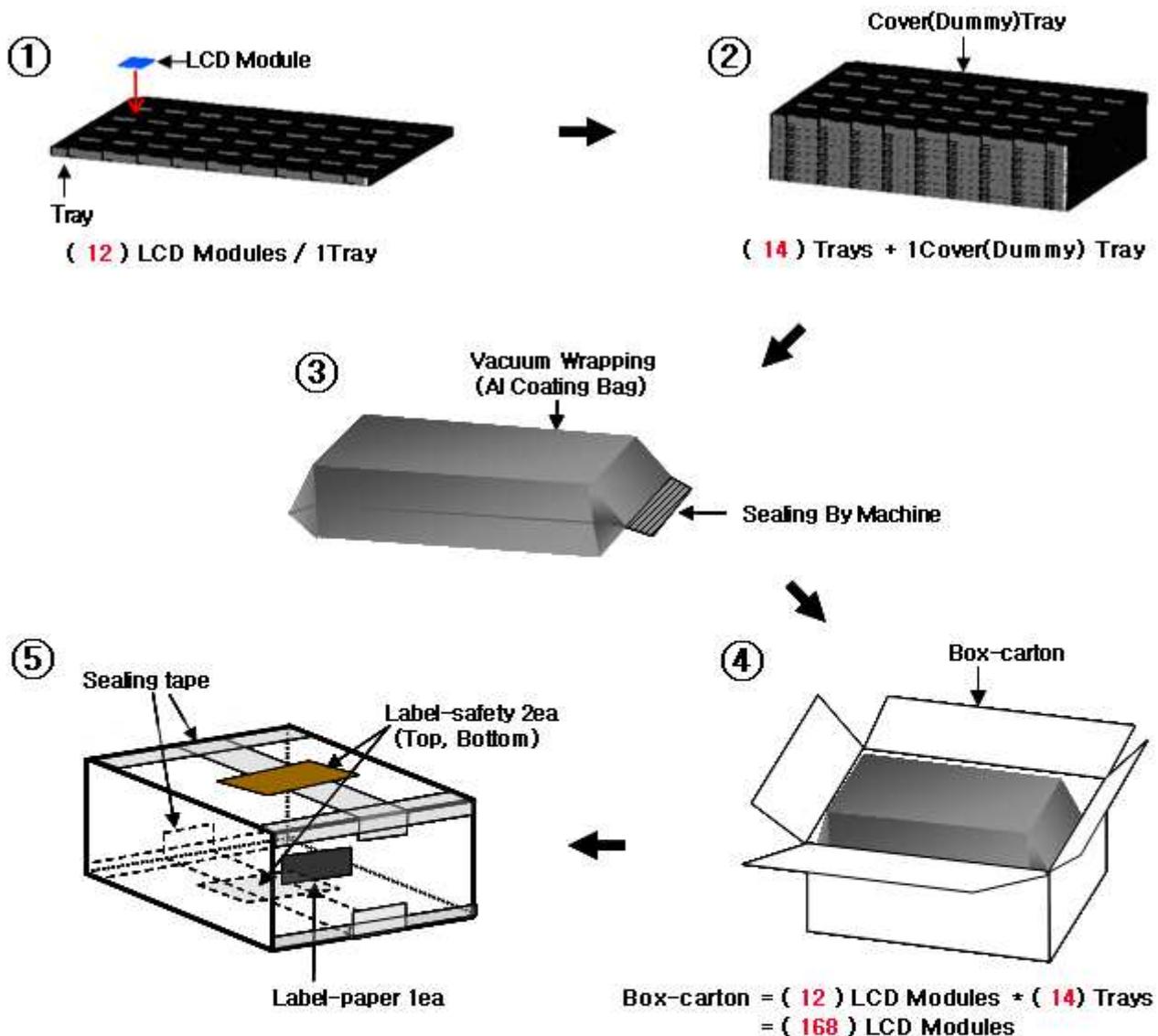


Symbol	Specification	Note
T1	$10 \text{ ms} < T1$	
T2	$T1 + 300\text{ms} < T2$	
T3	$T4 + 130\text{ms} < T3$	
T4	$10 \text{ ms} < T4$	

7. Module Outline Dimension



## 8. PACKING



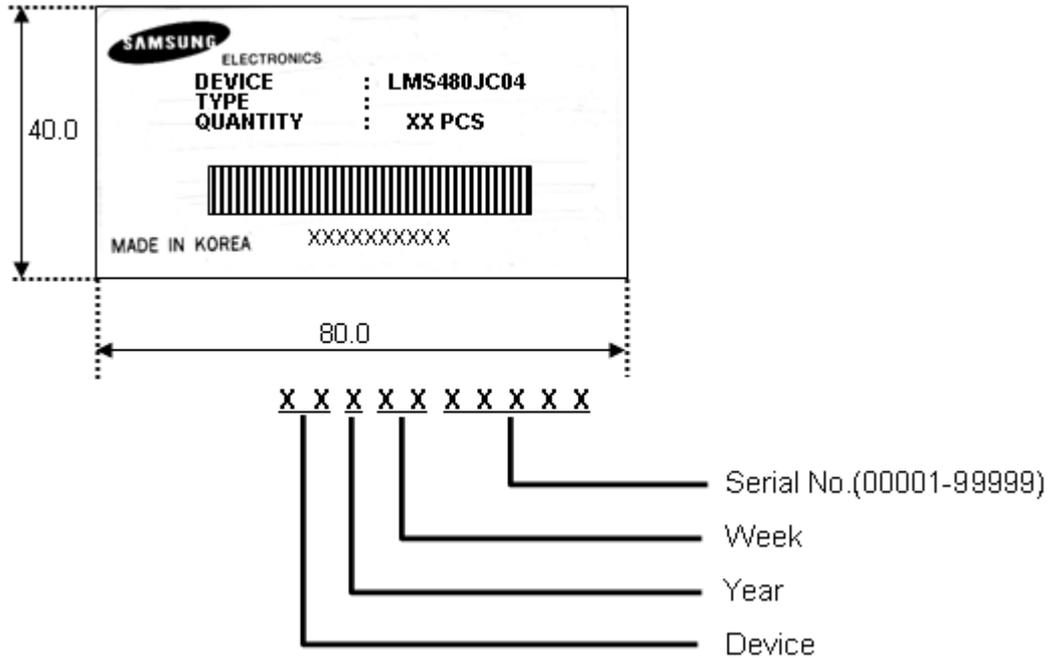
## Note

- (1) Total :Box-carton approx. : ( 13.1 )kg
- (2) Size : 583(L) x 388(W) x 210(H)
- (3) Place the LCD Module in the tray facing the active area direction.
- (4) Stack the trays and cover (dummy) tray.
- (5) Resistance of tray surface :  $10^6 \sim 10^9 \Omega$
- (6) Wrap the Al coating bag by vacuum sealing machine.
- (7) Put the bag in the Box-carton .
- (8) Seal the Box-carton and affix the Label-safety & Label-paper.

### 9. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

(1) Packing case attach



## 10. General Precautions

### 10.1 Handling

- (a) When the module is assembled, it should be attached to the system firmly. Be careful not to twist and bend the module.
- (b) Refrain from strong mechanical shock and / or any force to the module. In addition to damage, this may cause improper operation or damage to the module and back-light unit.
- (c) Note that polarizers are very fragile and could be easily damaged. Do not press or scratch the surface harder than a HB pencil lead.
- (d) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, Staining and discoloration may occur.
- (e) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
- (f) The desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethylacid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (g) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.
- (h) Protect the module from static, it may cause damage to the CMOS Gate Array IC.
- (i) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (j) Do not disassemble the module.
- (k) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (l) Pins of I/F connector shall not be touched directly with bare hands.

## 10.2 Storage

- (a) Do not leave the panel in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 0 to 35°C and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD module in direct sunlight.
- (c) The module shall be stored in a dark place. It is prohibited to apply sunlight or fluorescent light during the store.

## 10.3 Operation

- (a) Do not connect, disconnect the module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the item 7.3 "Power on/off sequence"

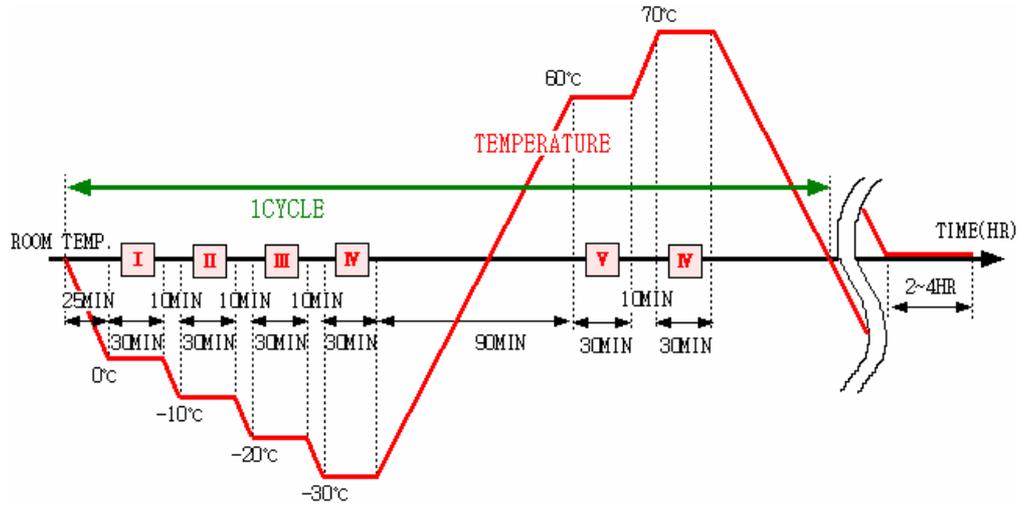
## 10.4 Others

- (a) The liquid-crystal is deteriorated by ultraviolet rays. Do not leave it in direct sunlight and strong ultraviolet rays for many hours.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. ( the supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the panel may be damaged.
- (d) If the panel displays the same pattern continuously for a long period of time, it can be the situation when the image "Sticks" to the screen.
- (e) This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed.

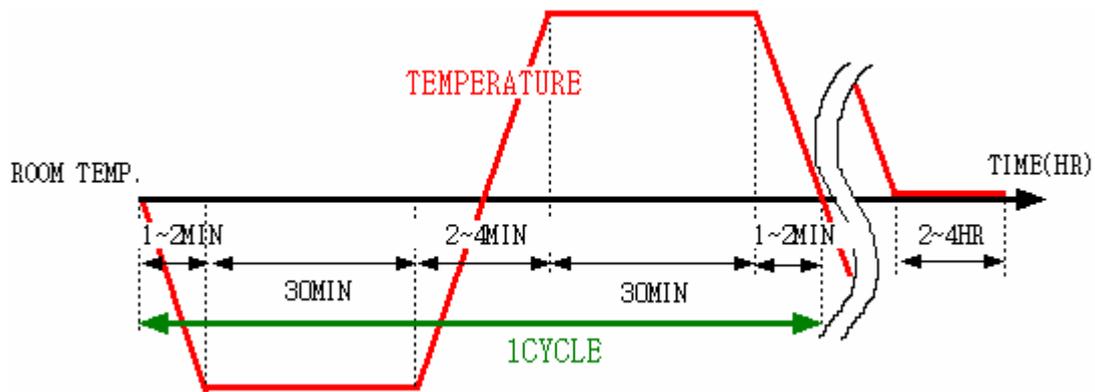
## 11. Reliability Test Condition

Item	Condition	Sample Size	Test Result	Remark
High Temperature Operating Life test	60°C 160HR	10EA	PASS	Note(5)
Low Temperature Operating Life test	-20°C 160HR	5EA	PASS	
Thermal Humidity Bias	60°C 90%RH 160HR	10EA	PASS	Note(5)
On/Off Test	70°C ⇔ -30°C (5CYC),	10EA	PASS	Note(1)
High Temperature Storage test	85°C 160HR	5EA	PASS	
Low Temperature Storage test	-40°C 160HR	5EA	PASS	
Wet Humidity Temperature Storage test	60°C /90%RH 160HR	10EA	PASS	
Altitude test	Non-operating: 188mb/Room Temp. 72HR	5EA	PASS	
Thermal Cycle storage test	-40~85°C, 30CYC	10EA	PASS	Note(2)
Electrical Static Discharge	Contact: ±4kV, 150pF/330Ω Non-contact: ±8kV, 150pF/330Ω	5EA 5EA	PASS	Note(3)
Box Vibration	Random Vibration MIN-STD 810E, up/down(Z)Axis	(1BOX Small)	PASS	Note(4) ♪
Box Drop	1 corner, 3 edges, 6 surfaces under 9.1kg:76cm, 9.2~18.2kg: 66cm	(1BOX Medium)	PASS	

Note(1) ON Time over 10 seconds, OFF Time under 10 seconds.



Note(2) STORAGE



Note(3) Main-LCD, 5 times to every 4 corners of active area.

Note(4) Basic transportation by common carrier environmental, 514.4 MIL-STD-810E.

OVERALL RMS LEVEL	BREAK POINT					
	FREQUENCY	PSD VALUE	FREQUENCY	PSD VALUE	FREQUENCY	PSD VALUE
0.74G	10Hz	0.00650	121Hz	0.00300	340Hz	0.00003
	20Hz	0.00650	200Hz	0.00300	500Hz	0.00015
	120Hz	0.00020	240Hz	0.00150	-	-

Note(5) After finishing Reliability Test and leaving samples in room temperature condition during 2hours, malfunction and abnormal should not be found.