SAMSUNG ELECTRONICS

Product Information

Customer : XXX **DATE : 18.Jun.2008**

SAMSUNG TFT-LCD

MODEL : **LTA520HE15**

NOTE :

Customer's *Approval*	
SIGNATURE	DATE

APPROVED BY Kyunghwan Ko	DATE 18. Jun.2008
PREPARED BY Jinsu Jung	DATE 18. Jun.2008

LCD Business

Samsung Electronics Co . , LTD.

정진수05156387응용기술2파트(HD)129252203 20080617172614

017831213691128

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* Revision History

Date	Rev. No	Page	Summary
Jun 18, 2008	000	all	First issued

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General Description

Description

LTA520HE15 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT(Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit. The resolution of a 52.0“ is 1920 x 1080 and this model can display up to 1.07 billion colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV and High Definition TV.

Features

- RoHS compliance (Pb-free)
- High contrast ratio & aperture ratio with color gamut
- SPVA(Super Patterned Vertical Align) mode
- Wide viewing angle (±178°)
- High speed response & Natural motion (DFR :Double Frame Rate)
- FHD resolution (16:9)
- Low Power consumption
- Direct Type 20 CCFLs (Cold Cathode Fluorescent Lamp)
- DE(Data Enable) mode
- 4ch LVDS (Low Voltage Differential Signaling) interface

General Information

Items	Specification	Unit	Note
Module Size	1226.0(H_{TYP}) x 719.2V_{TYP}) 58.5(D_{MAX})	mm	±1.0mm
Weight	18.000 (Max.)	g	
Pixel Pitch	0.6(H) x 0.6(V)	mm	
Active Display Area	1152.0(H) x 648.0(V)	mm	
Surface Treatment	Haze 14% , Hard-coating (3H)		
Display Colors	10bit – 1.07G	colors	
Number of Pixels	1920 x 1080	pixel	
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		
Luminance of White	500 (Typ.)	cd/㎡	

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1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item		Symbol	Min.	Max.	Unit	Note
Power Supply Voltage		V_{DD}	GND-1	*13.0*	V	(1)
Storage temperature		T_{STG}	-20	60	℃	(2)
Glass surface temperature (Operation)	Center	T_{OPR}	0	50	℃	(2),(5)
	T. Uniformity	△T	-	10	℃	
Shock (non - operating)		S_{nop}	-	30	G	(3)
Vibration (non - operating)		V_{nop}	-	1.5	G	(4)

Note (1) Ta= 25 ± 2 °C

(2) Temperature and relative humidity range are shown in the figure below.

a. 90 % RH Max. (Ta ≤ 39 °C)

b. Relative Humidity is 90% or less. (Ta > 39 °C)

c. No condensation

(3) 11ms, sine wave, one time for ±X, ±Y, ±Z axis

(4) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

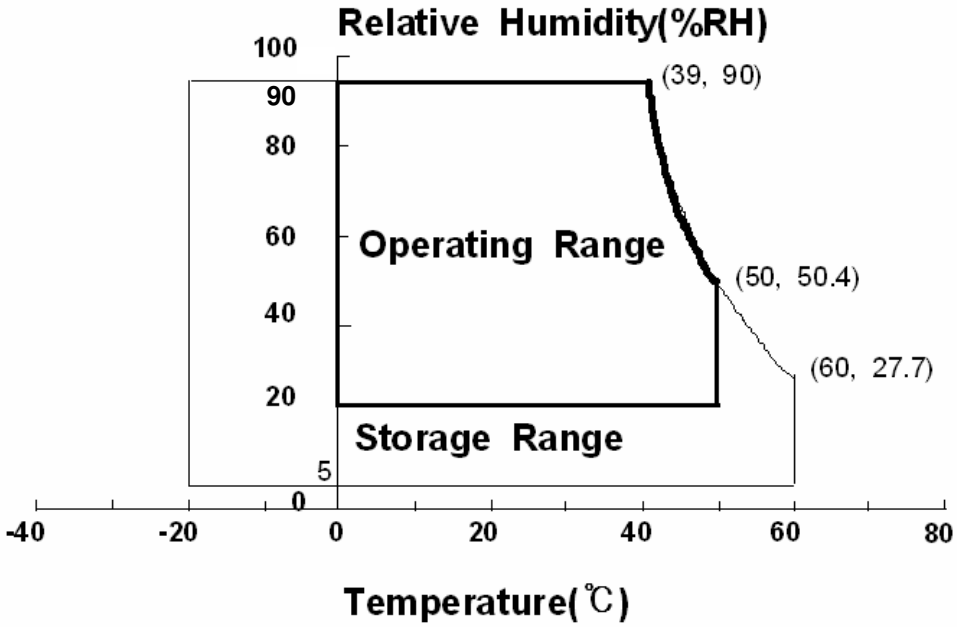

Fig. Temperature and Relative humidity range

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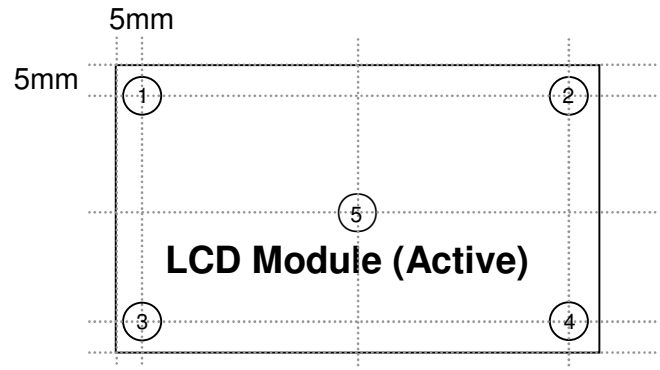
(5) Definition of test point



△T should be less than *10* ℃ (△T = | T_{OPR} – T_{MAX}|)

T_{OPR} : Temperature of the center of the glass surface (Test point 5)
T1~ T4 : Temperature of each edge of the glass surface
T_{MAX} : The highest temperature of the glass surface

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2. Optical Characteristics

The optical characteristics should be measured in a dark room or equivalent.
Measuring equipment : TOPCON RD-80S, TOPCON SR-3, ELDIM EZ Contrast

(Ta = 25 ± 2°C, VDD=12.0V, fv= 120Hz, f_{DCLK} =297MHz)

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio (Center of screen)		C/R	Normal θ**L,R**=0 θ**U,D**=0 Viewing Angle	2500	3000	-		(1) SR-3
Response Time	Rising	Tr		-	12	20	msec	(3) RD-80S
	Falling	Tf		-	6	8		
	G-to-G	Tg		-	6	8		
Luminance of White (Center of screen)		Y_L		400	500	-	cd/m^2	(4) SR-3
Color Chromaticity (CIE 1931)	Red	Rx		TYP. -0.03	TBD	TYP. +0.03		(5),(6) SR-3
		Ry			TBD			
	Green	Gx			TBD			
		Gy			TBD			
	Blue	Bx			TBD			
		By			TBD			
	White	Wx			0.280			
		Wy			0.290			
Color Gamut		-		-	90	-	%	(5) SR-3
Color Temperature		-		-	10000	-	K	
Viewing Angle	Hor.	θ_L	C/R≥10	75	89	-	Degree	(6) EZ-Contrast
		θ_R		75	89	-		
	Ver.	θ_U		75	89	-		
		θ_D		75	89	-		
Brightness Uniformity (9 Points)		B_{uni}		-	-	25	%	(2) SR-3

- Test Equipment Setup

The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the back light at the given temperature for stabilization of the back light. This should be measured in the center of screen.

Environment condition : Ta = 25 ± 2 °C

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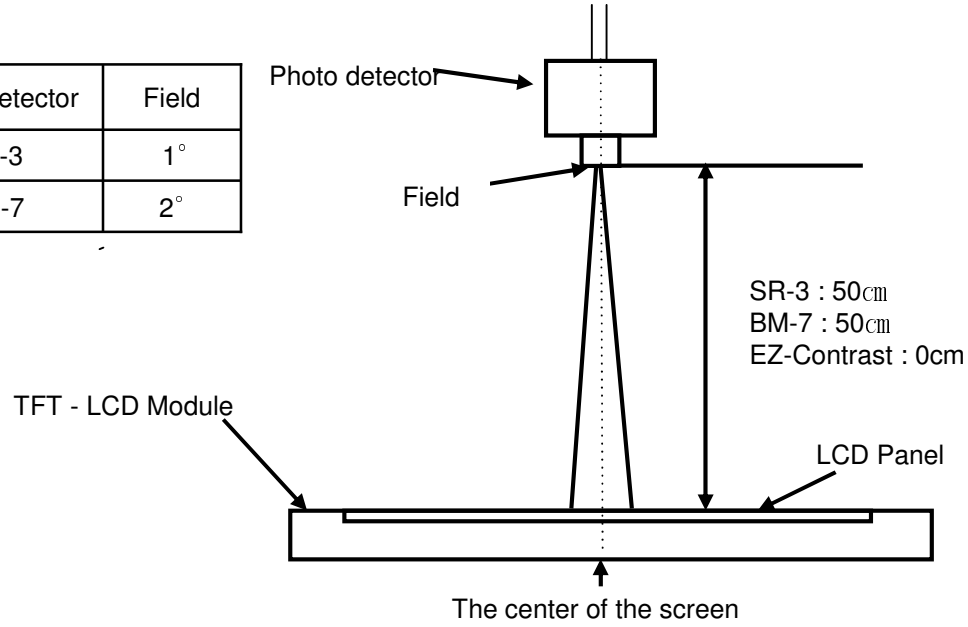
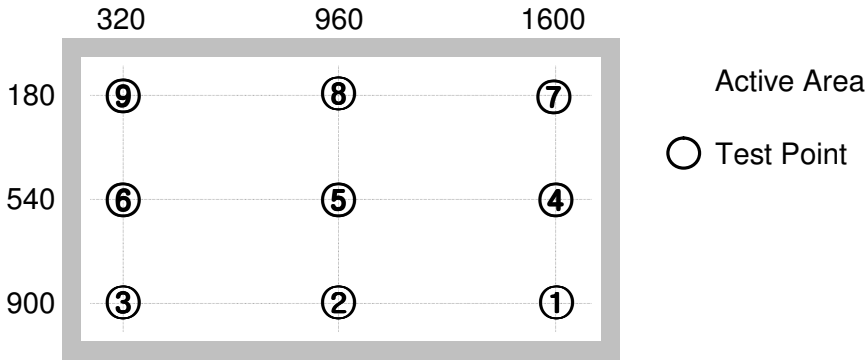
Photo detector	Field
SR-3	1°
BM-7	2°



- Definition of test point



Note (1) Definition of Contrast Ratio (C/R)
: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/\mathrm{R} = \frac{G\max}{G\min}$$

Gmax : Luminance with all pixels white
Gmin : Luminance with all pixels black

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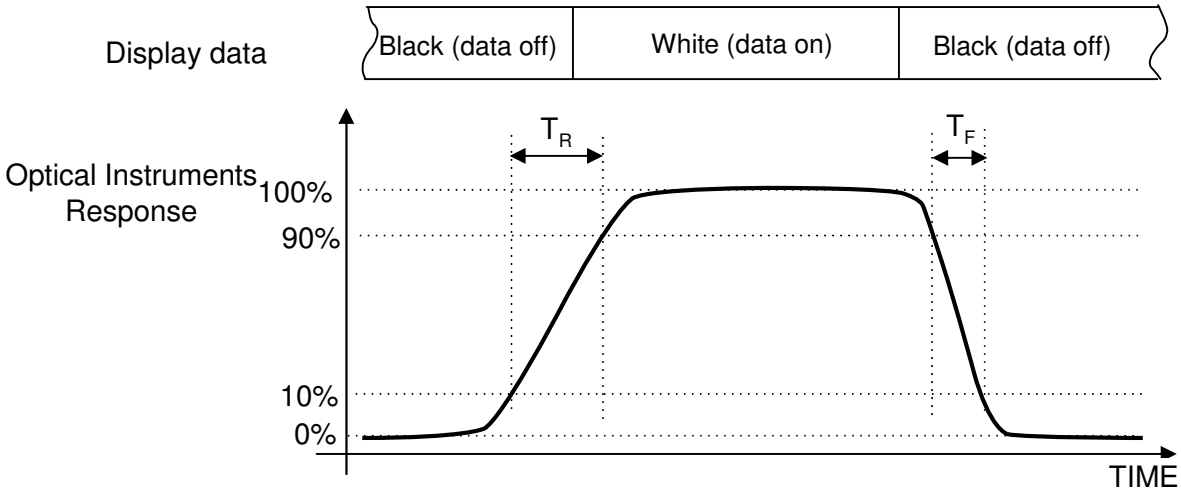
Note (2) Definition of 9 points brightness uniformity at maximum dimming voltage
(Test Pattern : Full White)

$$Buni = 100 * \frac{(B\max - B\min)}{B\max}$$

Bmax : Maximum brightness
Bmin : Minimum brightness

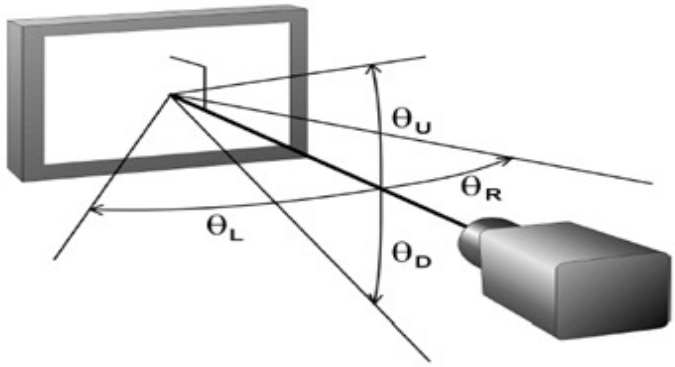
Note (3) Definition of Response time : Sum of Tr, Tf



Note (4) Definition of Luminance of White : Luminance of white at center point ⑤

Note (5) Definition of Color Chromaticity (CIE 1931)
Color coordinate of Red, Green, Blue & White at center point ⑤

Note (6) Definition of Viewing Angle
: Viewing angle range (C/R ⩾10)



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3. Electrical Characteristics

3.1 TFT LCD Module

The connector for display data & timing signal should be connected.

Ta = 25°C ± 2 °C

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply		V_{DD}	11	12	13	V	(1)
Current of Power Supply	(a) Black	I_{DD}	-	1100	-	mA	(2),(3)
	(b) White		-	1200	1350	mA	
	(c) N-pattern		-	2320	2550	mA	
Vsync Frequency		f_V	95	120	125	Hz	
Hsync Frequency		f_H	120	132	140	kHz	
Main Frequency		f_{DCLK}	270	297	307	MHz	
Rush Current		I_{RUSH}	-	-	7	A	(4)

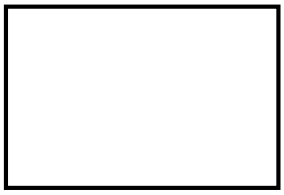
Note (1) The ripple voltage should be controlled under 10% of V_{DD}.

(2) fv=120Hz, f_{DCLK} = 297MHz, V_{DD} = 12.0V, DC Current.

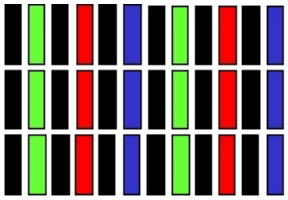
(3) Power dissipation check pattern (LCD Module only)

a) Black Pattern

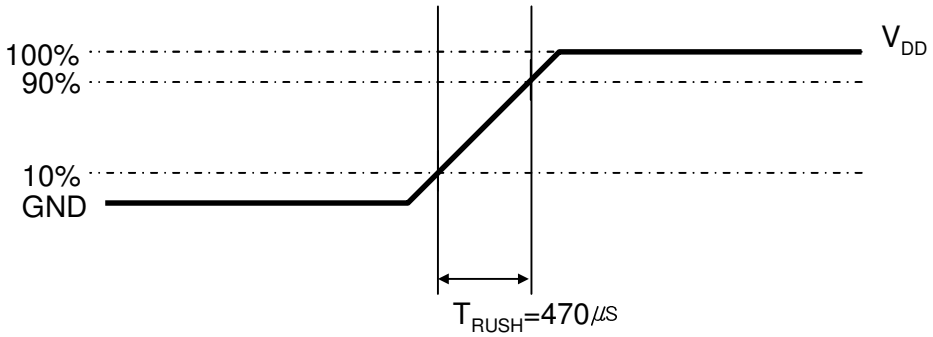
b) White Pattern



c) N-pattern



(4) Measurement Conditions



Rush Current I_{RUSH} can be measured when T_{RUSH}. is 470㎲.

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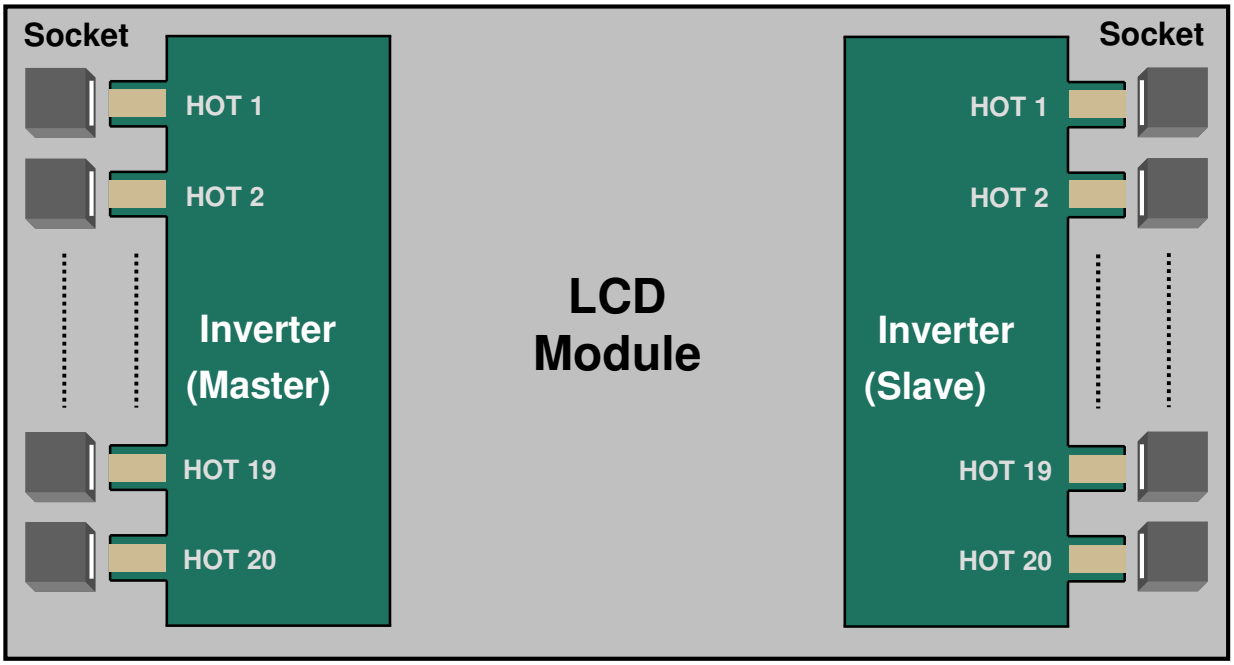
3.2 Back Light Unit

The back light unit contains 20 direct-lighting type CCFLs (Cold Cathode Fluorescent Lamp). The characteristics of lamps are shown in the following tables.

Ta=25 ± 2°C



Item	Symbol	Min.	Typ.	Max.	Unit	Note
Operating Life Time	Hr	35,000	-	-	Hour	(1)

Note (1) It is defined as the time to take until the brightness reduces to 50%of its original value.

[Operating condition : Ta = 25±2℃ , I_L = TBDmA , For Single Lamp Only]

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3.3 Inverter Input Condition & Specification

Items	Symbol	Conditions	Specifications			Unit	Note
			Min.	Typ.	Max.		
Input Voltage	Vin	-	22.8	24	25.2	V	Ta=25±2 °C
Input Current	I_{RUSH}	Vin=24.0V Vdim =3.3V Ta=25℃	-	12.2	13.4	A	(1)
			-	10.2	11.2		(2)
Lamp Current	$I_{O, max}$	Vin=24.0V Vdim =3.3V	8.5	9.0		mArms	After 2hour Warm up
Frequency	F_{LAMP}	Vin=24.0 V	40	42	44	kHz	-
Backlight On/Off	ON	Vin=24.0 V	2.4	-	5.5	V	-
	OFF	Vin=24.0 V	0	-	0.8		
Dimming Control	V_{DIM}	Max Lum PWM = 100%	3.3	-	-	V	-
		Min. Lum PWM = 20%	-	0	-		

Note) Power Consumption is measured when 500[cd/m^2] of luminance which is the typical luminance.
Lamp Current is measured at the point before Lamp.

(1) Max Value of the Power Consumption is measured during initial turn-on time* of the backlight.

(2) Max Value of the Power Consumption is measured after 60 min warm-up.

.

* Initial turn-on time : From 0sec to 60min after turn-on

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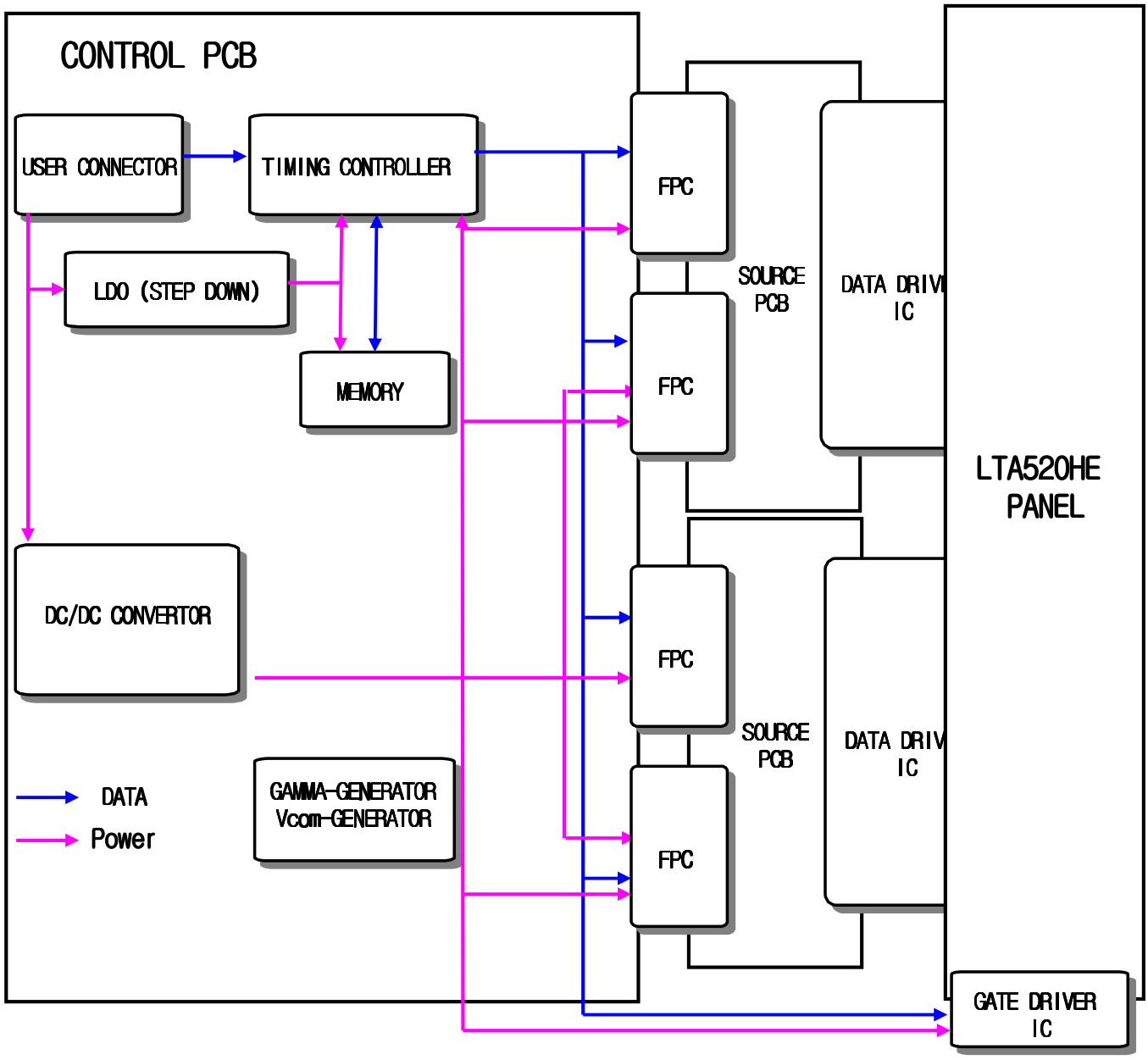
4. Block Diagram



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5. Input Terminal Pin Assignment

5.1.1 Input Signal & Power

Connector : FI-RE51S-HF (JAE)
IS050-C51B-C38(UJU)

PIN No.	Description		PIN No.	Description	
1	VDD(12V)		26	Even LVDS Signal	Rx4[0]P
2	VDD(12V)		27		Rx4[1]N
3	VDD(12V)		28		Rx4[1]P
4	VDD(12V)		29		Rx4[2]N
5	VDD(12V)		30		Rx4[2]P
6	GND		31		GND
7	GND		32		Rx4[CLK]N
8	GND		33		Rx4[CLK]P
9	GND		34		GND
10	Even LVDS Signal	Rx2[0]N	35		Rx4[3]N
11		Rx2[0]P	36		Rx4[3]P
12		Rx2[1]N	37		Rx4[4]N
13		Rx2[1]P	38		Rx4[4]P
14		Rx2[2]N	39	GND	
15		Rx2[2]P	40	No connection	
16		GND	41	No connection	
17		Rx2[CLK]N	42	No connection	
18		Rx2[CLK]P	43	No connection	
19		GND	44	No connection	
20		Rx2[3]N	45	LVDS Option	
21		Rx2[3]P	46	No connection	
22		Rx2[4]N	47	No connection	
23		Rx2[4]P	48	No connection	
24	GND		49	No connection	
25	Even LVDS	Rx4[0]N	50	No connection	
			51	No connection	

Note) No Connection: This PINS are only used for SAMSUNG internal using.
Note (1) LVDS OPTION : If this PIN : HIGH (3.3 V) → Normal LVDS format
: LOW (GND or N.C) → JEIDA LVDS format
SEQUENCE : On = VDD(T1) ≥ LVDS Option ≥ Interface Signal(T2)
OFF = Interface Signal(T3) ≥ LVDS Option ≥ VDD

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5. Input Terminal Pin Assignment

5.1.2 Input Signal & Power

Connector : FI-RE41S-HF (JAE)
IS050-C41B-C38 (UJU)

Pin	Symbol	Description	Pin	Symbol	Description
1	12V	DC power supply	21	Rx1[D]P	1st, 5th LVDS Signal +
2	12V	DC power supply	22	Rx1[E]N	1st, 5th LVDS Signal -
3	12V	DC power supply	23	Rx1[E]P	1st, 5th LVDS Signal +
4	12V	DC power supply	24	GND	Ground
5	12V	DC power supply	25	Rx3[A]N	3rd, 7th LVDS Signal -
6	GND	Ground	26	Rx3[A]P	3rd, 7th LVDS Signal +
7	GND	Ground	27	Rx3[B]N	3rd, 7th LVDS Signal -
8	GND	Ground	28	Rx3[B]P	3rd, 7th LVDS Signal +
9	GND	Ground	29	Rx3[C]N	3rd, 7th LVDS Signal -
10	Rx1[A]N	1st, 5th LVDS Signal -	30	Rx3[C]P	3rd, 7th LVDS Signal +
11	Rx1[A]P	1st, 5th LVDS Signal +	31	GND	Ground
12	Rx1[B]N	1st, 5th LVDS Signal -	32	Rx3CLK-	3rd, 7th LVDS Clock -
13	Rx1[B]P	1st, 5th LVDS Signal +	33	Rx3CLK+	3rd, 7th LVDS Clock +
14	Rx1[C]N	1st, 5th LVDS Signal -	34	GND	Ground
15	Rx1[C]P	1st, 5th LVDS Signal +	35	Rx3[D]N	3rd, 7th LVDS Signal -
16	GND	Ground	36	Rx3[D]P	3rd, 7th LVDS Signal +
17	Rx1CLK-	1st, 5th LVDS Clock -	37	Rx3[E]N	3rd, 7th LVDS Signal -
18	Rx1CLK+	1st, 5th LVDS Clock +	38	Rx3[E]P	3rd, 7th LVDS Signal +
19	GND	Ground	39	GND	Ground
20	Rx1[D]N	1st, 5th LVDS Signal -	40	No Connection	
			41	No Connection	

Note) No Connection : This Pins are only used for SAMSUNG internal using.

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Note (2) Pin number starts from Right side

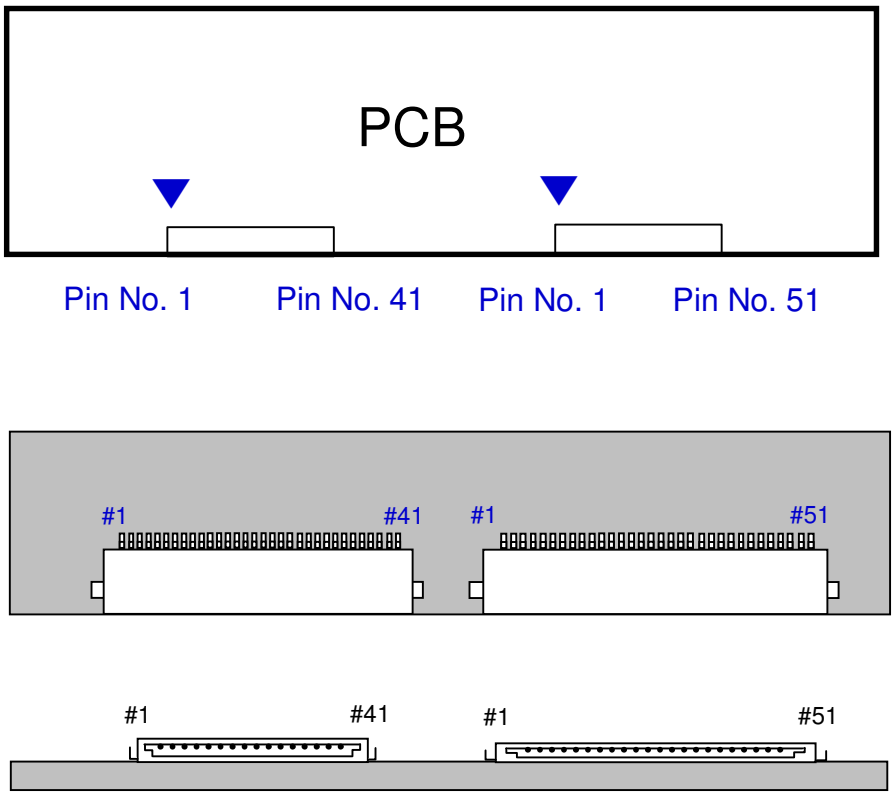

Fig. Connector diagram

a. All GND pins should be connected together and also be connected to the LCD's metal chassis.
b. All power input pins should be connected together.
c. All NC pins should be separated from other signal or power.

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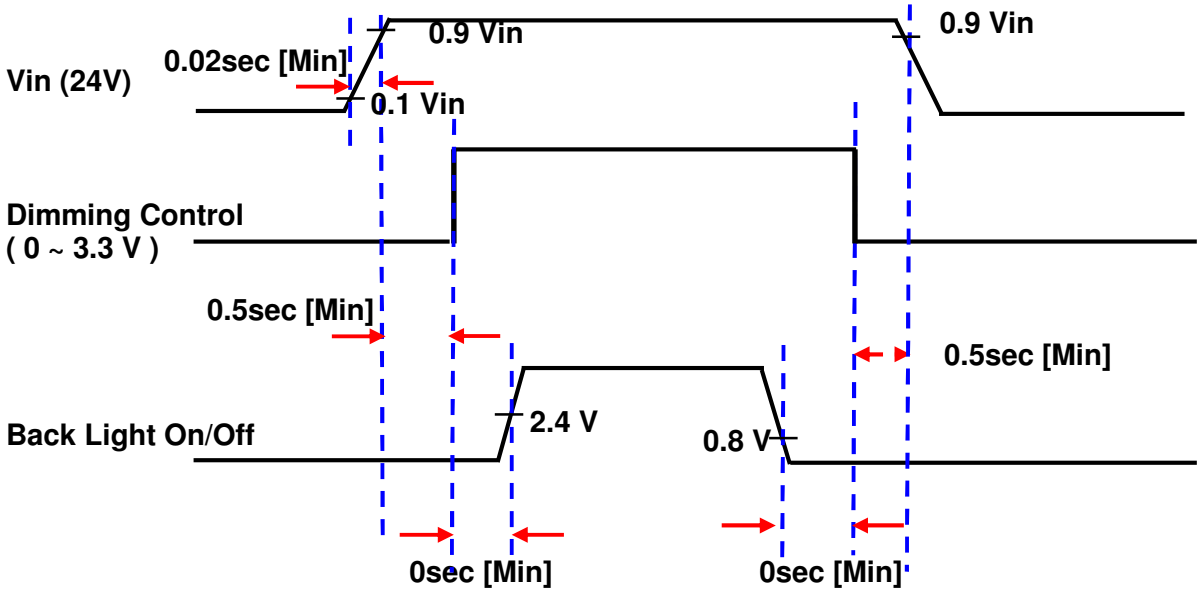
5.2. Inverter Input Pin Configuration

Connector : S14B-PHA-SM-TB(LF) (JST)

Pin No.	Pin Configuration(FUNCTION)	
	Master (Left)	Slave (Right)
1	24 V	24 V
2	24 V	24 V
3	24 V	24 V
4	24 V	24 V
5	24 V	24 V
6	GND	GND
7	GND	GND
8	GND	GND
9	GND	GND
10	GND	GND
11	No Connection	No Connection
12	Backlight On /Off	No Connection
13	Dimming Control	No Connection
14	No Connection	No Connection

4.3. Inverter Input Power Sequence



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5.3 LVDS Interface

- LVDS Receiver : Tcon (merged)
- Data Format (JEIDA)

	LVDS pin	JEIDA -DATA
TxOUT/RxIN0	TxIN/RxOUT0	R4
	TxIN/RxOUT1	R5
	TxIN/RxOUT2	R6
	TxIN/RxOUT3	R7
	TxIN/RxOUT4	R8
	TxIN/RxOUT6	R9
	TxIN/RxOUT7	G4
TxOUT/RxIN1	TxIN/RxOUT8	G5
	TxIN/RxOUT9	G6
	TxIN/RxOUT12	G7
	TxIN/RxOUT13	G8
	TxIN/RxOUT14	G9
	TxIN/RxOUT15	B4
	TxIN/RxOUT18	B5
TxOUT/RxIN2	TxIN/RxOUT19	B6
	TxIN/RxOUT20	B7
	TxIN/RxOUT21	B8
	TxIN/RxOUT22	B9
	TxIN/RxOUT24	HSYNC
	TxIN/RxOUT25	VSYNC
	TxIN/RxOUT26	DEN
TxOUT/RxIN3	TxIN/RxOUT27	R2
	TxIN/RxOUT5	R3
	TxIN/RxOUT10	G2
	TxIN/RxOUT11	G3
	TxIN/RxOUT16	B2
	TxIN/RxOUT17	B3
	TxIN/RxOUT23	RESERVED
TxOUT/RxIN4	TxIN/RxOUT28	R0
	TxIN/RxOUT29	R1
	TxIN/RxOUT30	G0
	TxIN/RxOUT31	G1
	TxIN/RxOUT32	B0
	TxIN/RxOUT33	B1
	TxIN/RxOUT34	RESERVED

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5.4 Input Signals, Basic Display Colors and Gray Scale of Each Color

COLOR	DISPLAY (8bit)	DATA SIGNAL																														GRAY SCALE LEVEL
		RED										GREEN										BLUE										
		R0	R1	R2	R3	R4	R5	R6	R7	R8	R9	G0	G1	G2	G3	G4	G5	G6	G7	G8	G9	B0	B1	B2	B3	B4	B5	B6	B7	B8	B9	
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	-
	CYAN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
	DARK ↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~ R1020
	↓ LIGHT	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
		1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1021
		0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1022
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1023
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
	DARK ↑	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~ G1020
	↓ LIGHT	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
		0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1021
		0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1022
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1023
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	B1
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	B2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~ B1020
	↓ LIGHT	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	B1021
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	B1022
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	B1023

Note) Definition of Gray :

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

Input Signal : 0 = Low level voltage, 1 = High level voltage

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6. Interface Timing

6.1 Timing Parameters (DE only mode)

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock	Frequency	$1/T_C$	270	297	307	MHz	-
Hsync		F_H	120	132	140	KHz	-
Vsync		F_V	95	120	125	Hz	-
Vertical Display Term	Active Display Period	T_{VD}	-	1080	-	lines	-
	Vertical Total	T_V	1090	1125	1380	lines	-
Horizontal Display Term	Active Display Period	T_{HD}	-	1920	-	clocks	-
	Horizontal Total	T_H	2090	2200	2350	clocks	-

Note) This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

(1) Test Point : TTL control signal and CLK at LVDS Tx input terminal in system

(2) Internal VDD = 3.3V

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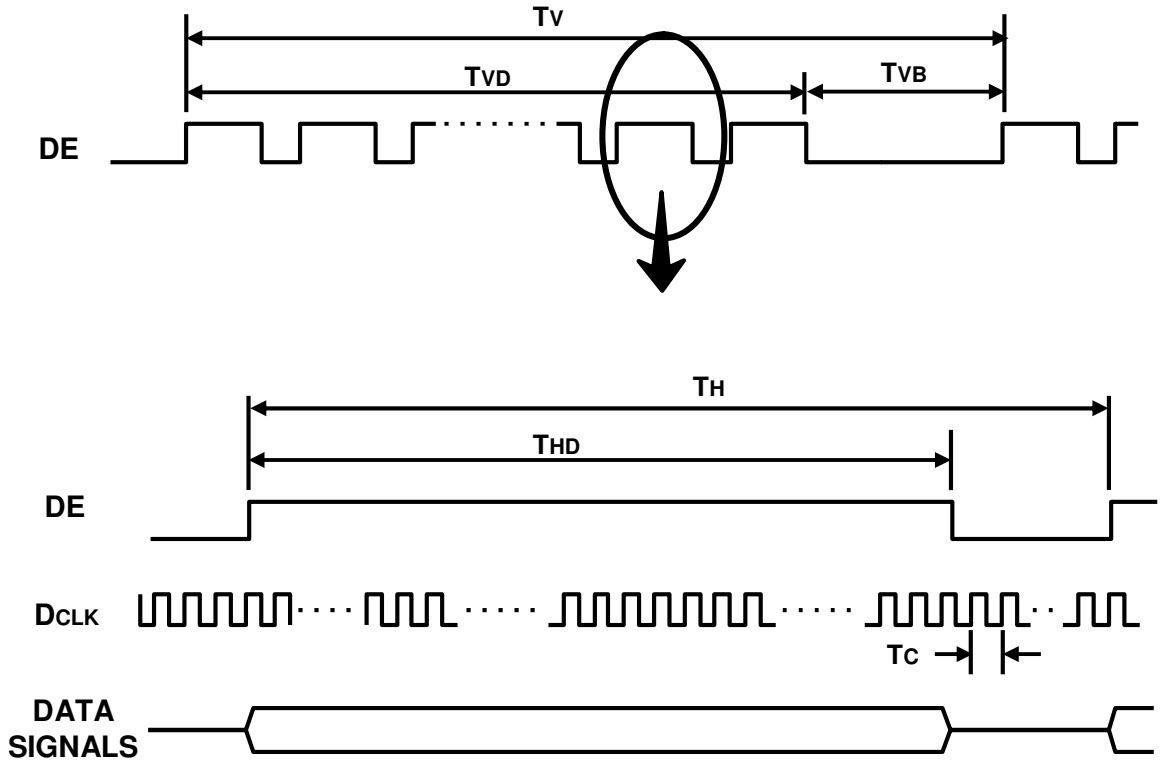
6.2 Timing diagrams of interface signal (DE only mode)



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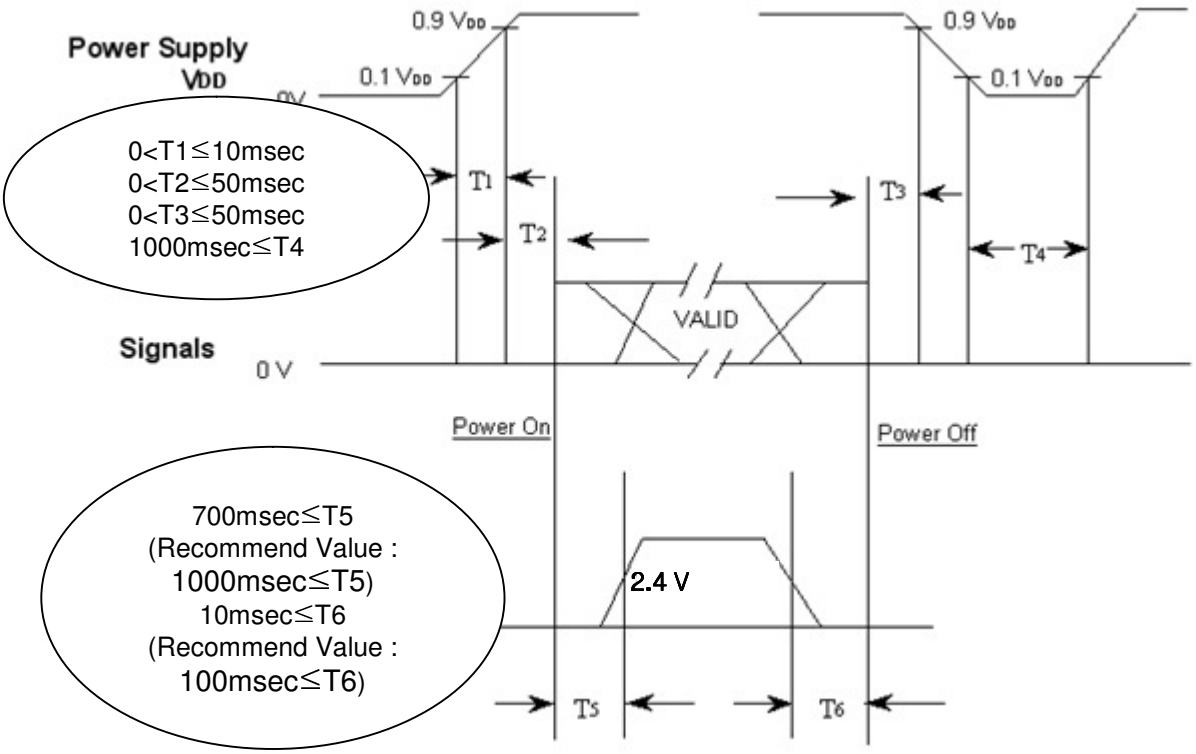
6.3 Power ON/OFF Sequence

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



T1 : V_{DD} rising time from 10% to 90%
T2 : The time from V_{DD} to valid data at power ON.
T3 : The time from valid data off to V_{DD} off at power Off.
T4 : V_{DD} off time for Windows restart
T5 : The time from valid data to B/L enable at power ON.
T6 : The time from valid data off to B/L disable at power Off.

- The supply voltage of the external system for the Module input should be the same as the definition of V_{DD}.
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V_{DD} = off level, please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.

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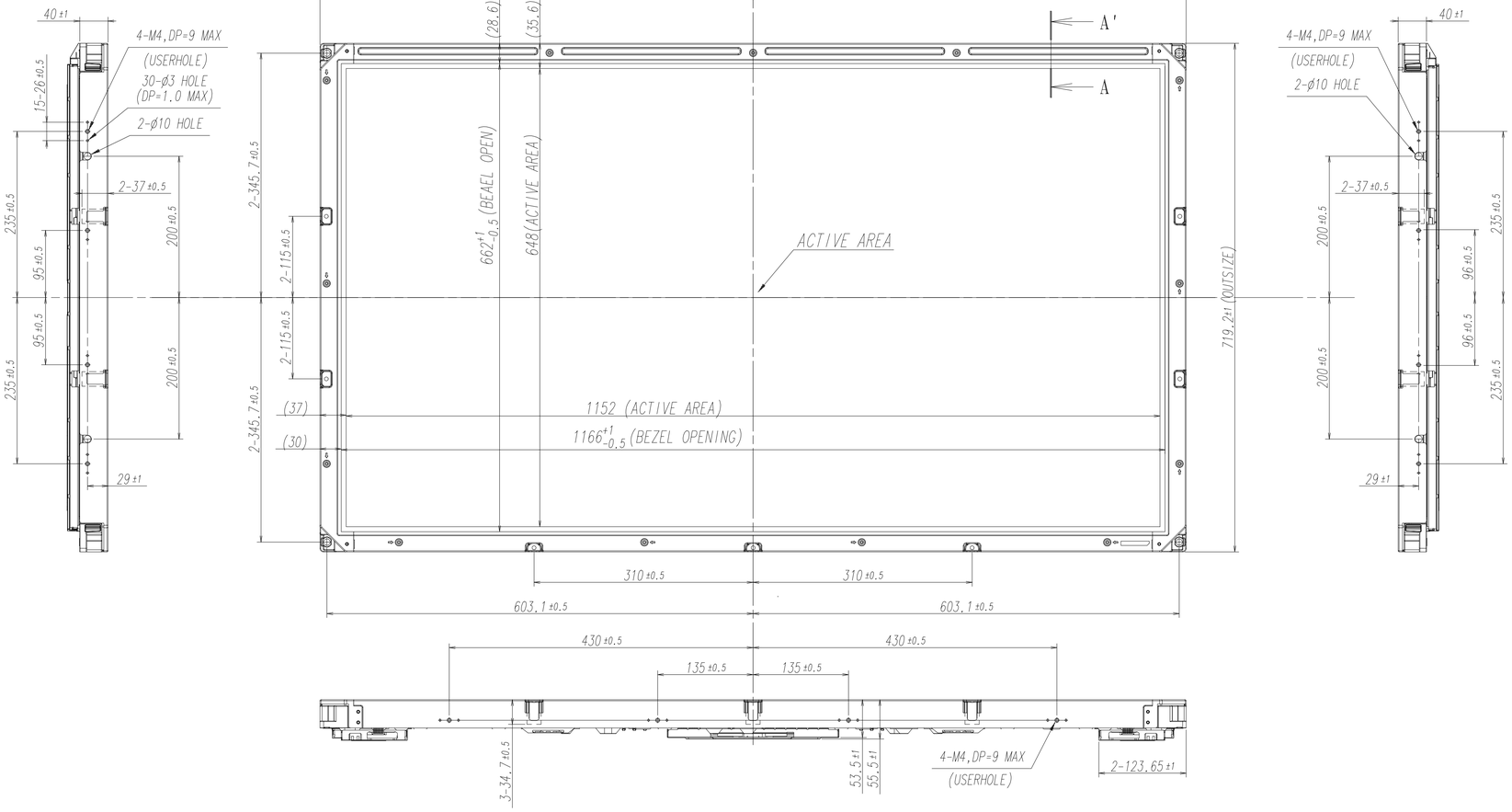
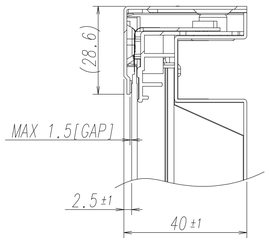
FILE NO.

NO	PART NAME	CODE NO	SPECIFICATION	Q'TY	WEIGHT FINISH	WEIGHT MATERIAL	UNFOLDED DIM. OF. MATERIAL	REMARK
	OUTLINE DIMENSION		LTA520HE15					



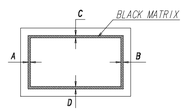


Section A-A'
(S:N/1)

* NOTES

1. BACKLIGHT : 24 COLD CATHODE FLUORESCENT LAMPS.
2. I/F CONNECTOR SPECIFICATION.(51PIN)
   - MAKER : JAE / UJU
   - PART NO. : FI-RE51S-HF / IS050-C51B-C38
3. I/F CONNECTOR SPECIFICATION.(41PIN)
   - MAKER : JAE / UJU
   - PART NO. : FI-RE41S-HF / IS050-C41B-C38
4. INVERTER CONNECTOR SPECIFICATION.(14PIN, 2EA)
   - MAKER : JST
   - PART NO. : S14B-PHA-SM-TB
5. USER HOLE: ALLOWED DEPTH OF USERHOLE SCREW INSERTION IS 9.0mm MAX.
6. GAP BETWEEN TOP CHASSIS AND GLASS(POL) IS 1.5mm MAX.
7. WEIGHT : Max 19Kg
8. SCREW TORQUE SPEC(MAX TORQUE)
   - M4 & M5 : 12.0kgf.cm
9. BLACK MATRIX SPEC

$-|A-B| \leq 2.0$ mm
$-|C-D| \leq 2.0$ mm



REVISION

GENERAL TOLERANCE			
STEP	LEVEL 1	LEVEL 2	LEVEL 3
0 < X ≤ 4	±0.05	±0.1	±0.2
4 < X ≤ 16	±0.08	±0.15	±0.3
16 < X ≤ 64	±0.12	±0.25	±0.5
64 < X ≤ 256	±0.25	±0.4	±0.8

REV	DATE	DESCRIPTION OF REVISION				REASON	CHG'D BY
UNIT	mm	DRA'N BY	DES'D BY	CHK'D BY	APP'D BY		
SCALE	N/S	D.W.KIM					
TOLERANCE	LEVEL 3	2008.05.26	. .				
SPEC. NO							

SAMSUNG ELECTRONICS

MODEL NAME	LTA520HE15		
PART/SHEET NAME	OUTLINE DIMENSION	SHEET	1/2
CODE NO.	(FRONT VIEW)	VER.	000

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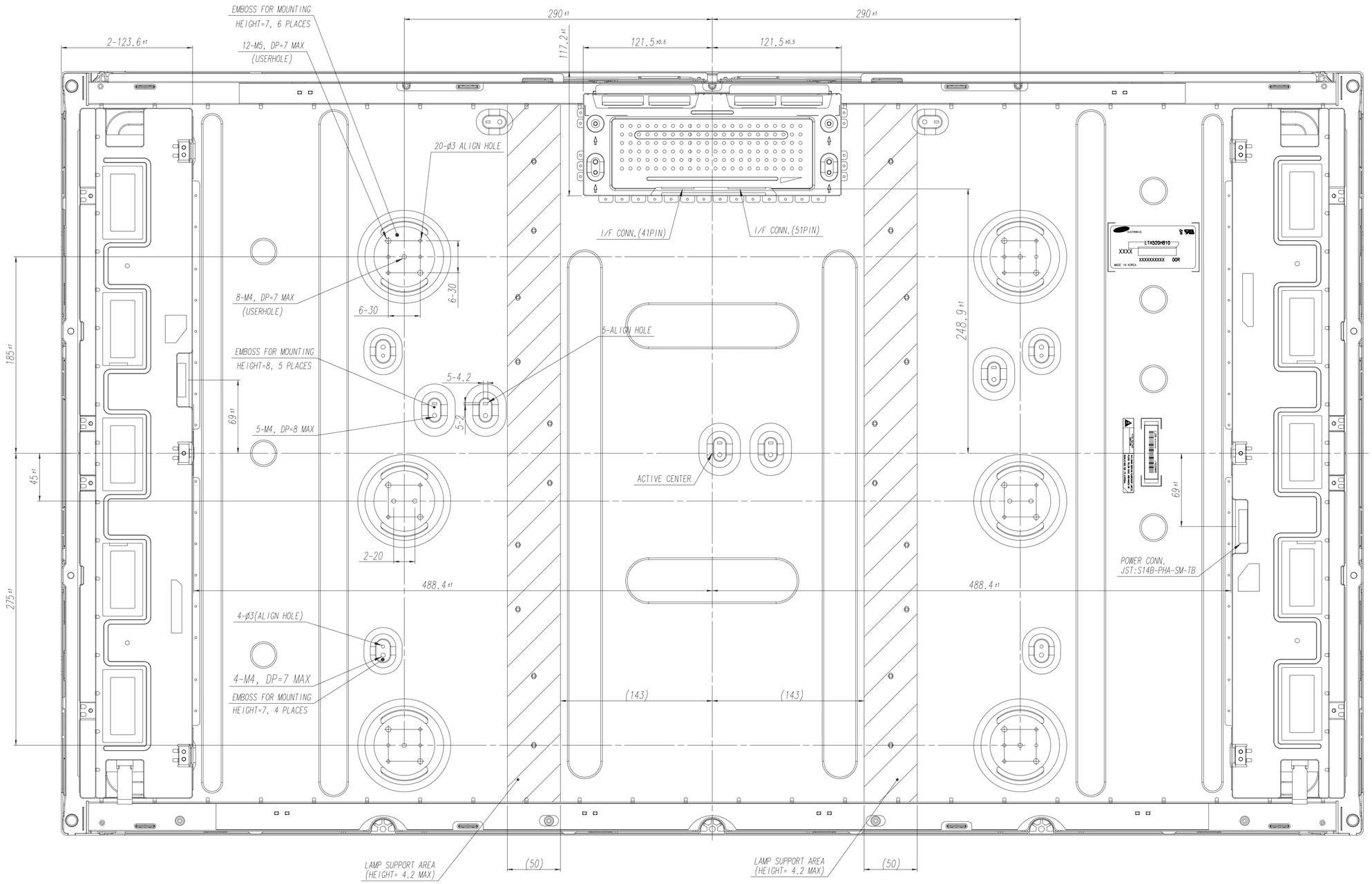
FILE NO.

NO	PART NAME	CODE NO	SPECIFICATION	Q'TY	WEIGHT FINISH	WEIGHT MATERIAL	UNFOLDED DIM. OF. MATERIAL	REMARK
	OUTLINE DIMENSION		LTA520HE15					



REVISION

GENERAL TOLERANCE			
STEP	LEVEL 1	LEVEL 2	LEVEL 3
0 < X ≤ 4	±0.05	±0.1	±0.2
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64 < X ≤ 256	±0.25	±0.4	±0.8

REV	DATE	DESCRIPTION OF REVISION				REASON	CHG'D BY
		UNIT mm	DRA'N BY	DES'D BY	CHK'D BY	APP'D BY		
		SCALE N/S	D.W.KIM				MODEL NAME	LTA520HE15
		TOLERANCE LEVEL 3	2008.05.26	. .			PART/SHEET NAME	OUTLINE DIMENSION SHEET 2/2
		SAMSUNG ELECTRONICS			SPEC. NO		CODE NO.	(REAR VIEW) VER. 000

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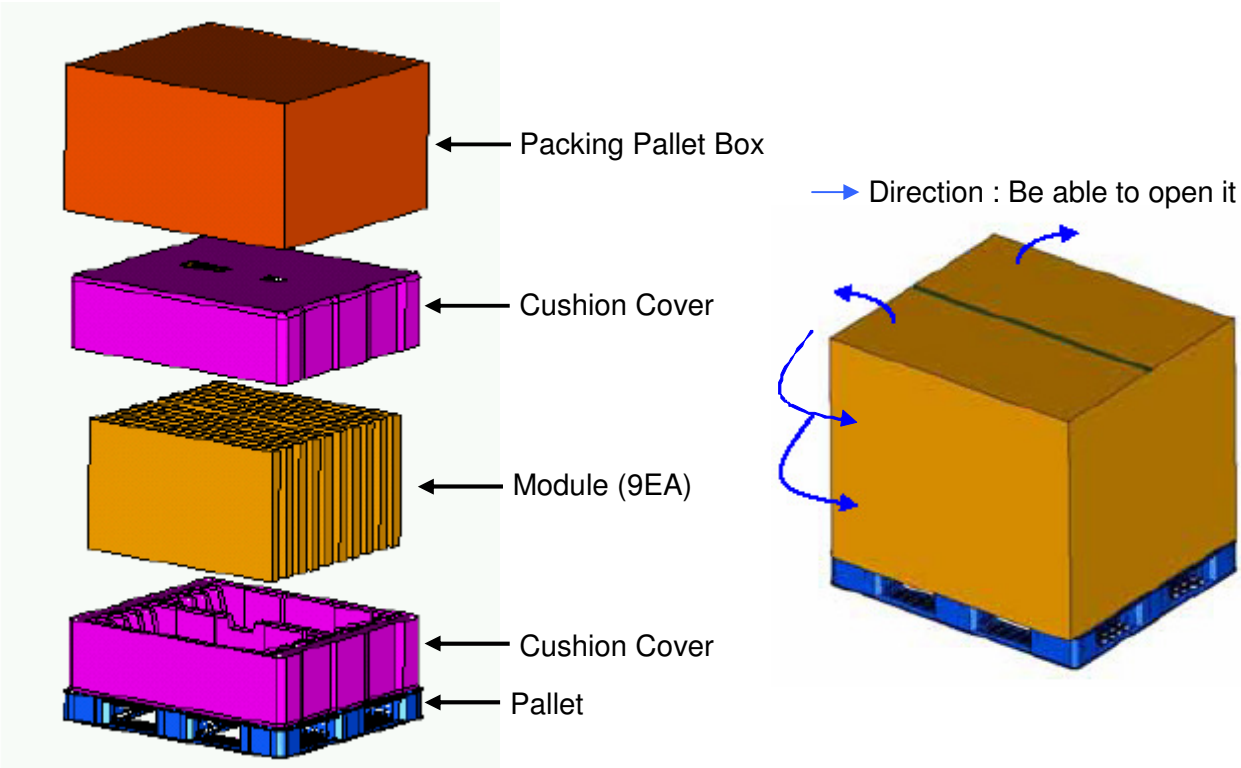
8. PACKING

8.1 CARTON (Internal Package)

(1) Packing Form

Corrugated fiberboard box and corrugated cardboard as shock absorber

(2) Packing Method



8.2 Packing Specification

Item	Specification	Remark
LCD Packing	9ea / (Packing-Pallet Box)	1. 162Kg / LCD (9ea) 2. 15.6 Kg / Cushion-pallet (2ea) 3. 10.5 Kg / Packing-Pallet Box (1ea) 4. Cushion-pallet Material : EPS 5. Packing-Pallet Box Material : DW4
Pallet	1Box / Pallet	1. Pallet weight = 10kg
Packing Direction	Vertical	
Total Pallet Size	H x V x height	1475mm(H) x 1150mm(V) x 995mm(height)
Total Pallet Weight	198.1kg	Pallet(10kg) + Module(18*9=162) + Cushion(15.6kg) + Pallet-BOX(10.5kg)

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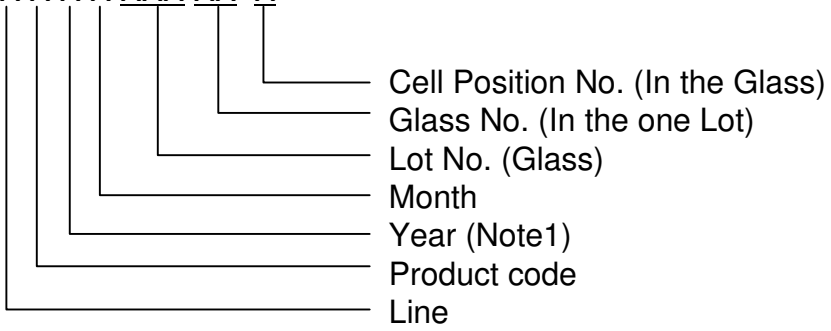
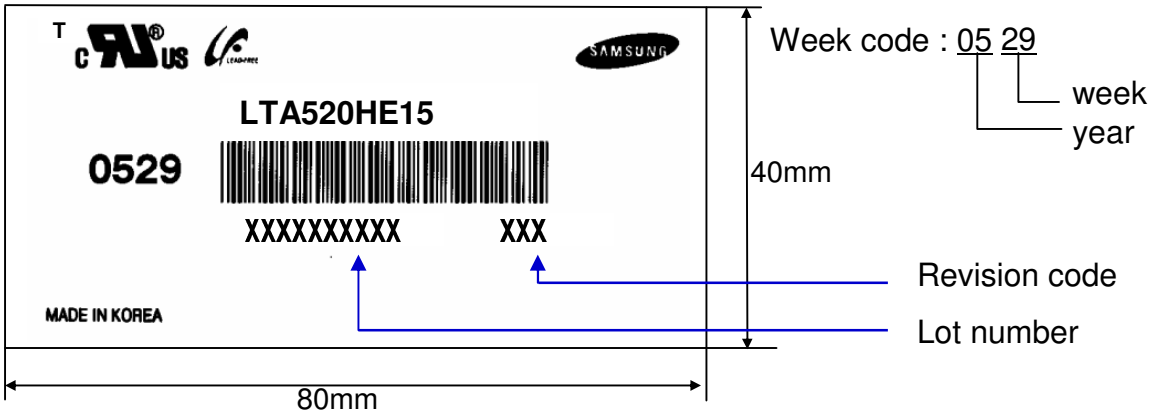
9. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

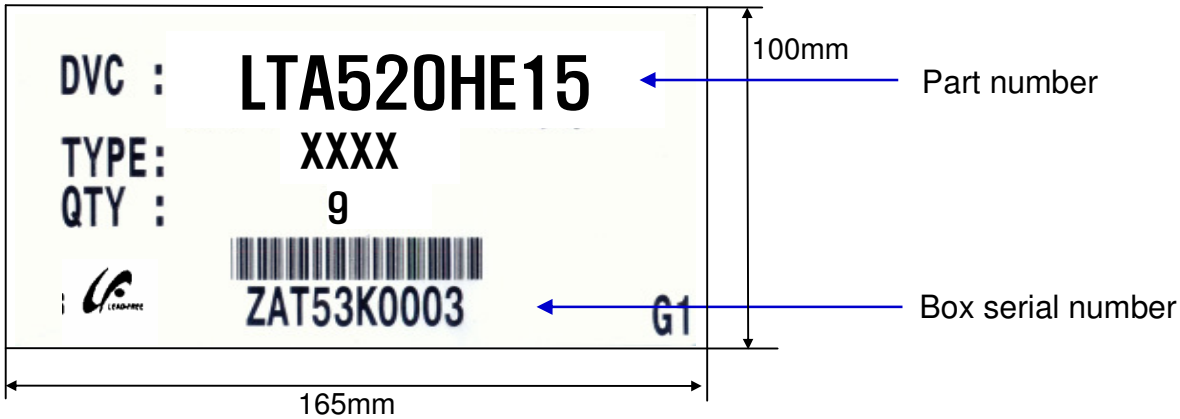
(1) Parts number : LTA520HE15
(2) Revision: Three letters
(3) Lot number : X X X X XXX XX X



(4) Nameplate Indication



(5) Packing box attach



(6) Others

1. After service part
   Lamps cannot be replaced because of the narrow bezel structure.

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10. General Precautions

10.1 Handling

(a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.

(b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.

(c) Refrain from strong mechanical shock and / or any force to the Module. In addition to damage, this may cause improper operation or damage to the Module and CCFT back light.

(d) Note that polarizers are very fragile and could be damage easily. Do not press or scratch the surface harder than a HB pencil lead.

(e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.

(f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.

(g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.

(h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.

(i) Protect the module from Electrostatic discharge. Otherwise the ASIC IC or semiconductor would be damaged.

(j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.

(k) Do not disassemble the Module.

(l) Do not disassemble shield case of inverter & LVDS board

(m) Do not connect N.C pins. (Samsung internal use only)

(n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized. Must put on antistatic glove while handling a module

(o) Pins of I/F connector should not be touched directly with bare hands.

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10.2 Storage

(a) Do not leave the Module in high temperature, and high humidity for a long time. It is highly recommended to store the Module with temperature from 0 to 35℃ and relative humidity of less than 70%.

(b) Do not store the TFT-LCD Module in direct sunlight.

(c) The Module should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light in storing.

10.3 Operation

(a) Do not connect or disconnect the Module in the "Power On" condition.

(b) Power supply should always be turned on/off by the "Power on/off sequence"

(c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

(d) The cable between the back light connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the back light and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage(Vs).

10.4 Operation Condition Guide

(a) The LCD product should be operated under normal conditions.
Normal condition is defined as below;
- Temperature : 20±15℃
- Humidity : 55±20%
- Display pattern : continually changing pattern (Not stationary)

(b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc.., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

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10.5 Others

(a) Ultra-violet ray filter is necessary for outdoor operation.

(b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.

(c) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)
Otherwise the Module may be damaged.

(d) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.
To avoid image sticking, it is recommended to use a screen saver.

(e) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.

(f) Please contact SEC in advance when you display the same pattern for a long time.

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