

PRODUCT SPECIFICATION

- () PRODUCT INFORMATION
- (√) APPROVAL SPECIFICATION

Any modification of specification is not allowed without SDC's permission

CUSTOMER	VD
PROGRAM	-

MODEL	LTM220MT12
EXTENSION CODE	V01/V02

CUSTOMER APPROVAL & FEEDBACK

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Application Engineering Group Samsung Display Co., Ltd.

Product Configuration Approval Sheet

Description

Items	Content
Customer	VD
Product Name	LTM220MT12
Project Name	-

Customer System Configuration

	Items	Content
System Name		
	Purpose	Standard MNT
IC	Scalar	_
IC	LED Driver	-
Input Interface		-
OS (AIO)		-
Grapl	hic Card (AIO)	-

Notice: SDC product approval spec guarantee a above customer system.



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Revision History

Version	Date	Page	Description
P0.0	6. Mar., 2013	All	Product information
P0.1	28. Mar., 2013	8 13 15	Color Chromaticity update Electronic characteristics update LED voltage, consumption update
A1.0	19. Jun.,2013	All	Approval Specification



1. General Description

Overview

LTM220MT12 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT (Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit. The resolution of a 22.0'' is 1680×1050 (WSXGA+) and this model can display up to 16.7 million colors.

Features

Application

- Workstation & Desktop monitors
- Display terminals for AV Products
- Monitors for Industrial machine

DE (Data Enable) only mode

LVDS (Low Voltage Differential Signaling) interface (2pixel/clock)

RoHS, Halogen Free

White LED Edge slim Backlight (1-side)

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TCO 6.0 compliance

General Information

Items	Specification	Unit
Pixel Pitch	0.282(H) x 0.282(W)	mm
Active Display Area	473.76(H) x 296.1(V)	mm
Surface Treatment	AG type, Haze 25%, Hard coating (3H)	-
Display Colors	16.7M (Hi-FRC)	colors
Number of Pixels	1,680 x 1,050	pixel
Pixel Arrangement	RGB vertical stripe	-
Display Mode	Normally White	-
Luminance of White	250 (Typ.)	cd/m²
Power Consumption	Total 14.5W(Typ.)(Panel 5.5W / BLU 9.0W)	W



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Mechanical Information

Item		Min.	Тур.	Max.	Unit	Note
	Horizontal (H)	493.2	493.7	494.2	mm	
Module size Vertical (Vertical (V)	319.6	320.1	320.6	mm	-
Depth (D)		9.7	10.2	10.7	mm	-
Weight		-	-	2,200	g	LCD module only

Note (1) Mechanical tolerance is \pm 0.5mm unless there is a special comment.

2. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V _{DD}	GND-0.5	6.5	V	(1)
Operating Temperature	T _{OPR}	0	50	င	(2)
Storage temperature	T _{STG}	-20	60	°C	(2)
Glass surface temperature (Operation)	T _{SUF}	0	65	°C	(3)

Note (1) Ta= 25 \pm 2 °C



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- (2) Temperature and relative humidity range are shown in the figure below.
 - a. 90 % RH Max. ($Ta \le 39 \, ^{\circ}C$)
 - b. Maximum wet-bulb temperature at 39 °C or less. (Ta ≤ 39 °C)
 - c. No condensation.
- (3) The maximum operating temperature of LCD module is defined with surface temperature of active area. Under any conditions, the maximum ambient operating temperature must keep the surface of active area not higher than 65 °C.

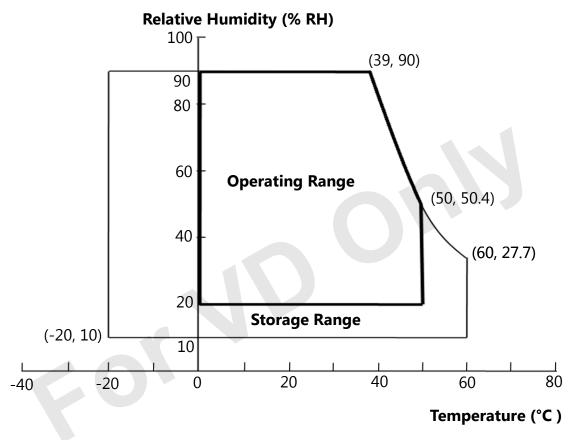


Fig. Temperature and Relative Humidity range



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3. Optical Characteristics

The optical characteristics must be measured in a dark room or equivalent. Measuring equipment: SR-3, RD-80S (TOPCON), EZ-Contrast (Eldim)

 $(Ta = 25 \pm 2^{\circ}C, VDD=5V, fv=60Hz, f_{DCLK}=59.6MHz, If =300mA)$

	`		•	-		DCLK 3313		
Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Contrast Ratio (Center of screen)		C/R		700	1000	-		(3) SR-3
Response Time	On/Off	Tr + Tf		-	5	10	msec	(5) RD-80S
Luminance of (Center of sc		Y _L		200	250	-	_	(6) SR-3
Brightness Unit (9 Points	-	B _{uni}		-	-	25	%	(4) SR-3
		Rx			0.635			
	Red	Ry			0.338			
	Green	Gx		- 0.030	0.334	+0.030		
Color		Gy	Normal		0.625			
Chromaticity (CIE 1931)		Bx	$\theta_{L,R} = 0$		0.156			
	Blue	Ву	θ _{υ,σ} =0 Viewing		0.044			
	VA/Init a	Wx			0.313			
	White	Wy	Angle		0.329			(7),(8)
	Dod	Ru'		-	0.439	-		SR-3
	Red	Rv'		-	0.526	-		
Cala	Cuana	Gu'		-	0.136	-		
Color Chromaticity (CIE 1976)	Green	Gv'		-	0.572	-		
	Dless	Bu'		-	0.195	-		
	Blue	Bv'		-	0.123	-		
	White	Wu'		-	0.198	-		
	vviile	Wv'		-	0.469	-		

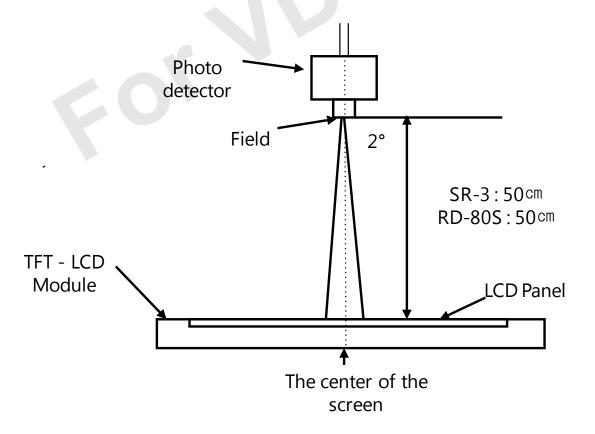


Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Color Gam	ut	-		ı	72	-	%	
Color Tempera	ature	-		-	6500	-	K	
	Hor. Viewing	θ_{L}	CR≥10	70	85	-		
Viewing		θ_{R}		70	85	-	Dograce	(8) EZ-
Angle	Vor	θ _U		70	80	-	Degrees	Contrast
	Ver.	$\theta_{\scriptscriptstyle D}$		70	80	-		

Note (1) Test Equipment Setup

The measurement must be executed in a stable, windless and dark room between 30min after lighting the back light at the given temperature for stabilization of the back light. This must be measured in the center of the screen.

LED forward current : If = 300mA Environment condition : Ta = 25 ± 2 °C

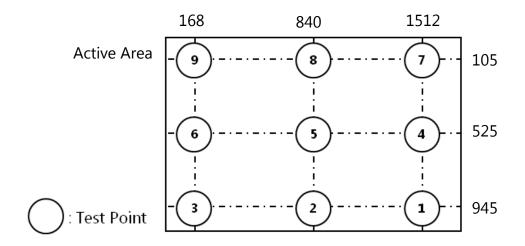




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(2) Definition of test point



(3) Definition of Contrast Ratio (CR)

: Ratio of gray max (G_{max}) & gray min (G_{min}) at the center point 5 of the panel

$$CR = \frac{G_{max}}{G_{min}}$$

 G_{max} : Luminance with all white pixels G_{min} : Luminance with all black pixels

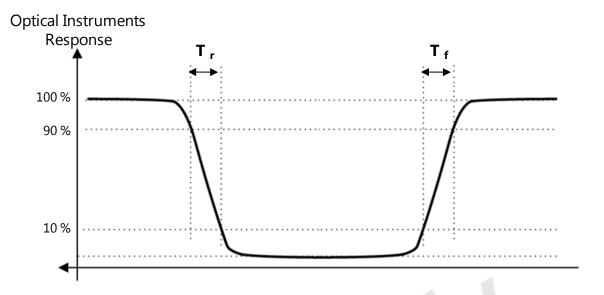
(4) Definition of 9 points brightness uniformity

$$B_{uni} = 100 \times \frac{B_{max} - B_{min}}{B_{max}}$$

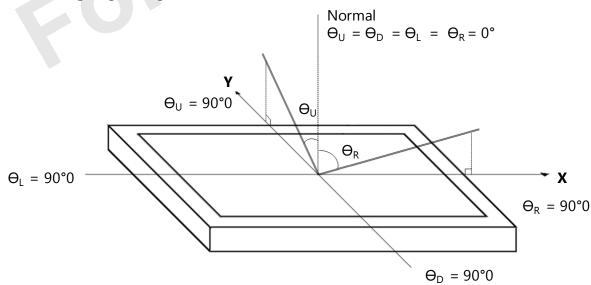
 B_{max} : Maximum brightness B_{min} : Minimum brightness



(5) Definition of Response time: Sum of Tr and Tf



- (6) Definition of Luminance of White: Luminance of white at center point (5)
- (7) Definition of Color Chromaticity (CIE 1931, CIE1976)
 Color coordinate of Red, Green, Blue & White at center point (5)
- (8) Definition of Viewing Angle: Viewing angle range (CR ≥ 10)





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4. Block Diagram

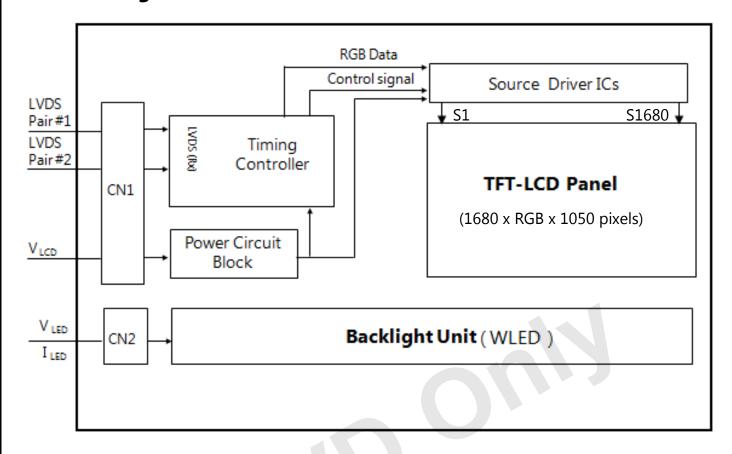


Fig. Function block diagram

Note (1) The connector of display data & timing signal must be connected



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5. Electrical Characteristics

5.1 TFT LCD Module

The connector of display data & timing signal must be connected.

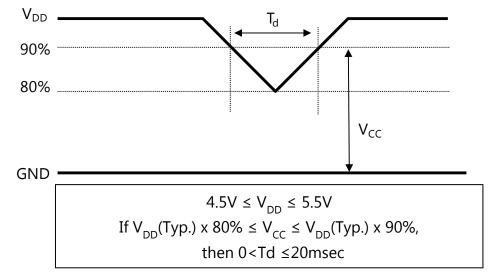
 $Ta=25 \pm 2$ °C

	Symbol	Min.	Тур.	Max.	Unit	Note		
Voltage of	f Power Supply	V _{DD}	4.5	5.0	5.5	٧	(1)	
Daa. D	V _{cc}	4.0	-	V _{DD}	V			
Power L	ip Condition	T _d	0	-	20	msec	(2)	
	(a) White		-	800	-	mA	(3),(4)	
Current of Power	(b) Black	I _{DD}	-	1500	-	mA		
Supply	(c) Mosaic	-DD	-	1100	1300	mA	(3),(4)	
	(d) Dot		-	1700	_			
Power C	P _{LCD}		5.5	-	Watt	(4),(5)		
Rusł	n Current	I _{RUSH}	-	-	5.0	А	(6)	

Note (1) The ripple voltage should be controlled under 10% of V_{DD}

(2) Definition of V_{DD} Power Dip

- The chart of conditions shown above conditions are for the glitch of the input voltage.
- It is highly crucial to follow the chart, for stable operation of an LCD module power.

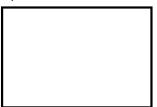




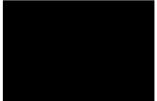
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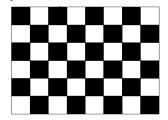
- (3) $f_V = 60$ Hz, $f_{DCLK} = 59.6$ MHz, $V_{DD} = 5.0$ V, DC Current.
- (4) Power dissipation check pattern (LCD Module only)
 - a) White Pattern



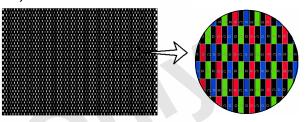
b) Black Pattern



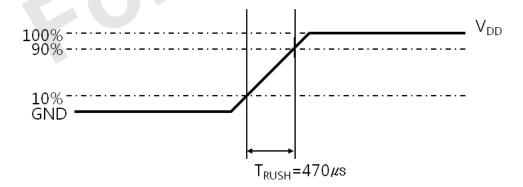
c) Mosaic Pattern



d) Dot Pattern



- (5) The power consumption is specified whereas Mosaic pattern is displayed at $f_V = 60$ Hz, $f_{DCLK} = 59.6$ MHz, $V_{DD} = 5.0$ V
- (6) Measurement Condition



Rush Current I_{RUSH} can be measured when T_{RUSH} . is 470 μ s



5.2 Backlight Unit

The characteristics of LED bar

 $Ta=25 \pm 2^{\circ}C$.

Item	Symbol	Min.	Тур.	Max.	Unit	Note
LED Forward Current	I _F	-	300	390	mA	(1),(2)
LED Array Voltage	V _P	-	30.0	-	V	(1)
Power Consumption	P _{BLU}	-	9.0	-	Watt	(3)
Operating Life Time	Hr	40,000	-	-	Hour	(4)

Note (1) The specifications shown above are not for the converter output, but for the LED bar.

- The LED bar consists of 30 LED packages; 3 parallel X 10 serial
- LED current is defined at 100% duty ratio of LED driver
- (2) The LED Forward current for single LED channel is Typ.100mA
 - The output current of converter in the system must be transmitted to the LED bar constantly.
 - It is recommended to control the returned signal respectively for even distribution of current to each channel of LED bar
- (3) The power consumption is specified at typical current 300mA with 100% duty ratio
 - It does not include power loss of external LED driver circuit block
 - Typical power consumption $P_{BLU} = I_F$ (Typ.) x V_P (Typ.)
- (4) Life time(Hr) is defined as the time when brightness of a LED package itself becomes 50% or less than its original value at the condition of Ta=25 \pm 2°C and I_F =300mA.



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5.3 LVDS Characteristics

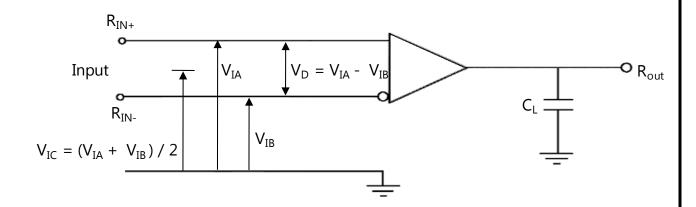
5.3.1. LVDS Input Characteristics

 $Ta=25 \pm 2$ °C

Item	Symbol	Min.	Тур.	Max.	Unit	Note	
Differential Input Voltage for LVDS	High	-	-	+50	mV	(1)	
receiver threshold	Low	-50	-	ı	mV	(1)	
LVDS skew	t _{skew}	-270	-	270	ps	(2)	
Differential input voltage	IV _{id} I	100	-	600	mV	(3)	
Input voltage range(single ended)	V _{in}	0.0	-	2.4	V	(3)	
Common mode voltage	V _{cm}	0.4	1.2	2.9	V	(3)	

Note (1) Differential receiver voltage definitions and propagation delay and transition time test circuit

- a. All input pulses have frequency of 10MHz, t_R or t_F =1ns
- b. C_L includes all probe and fixture capacitance



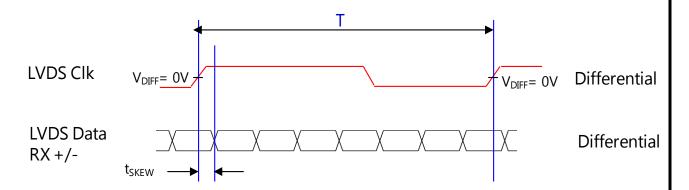


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(2) LVDS Receiver DC parameters are measured under static and steady conditions which may not be reflective of its performance in the end application.

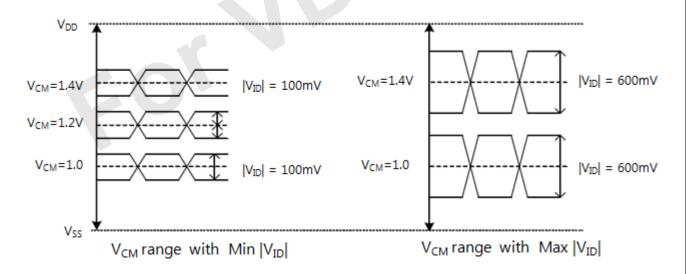


where t_{SKEW}: skew between LVDS clock & LVDS data,

T: 1 period time of LVDS clock

cf. (-/+) of 300psec means LVDS data goes before or after LVDS clock

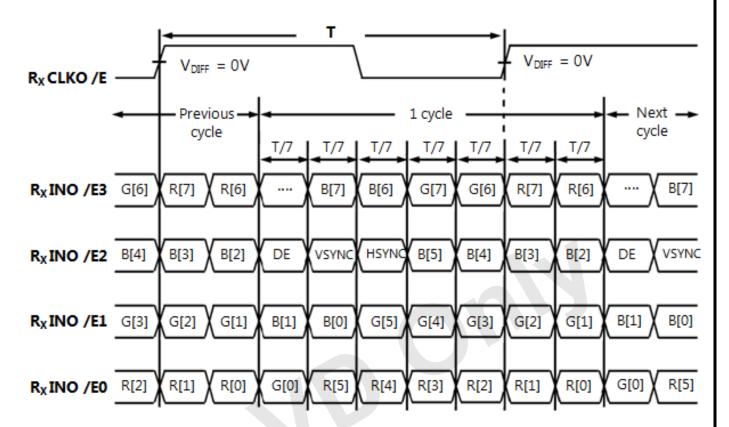
(3) Definition of V_{ID} and V_{CM} using single-end signals



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5.3.2. LVDS Data Format

Timing Diagrams of LVDS For Transmitting
- LVDS Receiver : Integrated T-CON





5.4 Interface Timing Specification

5.4.1. Timing Parameters

SIGNAL	ITEM	SYMBOL	Min.	Тур.	Max.	Unit	Note
Clock		1/T _C	49.68	59.6	76.50	MHz	-
Hsync	Frequency	F _H	54.0	64.8	83.2	kHz	-
Vsync		F _V	50	60	77	Hz	-
Vertical Display Term	Active Display Period	T _{VD}	1050	1050	1050	Lines	-
	Vertical Total	T _V	1059	1080	1100	Lines	-
Horizontal	Active Display Period	T _{HD}	840	840	840	Clocks	2pixel/clock
Display Term	Horizontal Total	T _H	913	920	1004	clocks	2pixel/clock

Note (1) DE only mode

- While operation, DE signal must have the same cycle.
- (2) Best operation clock frequency is 59.6MHz(60Hz)
- (3) Max, Min variation range is at main clock typical value (59.6MHz)
- (4) Main frequency Max is 76.5MHz without spread spectrum



SAMSUNG DISPLAY 5.4.2. Timing diagrams of interface signal (DE only mode) T_V T_{VD} T_{VB} DE T_H T_{HD} DE D_{CLK} T_C → DATA **SIGNALS** T_{C} $T_{\underline{CH}}$ T_{CL} $\mathsf{D}_{\mathsf{CLK}}$ 0.5 V_{CC} T_{DS} T_{DH} **DISPLAY** · 0.5 V_{CC} DATA T_{ES} DE SAMSUNG **A1.0** LTM220MT12 19. Jun. 2013 20/36

5.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

												DA	ATA S	SIGNA	٩L											GRAY
COLOR	DISPLAY (8bit)				RE	D							GRI	EEN							BL	UE				SCALE
	(53.4)	R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	В0	B1	B2	В3	В4	B5	В6	В7	LEVEL
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
BASIC	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
COLOR	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
		1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
CDAY	DARK	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
GRAY SCALE OF	<u>†</u>	:	:	•	••	:	:			•	ě	:	:	:):	:			
RED	↓ LIGHT	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R253
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R254
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R255
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
		0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
	DARK	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
GRAY SCALE OF GREEN		:					:			:	:	:	:	:	:			:	• •		:	:	:			
	LIGHT	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G253
		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G254
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G255
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	В0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	B1
	DARK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	В2
GRAY SCALE OF BLUE	1	:	:		• •	•••	÷			••	••	:	••	:	••			:	• •	•••	:	:	÷			
	LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	B253
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	B254
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B255

Note (1) Definition of Gray

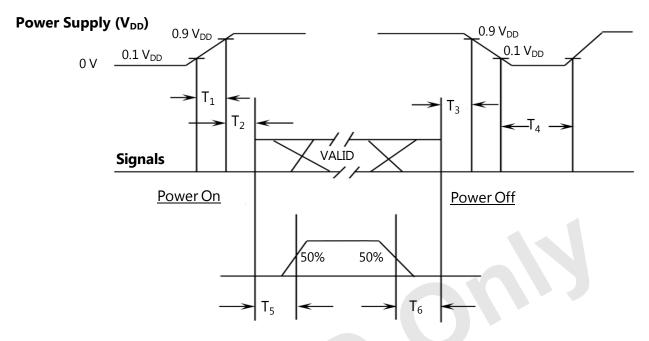
- Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level) Input Signal : 0 = Low level voltage, 1 = High level voltage



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5.6 Power ON/OFF Sequence

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence must be as the diagram below.



SYMBOL	Min.	Тур.	Max.	Unit	Description
T ₁	0.5	_	10	ms	V _{DD} rising time from 10% to 90%
T ₂	0.01	-	50	ms	The time from V _{DD} to valid data at power ON
T ₃	0.01	-	50	ms	The time from valid data off to $V_{\scriptscriptstyle DD}$ off at power Off
T ₄	1		_	S	V _{DD} off time for Windows restart
T ₅	500	_	-	ms	The time from valid data to B/L enable at power ON
T ₆	100	-	-	ms	The time from valid data off to B/L disable at power Off

- Note (1) The supply voltage of the external system of the Module input must be the same as the definition of VDD.
 - (2) Apply the BLU power within the LCD operation range. When the back light is turned on before the LCD operation or the LCD turns off before the back light is turned off, the display may momentarily show abnormal screen.
 - (3) In case of V_{DD} = off level, please keep the level of input signals low or keep a high impedance.
 - (4) T4 has to be measured after the Module has been fully discharged between the period of power off and on.
 - (5) Interface signal must not be kept at high impedance when the power is on.



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5.7 Input Terminal Pin Assignment

5.7.1. Input signal & Power Pin Assignment

Connector: UJU IS100-L30B-C23 or equivalent

Pin No.	Symbol	Function
1	RXO0N	Negative Transmission Data of Pixel 0 (ODD data)
2	RXO0P	Positive Transmission Data of Pixel 0 (ODD data)
3	RXO1N	Negative Transmission Data of Pixel 1 (ODD data)
4	RXO1P	Positive Transmission Data of Pixel 1 (ODD data)
5	RXO2N	Negative Transmission Data of Pixel 2 (ODD data)
6	RXO2P	Positive Transmission Data of Pixel 2 (ODD data)
7	GND	Power Ground
8	RXOC-	Negative Sampling Clock (ODD data)
9	RXOC+	Positive Sampling Clock (ODD data)
10	RXO3N	Negative Transmission Data of Pixel 3 (ODD data)
11	RXO3P	Positive Transmission Data of Pixel 3 (ODD data)
12	RXE0N	Negative Transmission Data of Pixel 0 (EVEN data)
13	RXE0P	Positive Transmission Data of Pixel 0 (EVEN data)
14	GND	Power Ground
15	RXE1N	Negative Transmission Data of Pixel 1 (EVEN data)
16	RXE1P	Positive Transmission Data of Pixel 1 (EVEN data)
17	GND	Power Ground
18	RXE2N	Negative Transmission Data of Pixel 2 (EVEN data)
19	RXE2P	Positive Transmission Data of Pixel 2 (EVEN data)
20	RXEC-	Negative Sampling Clock (EVEN data)
21	RXEC+	Positive Sampling Clock (EVEN data)
22	RXE3N	Negative Transmission Data of Pixel 3 (EVEN data)
23	RXE3P	Positive Transmission Data of Pixel 3 (EVEN data)
24	GND	Power Ground
25	NC	* Reserved for LCD manufacturer's use (CE_DVR)
26	NC	* Reserved for LCD manufacturer's use (CTL_DVR)
27	NC	No Connection
28	VDD	
29	VDD	Power Supply : +5V
30	VDD	



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- Note (1) If the system already uses the 25, 26pins, it must be keet under GND level The voltage applied to those pins must not exceed -200mV.
 - (2) Pin number starts from the Left

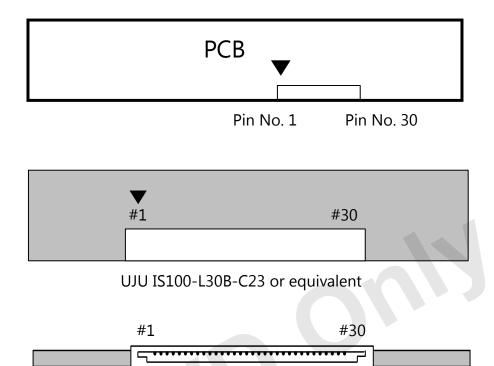


Fig. Connector diagram

- (3) All GND pins must be connected to each other and also be connected to the LCD's metal chassis.
- (4) All power input pins must be connected to each other.
- (5) All NC pins must be separated from other signal or power



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5.7.2. LED Connector Pin assignment

Connector: Molex 104086-0410 or equivalent

- The mating type connector: Molex 104085-0400 or equivalent

Pin No.	Symbol	Function				
1	Vin	LED power input				
2	RTN 1	Channel 1 LED return				
3	RTN 2	Channel 2 LED return				
4	RTN 3	Channel 3 LED return				

Note (1) Pin number starts from the left

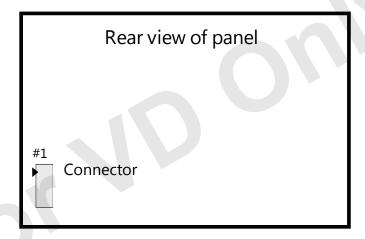




Fig. Connector diagram



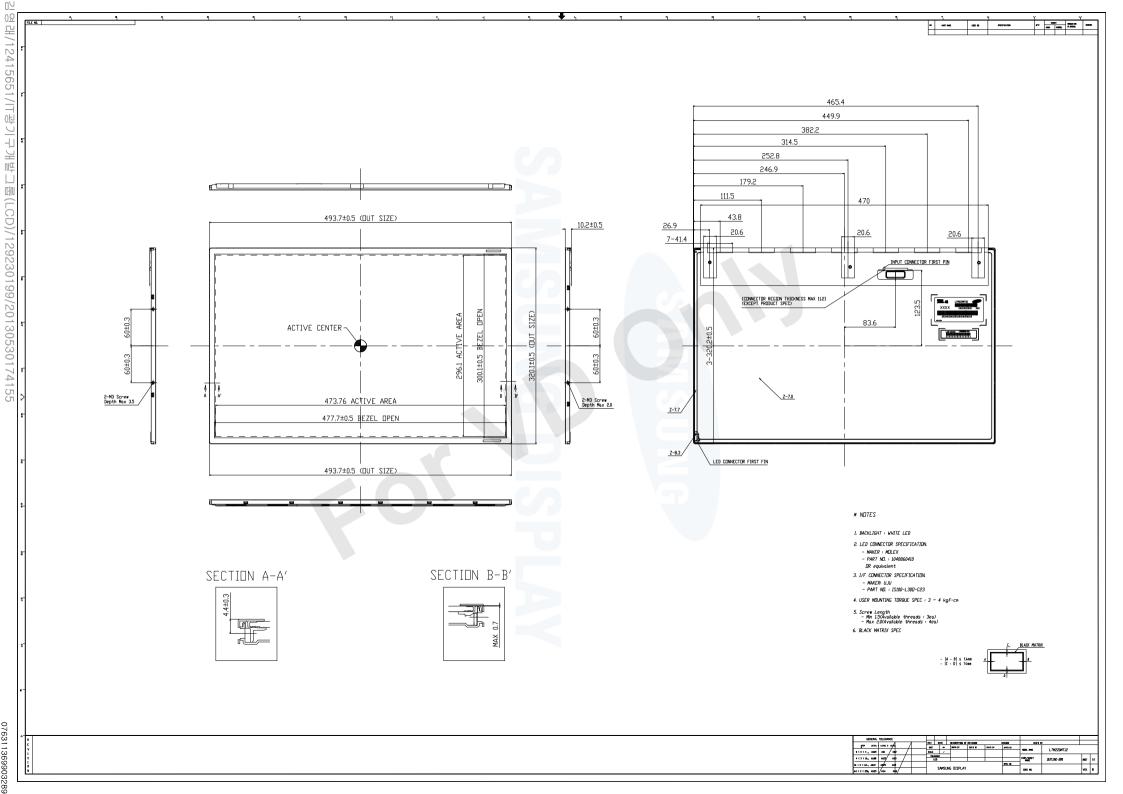
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6. Outline Dimension

[Refer to the next page]







7. Reliability Test

Te	est Item	Condition	Time/Cycle
	HTOL	50°C , Bias	500 hrs
	LTOL	0°C , Bias	500 hrs
	THB	40°C / 90% , Bias	500 hrs
	HTS	70°C , No Bias	500 hrs
	LTS	-20°C , No Bias	500 hrs
The	rmal Cycle	-20°C/30min ~ +60°C/30min , No bias	100 cycle
	Shock -operating)	50G , 11msec Sine wave , ± x/y/z axis	1time/axis
	ibration -operating)	1.47G , 5~200 Hz x/y/z axis , sweep rate : 10 min	30min/axis
	Non- Operating	CDM : 150pF, 330Ω, 9point, 3 times/point	± 10kV
ESD		Contact : 150pF, 330Ω, 100point, once/point	± 8kV
	Operating	Air(non-contact) : 150pF, 330Ω, 100point, once/point	± 15kV
А	ltitude	Thermal :-10~50 $^{\circ}$ C, 15000ft(Operating), 40000ft(Non-operating) Normal :45 $^{\circ}$ C, 15000ft	8Hr 10Hr

[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these should be no change which may affect practical display functions.

- HTOL / LTOL : High/Low Temperature Operating Life

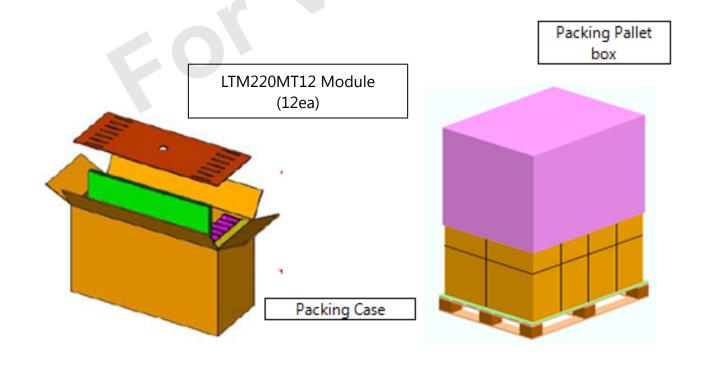
THB : Temperature Humidity BiasHTS/LTS : High/Low Temperature Storage



8. Packing

8.1 Carton

Item	Packing form	Specification
Weight	-	- Total Weight (Including Pallet) : Approx. 338Kg
Packing case	12 panels in a case	- Packing Case Size : W263 x L596 x H375 - Material : Paper (SW, DW)
Pallet box	16 cases in a box 192 panels in a box	- Packing Pallet Box Size : W1072 x L1212 x H740 - Material : Paper (SW)
Pallet	-	- Pallet Size : W1270 x L1150 x H122 - Material : Wood



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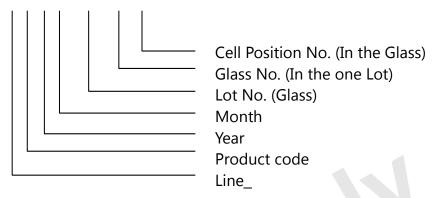
8.2 Marking

A nameplate bearing shown below is attached to a shipped product at the specified location

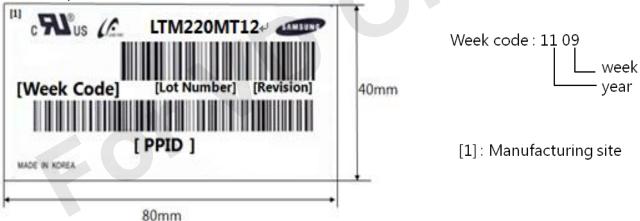
(1) Parts number: LTM220MT12

(2) Revision: Three letters

(3) Lot number: X X X X X XXX XX XX

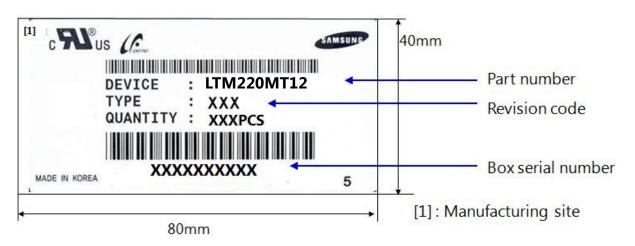


(4) Nameplate Indication



(5) Packing box attach

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9. General Precautions

9.1 Handling Precautions

- A. When assembling LCD module into its system, using all the mounting holes is strongly suggested.
- B. Keep LCD module from any external shock or force which can cause physical damage to LCD module. It may cause improper operation or damage to LCD module.
- C. Polarizer films are very fragile. It could be damaged easily. Do not press or scratch the surface harder than a HB pencil lead.
- D. Wipe off water droplets or oil immediately. Water drops or oils can cause permanent stain or discoloration.
- E. To clean LCD module, please use IPA (Isopropyl Alcohol) or Hexane.
- F. Do not use ketone type material (ex. Acetone), ethyl alcohol, toluene, ethyl acid or methyl chloride. Using these could cause permanent polarizer damage to the LCD module.
- G. If the liquid crystal leaks from LCD module, keep it away from human eyes or mouth. In case of contact with human body or clothes, it should be washed with soap thoroughly.
- H. Protect LCD module from static discharge.
- I. To keep the LCD module clean, make sure to wear fabric gloves and finger coats when you are inspecting and/or assembling the unit.
- J. Do not disassemble LCD module.
- K. Protection film on LCD module display area should be slowly peeled off just before assembly to prevent static discharge.
- L. Pins of the Interface connector should not be touched directly with bare hands.



9.2 Storage Precautions

It is highly recommended to comply with the criteria in the table below

Item	Unit	Min.	Max.						
Storage Temperature	(℃)	5	40						
Storage Humidity	(%rH)	35	75						
Storage life		12 months							
Storage Condition	Control - Products should not be from a wall - Prevent products from a Be cautious of a build u - Avoid other hazardous of 3 months, the recom	environment while storing kept in conditions of over mended temperature or heave them at a temperatur	on the Pallet away or water; goods. The storage period numidity range,						



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9.3 Operating Precautions

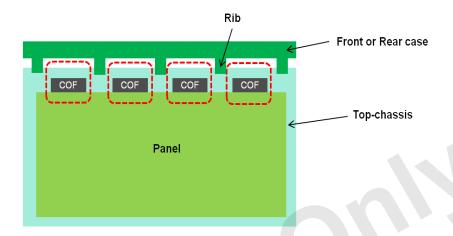
- A. If the module is used to other applications besides the recommendation on General Description, please contact SAMSUNG for application engineering device in advance
- B. Do not connect or disconnect the LCD module when it is set to the "Power On" condition.
- C. Input power should always follow '5.6 Power on/off sequence'
- D. Polarizer films are very fragile. It could be damaged easily. Do not press or scratch the Polarizer films
- E. LCD module contains electrical circuits that operate in high frequencies. To minimize electromagnetic interference, be sure to sufficiently ground and shield the LCD module and system.
- F. If LCD module containing system is out of SAMSUNG 's operating condition, SAMSUNG can not guarantee LCD module operating properly.
- G. If the product will be used in extreme conditions such as high temperature, humidity, display patterns, operation time, etc., it is strongly recommended to contact SAMSUNG for application engineering device. Otherwise, the reliability and function of the module may not be guaranteed. Extreme conditions are commonly found at airports, transit stations, banks, stocks, markets, and controlling systems.
- H. Ultra-violet ray filter is necessary for outdoor operation.
- I. If the module keeps displaying the same pattern for a long period of time, the image maybe burned in to the screen. To avoid image retention, it is recommended to use a screen saver.
- J. This module has its PCB's circuitry on the rear side and should be handled carefully in order to avoid stress.
- K. Please contact SAMSUNG beforehand, if you plan to display the same pattern for a long period of time.
- L. Any foreign materials brought into an LCD module by external forced-airflow are not guaranteed by SAMSUNG .



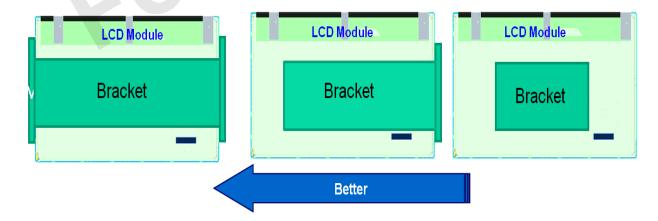
9.4 Design Guide for System

- A. The LED driver should be designed in compliance with the specifications of LED bar strictly to make the LED in LCD module perform as expected.
- B. It is recommended that you locate the rib on the front or rear cover not to be placed on the spot where D-IC is located on the upper or left of LCD module

(See '6. Outline Dimension ' for the exact location of driver ICs)



- C. It is recommended that assemble the bracket which has two sides with holes for assembly.
- D. It is recommended that you design the bracket with the structure which covers the sides of module when designing the bracket for customer.
- E. It is recommended that you design the bracket not to be interfered with the SET at the area where the PBA of module is located.



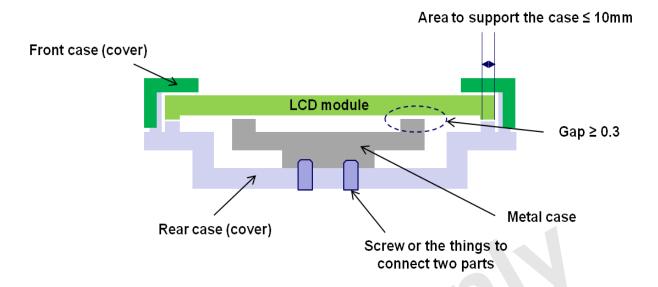
- F. D. It is recommended that more than 0.3 mm is allowable as a gap between the metal case and the rear of module
- G. It is recommended that structure to support the module shall be far away 10mm from the edge of border.



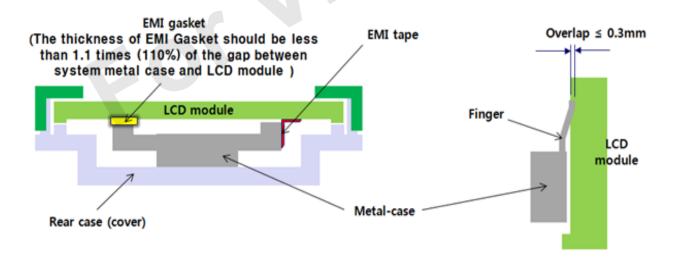
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H. It is recommended that metal case (or board) shall be affixed to the rear case at the spot where is far away 10mm from the edge of border.



- I. When applying the measures described below to reduce the level of EMI which occurs between the metal cover and the rear of module.
- J. If you use Finger, less than 0.3mm is allowable for overlap.

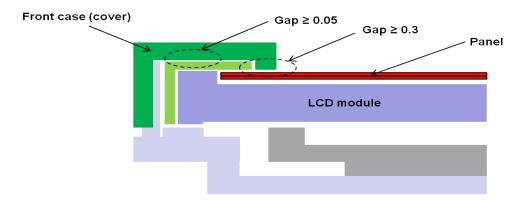


K. It is recommended that more than 0.3mm gap between the front case (or cover) and the panel glass is allowable.

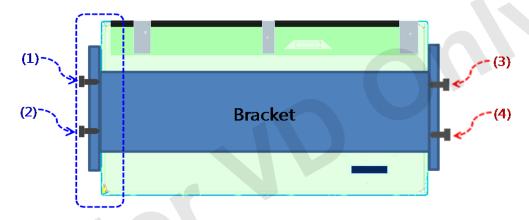


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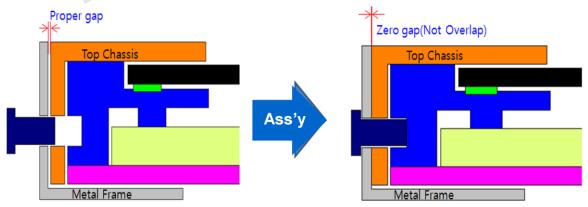
L. It is recommended that more than 0.05mm gap between the front case and the top chassis is allowable.



M. It is recommended that insert the screws into user holes from the ones on the parts, which the light comes out to ones in the corresponding parts.



N. It is recommended that design the metal frame and the top chassis to be in parallel with having no gap after inserting the side screw.





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