



SAMSUNG DISPLAY



Product Specification

CUSTOMER	
DATE OF ISSUE	

Approved by	
Prepared by	

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REVISION HISTORY

1. GENERAL DESCRIPTION

DESCRIPTION

The LTN156HL02-201 active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFTs as switching components. This model is composed of a TFT LCD panel, a driver circuit, and a backlight unit. This 15.6" model has a resolution of 1920 x 1080 pixels and can display up to 262,144 colors.

FEATURES

- High contrast ratio
- FHD (1920 x 1080 pixels) resolution
- Low power consumption
- Fast Response
- LED back light with an embedded LED driver
- DE (Data enable) only mode
- 3.3V eDP Interface
- Onboard EDID chip

APPLICATIONS

Notebook PC

If the intent to use this product is for other purpose, please contact Samsung Display.

GENERAL INFORMATION

Item	Specification	Unit	Note
Display area	344.16 (H) x 193.59 (V) (15.6"diagonal)	mm	
Driver Element	a-Si TFT active matrix		
Display colors	262,144 (6bit)		
Number of pixel	1920 * 1080 (FHD)	Pixel	16:9
Pixel Arrangement	RGB vertical stripe		
Pixel pitch	0.17925 (H) x 0.17925 (V) (TYP.)	mm	
Display Mode	Normally black, PLS mode		
Thickness of glass	0.3	mm	
Surface treatment	Haze 25%, Hardness 3H		Anti-glare
Environmental safe regulation	Pb Free, Halogen Free		
Power Consumption	Total 4.95W(Typ)@Logic 1.23W(Typ) BLU 3.72W(Typ) Total 5.5W(Max)		Mosaic PTN

MECHANICAL INFORMATION

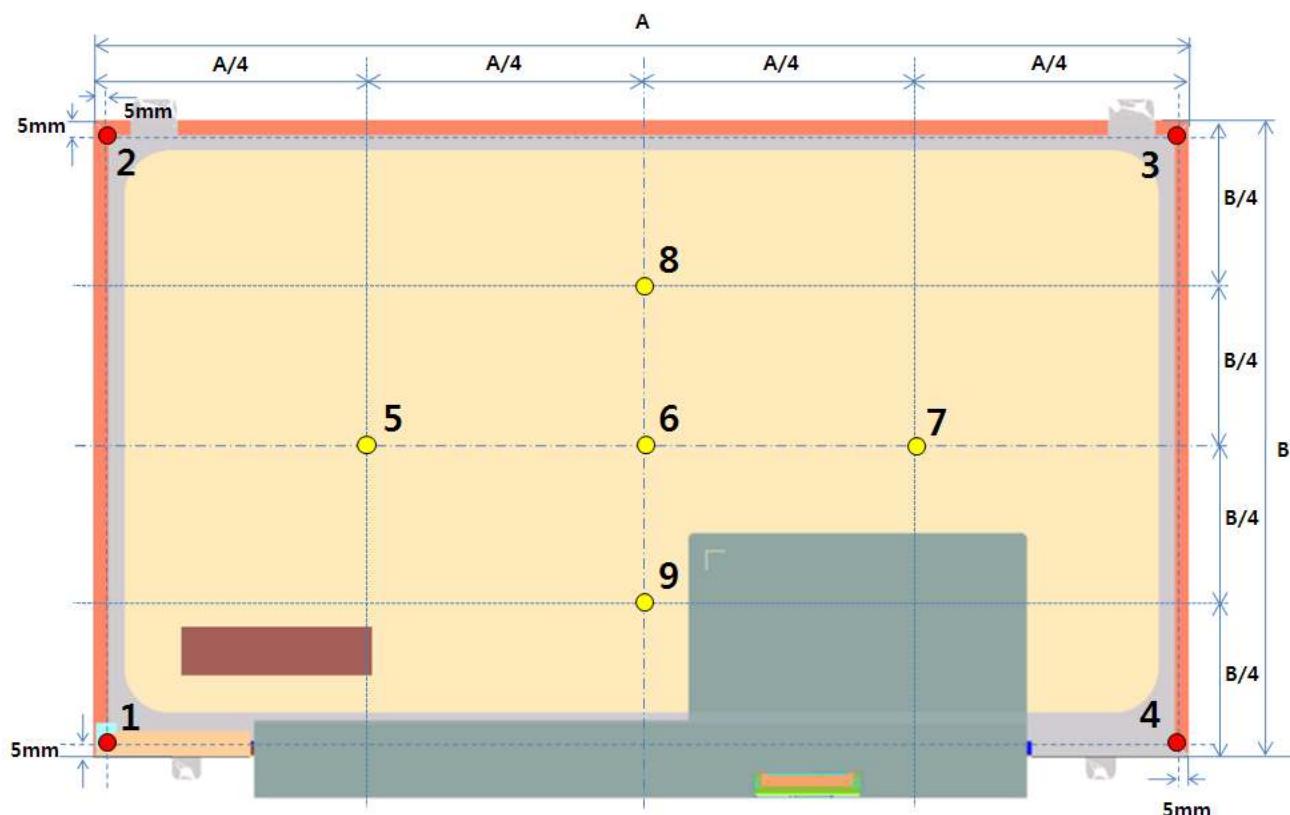
Item	Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal (H)	359.0	359.5	360.0	mm w/o flange & PCB
	Vertical (V)	223.3	223.8	224.3	mm with flange & PCB
	Depth (D)	206.0	206.5	207.0	mm w/o flange & PCB
	Weight	-	3.05	3.2	mm (1)
		-	325	350	g

NOTE (1) Measuring method for thickness

Force to be applied for measurement (1,2,3,4 Part) : The 200gf when using the height gauge.

Force to be applied for measurement (5,6,7,8,9 Part) : The 30gf when using the height gauge.

Force to be applied for measurement (COF Part) : The 50gf when using the height gauge.



This picture is reference for measurement. Appearance may be different according to each model type.

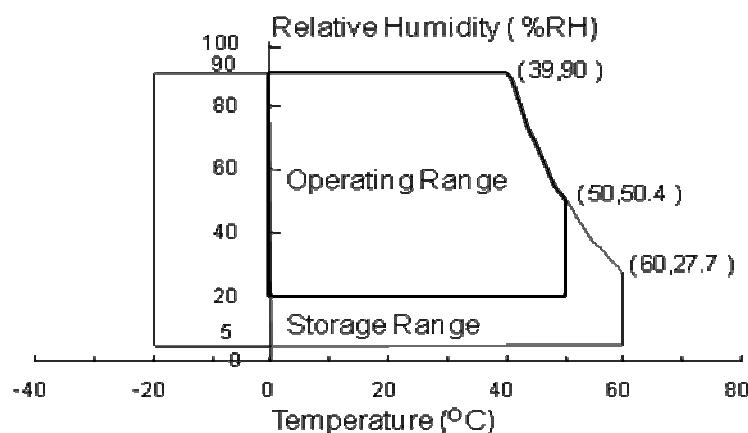
2. ABSOLUTE MAXIMUM RATINGS

2.1 ENVIRONMENTAL ABSOLUTE RATINGS

Item	Symbol	Min.	Max.	Unit	Note
Storage temperate	TSTG	-20	60	→C	(1)
Operating temperature (Temperature of glass surface)	TOPR	0	50	→C	(1)
Shock (non-operating)	Snop	-	240	G	(2), (4)
Vibration (non-operating)	Vnop	-	2.41	G	(3), (4)

Note (1) The range of temperature and relative humidity is shown in the graph below 90% RH Max..

(39°C ≥ Ta) If the temperature is higher than 40 °C, the maximum temperature of wet-bulb shall be less than 39°C. No condensation



(2) Vibrate ←X, ←Y, and ←Z axis in the shape of the half sine wave one time for 2ms.

(3) Vibrate the X, Y, and Z randomly within a 5 - 500 Hz range for 30min.

(4) When testing a vibration and a shock, the fixture, which holds the module to be tested, shall be hard and rigid in order for the module not to be twisted or bent by the fixture.

2.2 ELECTRICAL ABSOLUTE RATINGS

(1) TFT LCD MODULE

$V_{LCD_VCC} = 3.3V$, $V_{SS} = GND = 0V$

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V_{LCD_VCC}	$V_{SS} - 0.3$	4.0	V	(1), (2)
eDP Input Voltage	V_{eDP}	$V_{SS} - 0.3$	2.0		

Note (1) Within T_a ($25 \leftarrow 2 \rightarrow C$)

(2) Permanent damage to the device may occur if exceed maximum values.

(2) BACKLIGHT UNIT

$V_{SS} = GND = 0V$

Item	Symbol	Min.	Max.	Unit	Note
BLU Supply Voltage	V_{BL_PWR}	$V_{SS} - 0.3$	26.5	V	(1), (2) (1), (2) Vin=12V Duty 100%
BLU Supply Current	I_{BL_PWR}	-	0.96		

Note (1) Within T_a ($25 \leftarrow 2 \rightarrow C$)

(2) Permanent damage to the device may occur if exceed maximum values

2.3 THE OTHERS

(1) STATIC ELECTRICITY PRESSURE RESISTANCE

Item	Test Conditions	Remark
CONTACT DISCHARGE	150pF, 330 μ s, $\rightarrow 8kV$, 200points, 1 time/point	Operating
AIR DISCHARGE	150pF, 330 μ s, $\rightarrow 15kV$, 200points, 1 time/point	Operating

3. OPTICAL CHARACTERISTICS

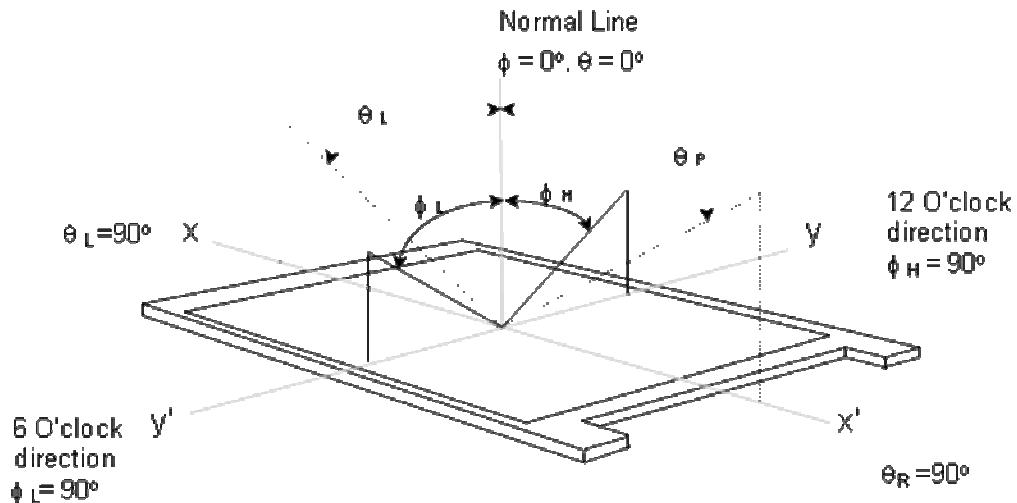
The following items are measured under the stable conditions.* The optical characteristics should be measured in the dark room or the equivalent environment by the methods shown in the Note (5).

Measuring equipment: TOPCON SR-3

T_a = 25 °C, V_{LCD} V_{CC} = 3.3V, f_V = 60Hz, f_{DCLK} = 73.44MHz, I_F = 100% duty

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note						
Contrast Ratio	CR	Normal Viewing Angle ← = 0 ▼ = 0	600	700	-	-	(1),(2),(5)						
Response time (Rising + Falling)	T _{RT}		-	30	35	msec	(1),(3)						
Average Luminance of White (5 Points)	Y _{L,AVE}		255	300	-	cd/m ²	IF=100% Duty (1),(4)						
Cross Modulation	DSHA		-	1.0	2.0	%	(7)						
Color Chromaticity (CIE)	Red	-0.03	0.635 0.335 0.295 0.610 0.150 0.060 0.313 0.329	+0.03		Degrees	(1),(5)						
	Green												
	Blue												
	White												
Viewing Angle	Hor.	CR ≥ 10 At center	80	85	-	Degrees	(1),(5)						
			80	85	-								
	Ver.		80	85	-								
			80	85	-								
Color Gamut	CG		68	72	-	%							
White variation (11P)	↔ _L		-	1.43	1.54		(6)						

Note (1) The definition of viewing angle: The range of viewing angle (10 ↑ C/R)

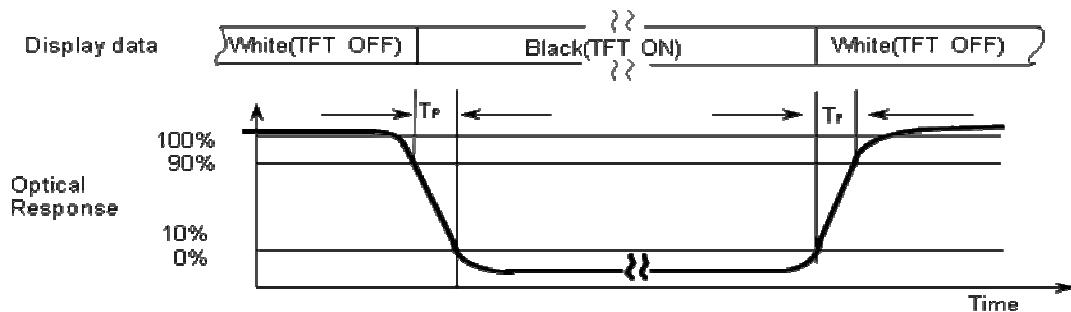


Note (2) The definition of contrast ratio (CR): The ratio of max. gray and min gray at 5 points
(4, 5, 7, 9, and 10)

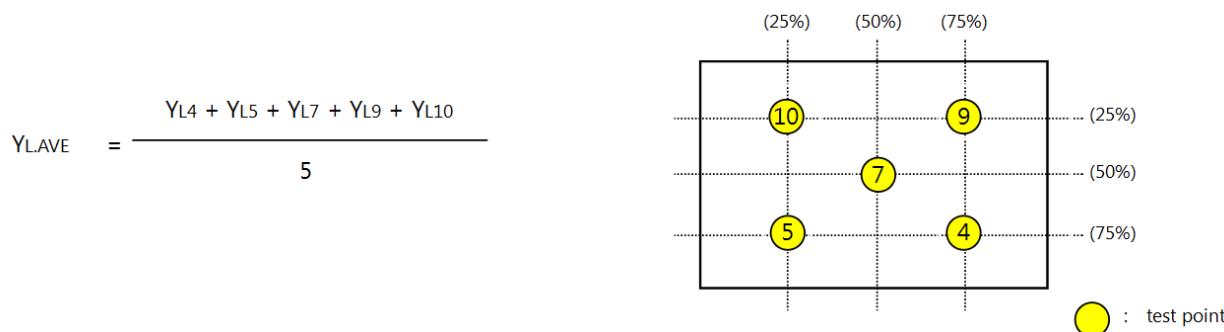
$$CR = \frac{CR(4) + CR(5) + CR(7) + CR(9) + CR(10)}{5}$$

Points = (4), (5), (7), (9), (10) at the figure of Note(6).

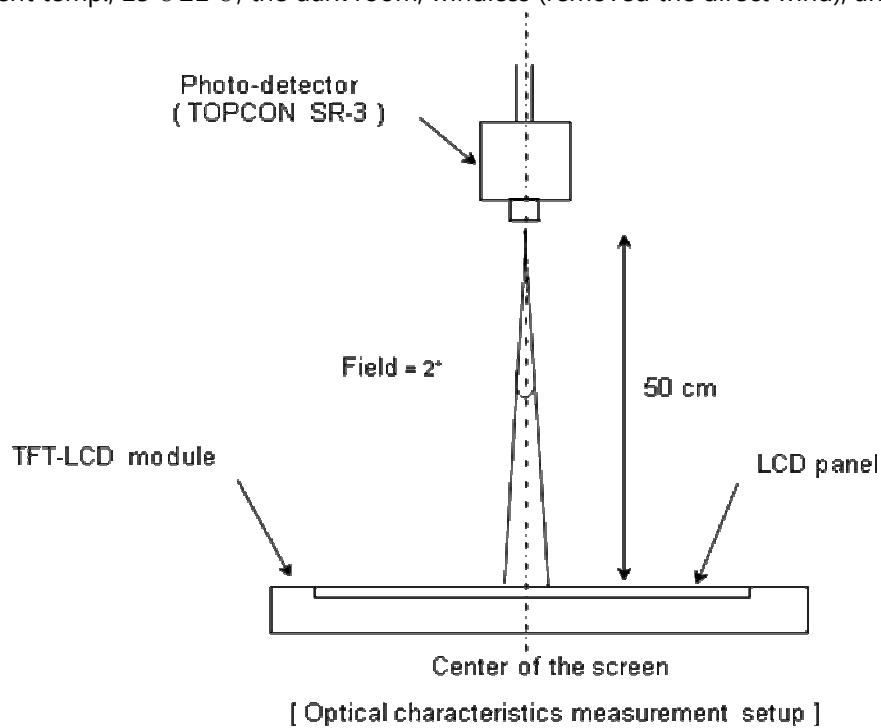
Note (3) The definition of Response time: Subtotal of the time, during which the transmission changes from 10% to 90% when the TFT turns on and off.



Note (4) The definition of average luminance of white: Measure the luminance of white at 5 points.



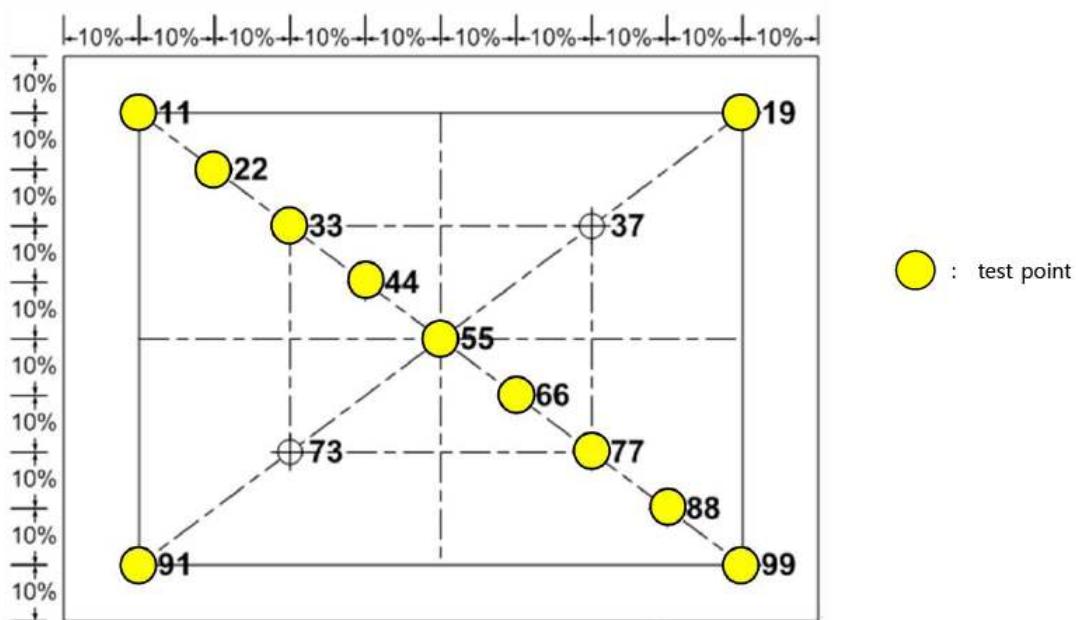
Note (5) Measure the panel, which is left for 30 min. at the normal temp. after leaving it for 30 min with turning the back light on at the rating. The measurement should be executed under the condition including the ambient temp., $25^{\circ}\text{C} \pm 2^{\circ}\text{C}$, the dark room, windless (removed the direct wind), and no vibration.



Note (6) The definition of white variation at 11 points ($\Leftrightarrow L$)

[11 22 33 44 55 66 77 88 99 19 91]

$$\Leftrightarrow L = \frac{\text{Maximum luminance of 11 points}}{\text{Minimum luminance of 11 points}}$$



Note (7) The definition of crosstalk (Cross modulation)

: The phenomenon, which the contrast ratio is decreased by the interference of signal between pixels
Crosstalk Calculation Method

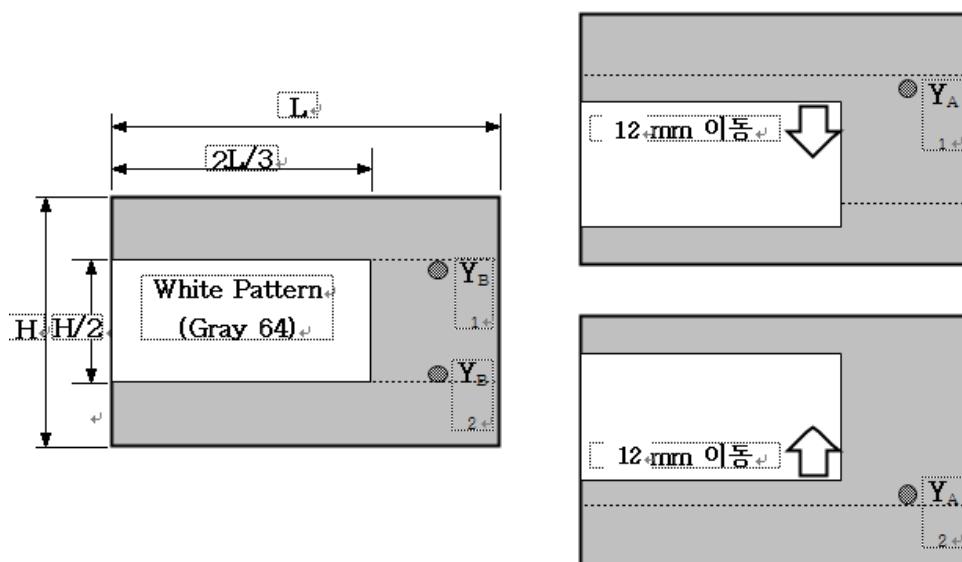
$$\text{Crosstalk Modulation Ratio}(D_{SHA}) = \bullet \frac{|Y_A - Y_B|}{Y_A} \diamond 100 (\%)$$

Where

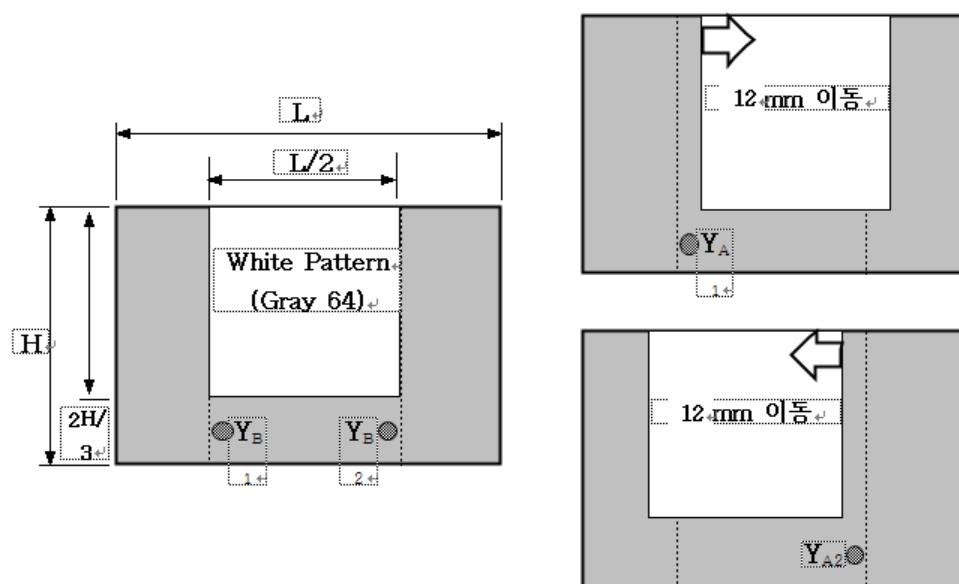
Y_A, Y_B = Measured under the 2° viewing angle (Measured area: $\psi 12 \text{ mm}$)

The rectangle area, which excludes the black-colored rectangle includes the range, which is from 1 gray to 63 gray.

1) The method of measurement for horizontal-crosstalk

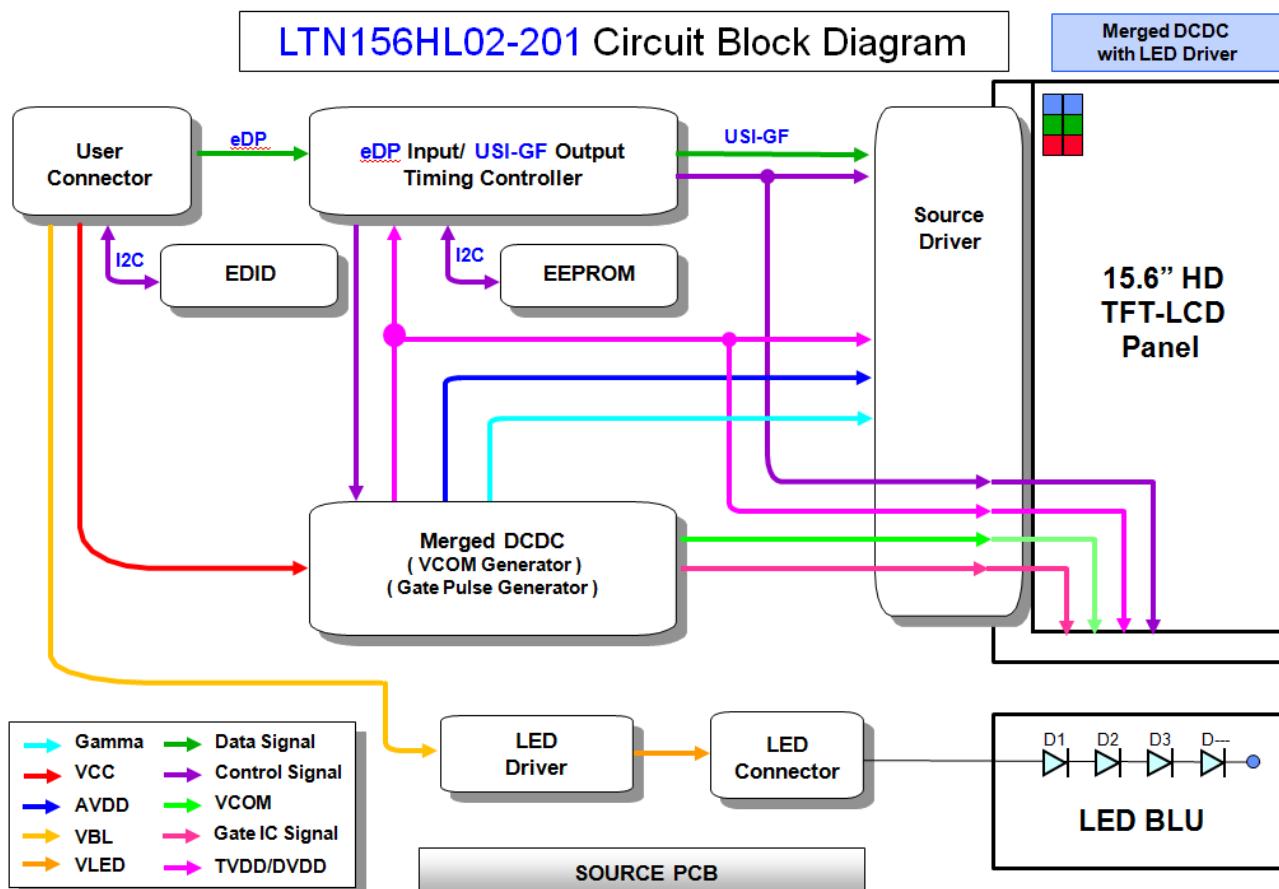


2) The method of measurement for vertical-crosstalk

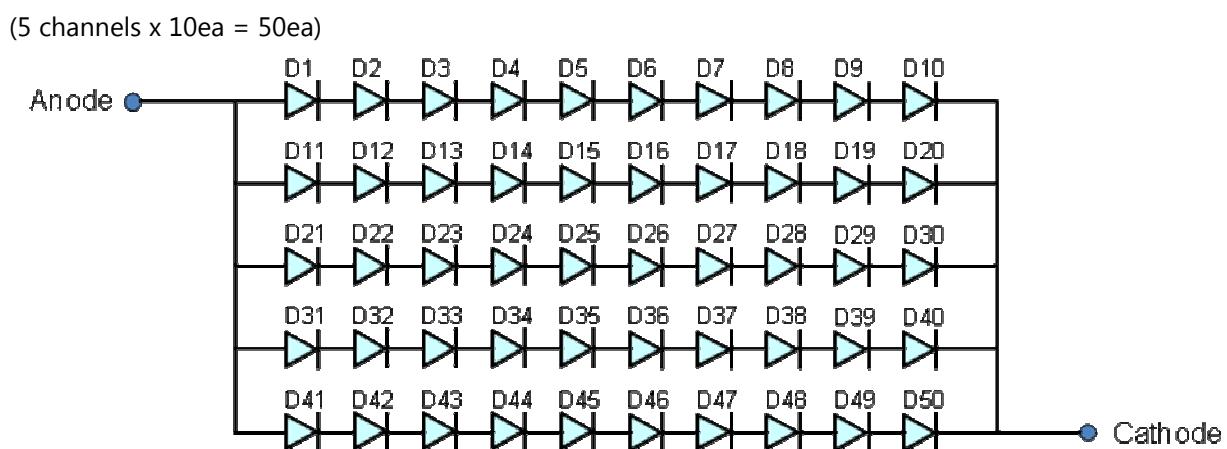


4. BLOCK DIAGRAM

4.1 TFT LCD MODULE



4.2 THE STRUCTURE OF LED PLACEMENT



5. ELECTRICAL CHARACTERISTICS

5.1 TFT LCD MODULE

T_a = 25 ± 2 °C

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Power Supply Voltage	V _{LCD_VCC}	3.0	3.3	3.6	V	
T-CON TTL Input Voltage	High	V _{IH}	0.7 V _{LCD_VCC}	-	-	V
	Low	V _{IL}	-	-	0.3 V _{LCD_VCC}	V
Vsync	60Hz	f _v	-	60	-	Hz
	50Hz	f _v	-	50	-	Hz
	40Hz	f _v	-	40	-	Hz
Hsync	f _H	66.84	68.64	70.44	kHz	
Main Frequency	60Hz	f _{DCLK}	66.17	73.44	76.78	MHz
	50Hz	f _{DCLK}	-	61.20	-	MHz
	40Hz	f _{DCLK}	-	48.96	-	MHz
Rush Current	I _{RUSH}	-	-	1.5	A	(5)
Input Current	White	I _{LCD_VCC}	-	489	606	mA
	Mosaic	I _{LCD_VCC}	-	374	484	mA
	Black	I _{LCD_VCC}	-	285	360	mA
	V.Stripe	I _{LCD_VCC}	-	475	606	mA
	Red	I _{LCD_VCC}	-	343	450	mA
	Green	I _{LCD_VCC}	-	343	450	mA
	Blue	I _{LCD_VCC}	-	343	450	mA
	1Dot	I _{LCD_VCC}	-	475	606	mA
Logic Input Power Consumption	P _{LCD_VCC}	-	1.61	2.0		(4)
EDID Input Voltage	V _{EDID}	3.0	3.3	3.6	V	
EDID Input Current	I _{EDID}	-	-	5	mA	
Skew	PS	-400	-	400	ps	(2)

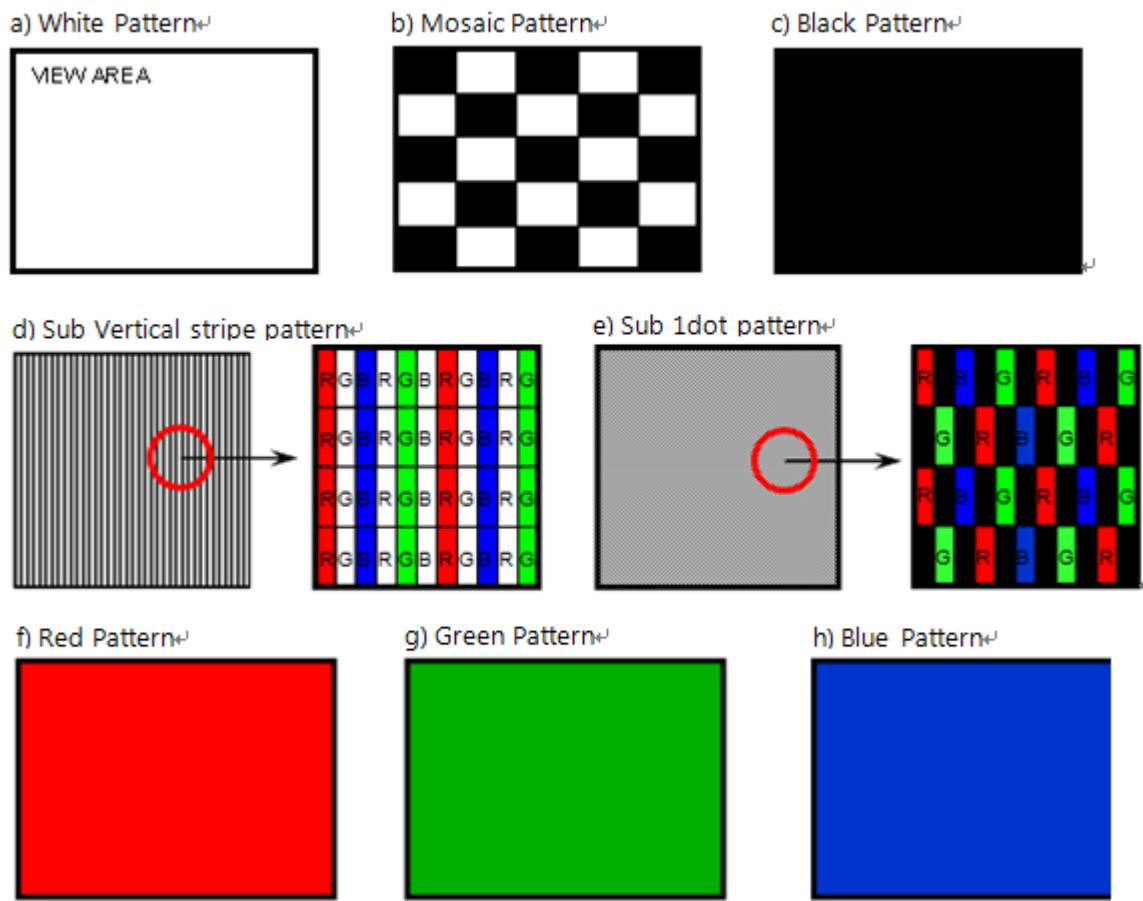
Note (1) The data pins for display and signal pins for timing should be connected. (GND= 0V)

(2) f_v = 60Hz, f_{DCLK} = 73.44MHZ, V_{LCD_VCC} = 3.3V, DC Current.

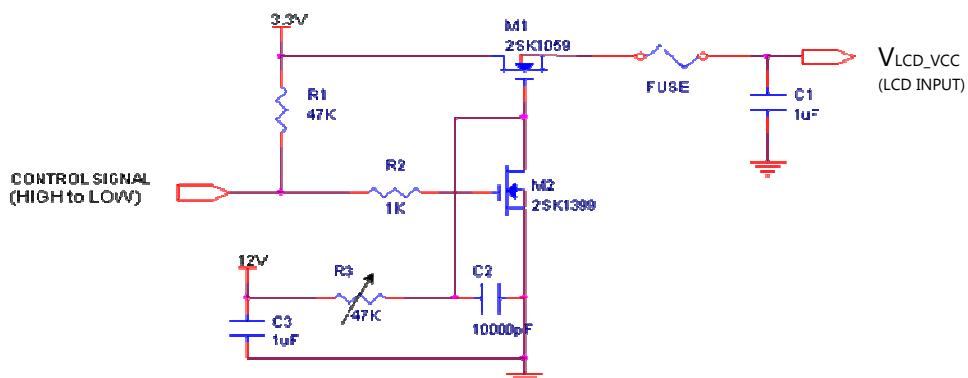
(3) In the case of 40Hz & 50Hz for sDRRS, FOS,

Flicker & Brightness are not guaranteed, because their level might be different from 60Hz operation.

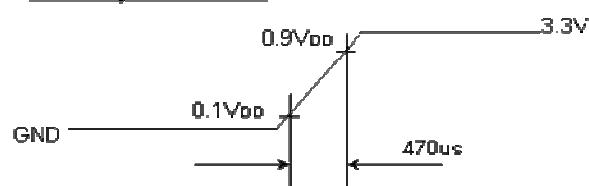
Note (4) The dissipation pattern for power



Note (5) The condition for measurement for rush current



VDD rising time is 470us



5.2 BACK LIGHT UNIT

Ta = 25 \leftarrow 2 \rightarrow C

Item	Symbol	Min.	Typ.	Max.	Unit	Note
LED Forward Current	IF	-	22.3	-	mA	
LED Forward Voltage	VF	2.7	2.9	3.1	V	IF = 20mA
LED Array Voltage	VP	-	29	-	V	
LED Power Consumption	P	-	3.72	3.9	W	
LED Life time	Hr	15,000	-	-	Hours	(1)
LED Counts	Q	-	50	-	EA	

Note (1) The life time (Hr) of LEDs can be defined as the time during which it continues to operate under the condition, which the Ta is 25 \leftarrow 2 \rightarrow C and IF= 22.3 mA rms until the one of the following events occurs when the brightness becomes 50% or lower than the original.

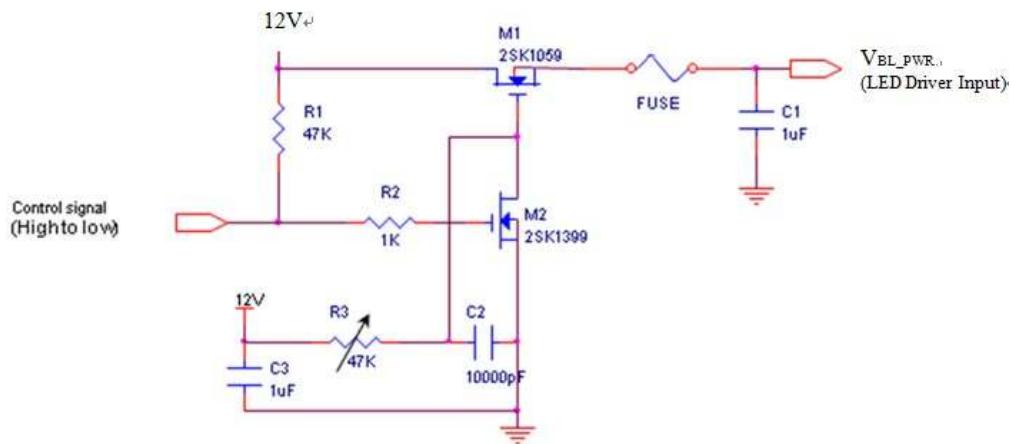
5.3 LED DRIVER

The manufacturer of LED driver: Richtek RT8561D

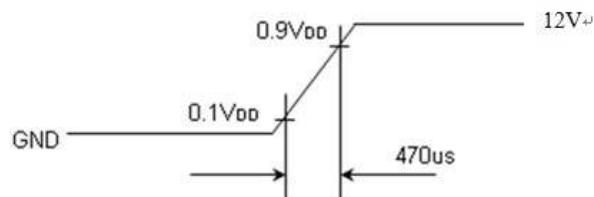
Ta= 25 \leftarrow 2 \rightarrow C

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Input Voltage	V _{BL_PWR}	5	12	21	V	
Input Current	I _{BL_PWR}	-	310	325	mA	Vin=12V Duty 100%
PWM duty Ratio	D _{BL_PWM_DIM}	2	-	100	%	
External PWM Frequency	F _{BL_PWM_DIM}	0.2	1	2	kHz	APS ON
		0.2	1	2	kHz	APS OFF
PWM Resolution	R _{BL_PWM_DIM}	0.8	-	-	%	APS ON
		0.3			%	APS OFF
In-Rush Current	I _{RUSH_BL_PWR}	-	-	1.5	A	(1)
EN Control Level	High	V _{BL_ENABLE}	1.6	-	5.0	V
			0.0	-	1.0	V
PWM Control Level	High	V _{BL_PWM_DIM}	1.3	-	5.0	V
			0.0	-	0.15	V
VBL_PWR @ LED Driver On	V _{BL_PWR}	5	-	20	V	
VBL_PWR @ LED Driver Off	V _{BL_PWR}	0	-	1.5	V	
Operating frequency	FO	0.8	1.0	1.2	kHz	
Efficiency	η	-	87	-	%	

Note (1) Rush current measurement condition



The V_{BL_PWR} rising time is 470us.



5.4 eDP INTERFACE

eDP Input Specifications

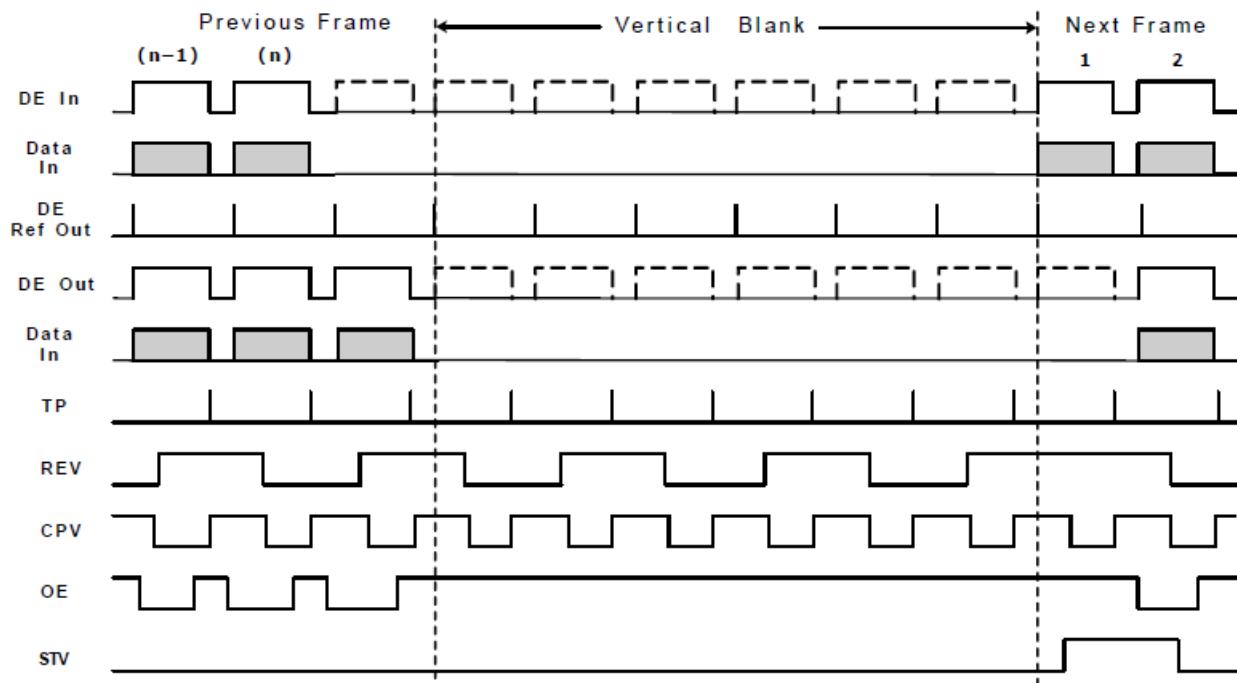
Characteristics	Symbol	Min.	Typ.	Max.	Unit	Conditions
AUX peak-to-peak output differential voltage	$V_{AUX-TX-DIFFp-p}$	0.39	-	1.38	V	
AUX peak-to-peak input differential voltage	$V_{AUX-RX-DIFFp-p}$	0.32		1.36	V	
AUX DC Common Mode Voltage	$V_{AUX-DC-CM}$	-	0.85	-	V	
AUX short circuit current limit	$I_{AUX-SHORT}$		-	28	mA	
RX. Input DC common mode voltage	$V_{RX-DC-CM}$		0.85		V	

5.5 INTERFACE TIMING

5.5.1 TIMING PARAMETERS

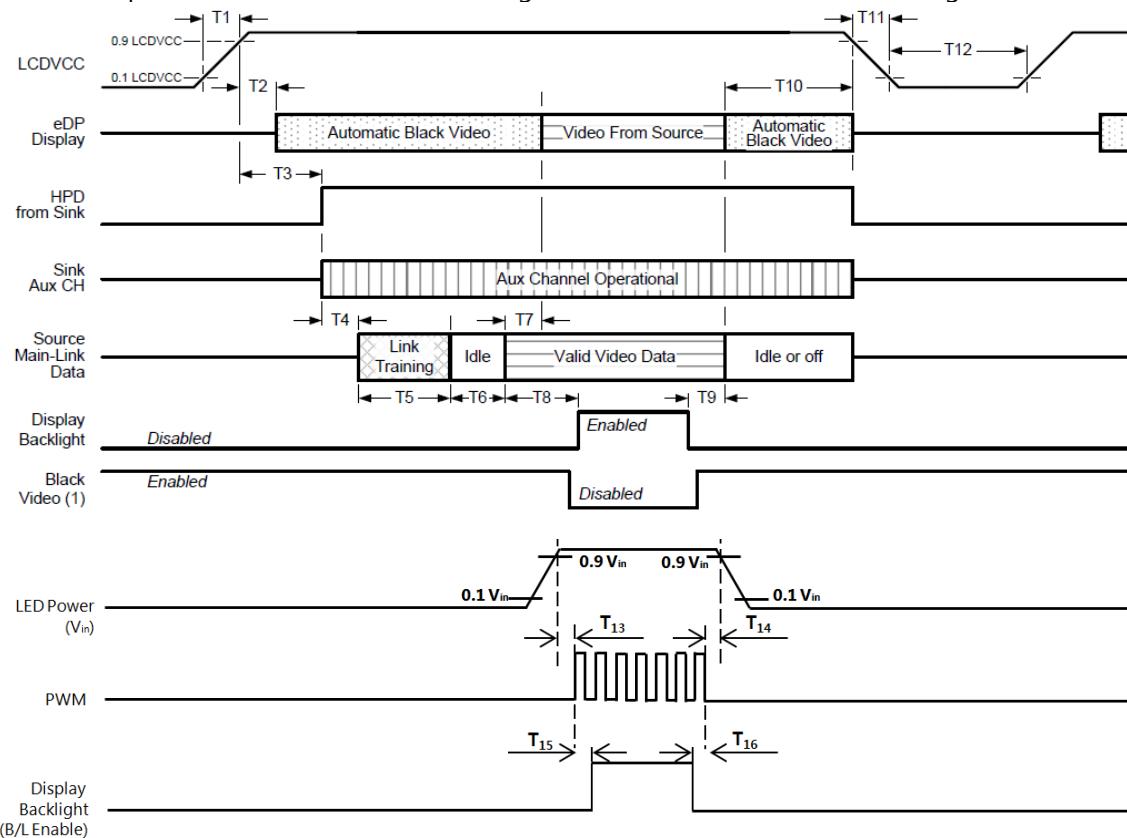
Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
Frame Frequency	Cycle	T_V	1114	1144	1174	Lines	
Vertical active in the display term	Display Period	T_{VD}	-	1080	-	Lines	
Scanning time in one line	Cycle	T_H	990	1070	1090	Clocks	2Lane
Horizontal active in the display term	Display Period	T_{HD}	-	960	-	Clocks	

5.5.2 TIMING DIAGRAMS OF INTERFACE SIGNAL



5.6 POWER ON/OFF SEQUENCE

To prevent the product from being latched up or the DC in the LCD module from starting an operation, the order to turn the power on and off should be changed to the order as shown in the diagram below.



Timing (ms)	Remarks	Note
$0.5 < T_1 \leq 10$	Power rail rise time, 10% to 90%	
$0 < T_2 \leq 200$	Delay from LCDVCC to automatic Black Video generation	(1)(2)
$0 < T_3 \leq 200$	Delay from LCDVCC to HPD high	(3)
$0 \leq T_4$	Delay from HPD high to link training initialization	
$0 \leq T_5$	Link training duration	
$0 \leq T_6$	Link idle	
$0 < T_7 \leq 50$	Delay from valid video data from Source to video on display	
$0 < T_8$	Delay from valid video data from Source to backlight enable	
$0 < T_9$	Delay from backlight disable to end of valid video data	(1)(2)
$0 < T_{10} \leq 500$	Delay from end of valid video data from Source to power off	
$0 < T_{11} \leq 10$	Power rail fall time, 90% to 10%	
$150 < T_{12}$	Power off time	
$0 < T_{13}$	Interval from LED driver Vin rising time 90% to PWM ON	
$0 < T_{14}$	Interval from PWM Off to LED driver Vin falling time 90%	
$0 \leq T_{15}$	Interval from PWM ON to B/L Enable ON	
$0 \leq T_{16}$	Interval from B/L Enable Off to PWM Off	

The backlight may be flashed if the interface signal remains floated when the above-mentioned signal becomes invalid.

- Note (1) The Sink must include the ability to automatically generate Black Video autonomously. The Sink must automatically enable Black Video under the following conditions: Upon LCDVCC power-on (within T2 max) When the "No Video Stream _Flag"(VB-ID Bit3) is received from the Source (at the end of T9) When no Main Link Data, or invalid data, is received from the Source, Black Video must be displayed within 50ms(max) from the start of either condition. Video data can be deemed invalid based on MSA and timing information, for example.
- (2) The Sink may implement the ability to disable the automatic Black Video function, as described in Note 1, above, for system development and debugging purposes.
- (3) The Sink must support AUX Channel polling by the Source immediately following LCDVCC power-on without causing damage to the Sink device (the Source can re-try if the Sink is not ready). The Sink must be able to respond to an AUX Channel transaction with the time specified within T3 max.

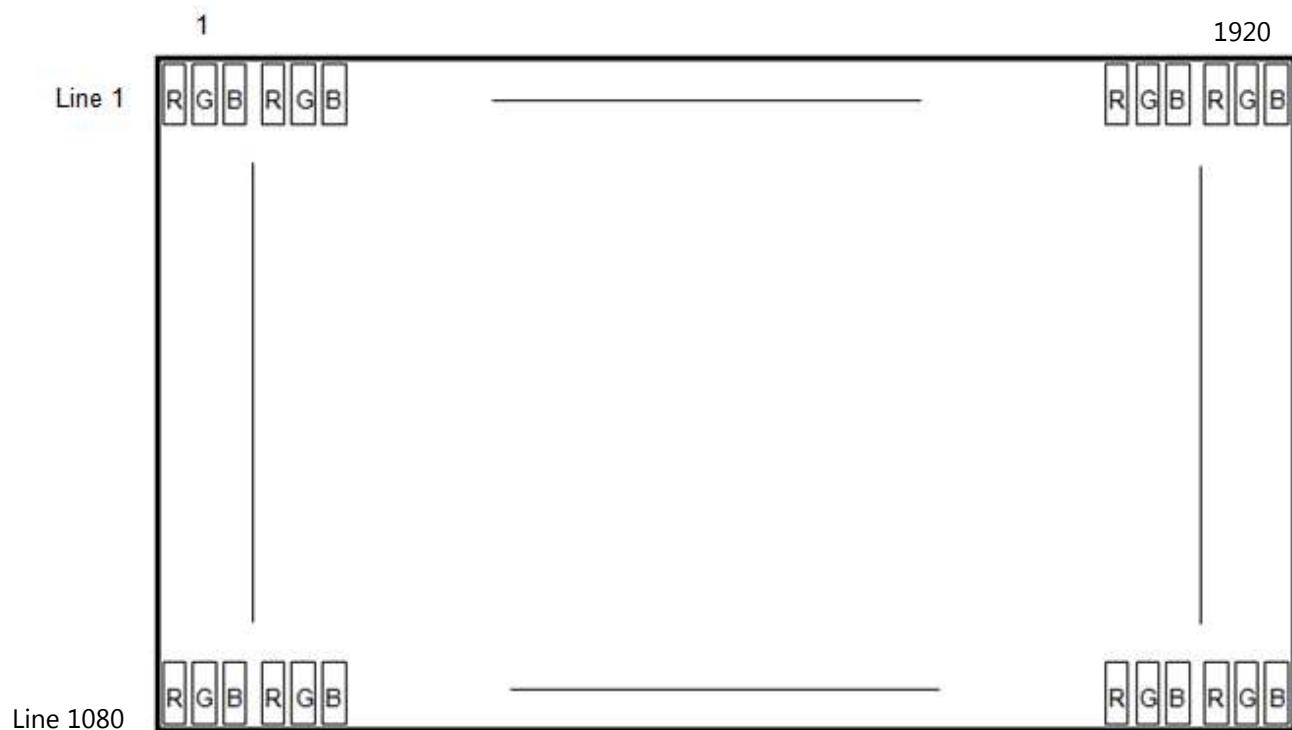
5.7 INPUT TERMINAL PIN ASSIGNMENT

5.7.1 INPUT SIGNAL & POWER

(eDP, Connector: 20455-030E-02R, I-PEX or the equipment with the equivalent capability)

Pin	Symbol	Function
1	APS_EN	APS on/off or No connection (optional)
2	H_GND	High Speed Ground
3	Lane1_N	Complement Signal Link Lane 1
4	Lane1_P	True Signal Link Lane 1
5	H_GND	High Speed Ground
6	Lane0_N	Complement Signal Link Lane 0
7	Lane0_P	True Signal Link Lane 0
8	H_GND	High Speed Ground
9	AUX_CH_P	True Signal Auxiliary Channel
10	AUX_CH_N	Complement Signal Auxiliary Channel
11	H_GND	High Speed Ground
12	LCD_VCC	LCD logic and driver power
13	LCD_VCC	LCD logic and driver power
14	LCD_Self_Test	LCD Panel Self-Test Enable
15	LCD_GND	LCD logic and driver ground
16	LCD_GND	LCD logic and driver ground
17	HPD	Hot Plug Detect
18	BL_GND	Backlight Ground
19	BL_GND	Backlight Ground
20	BL_GND	Backlight Ground
21	BL_GND	Backlight Ground
22	BL_ENABLE	Backlight on/off
23	BL_PWM_DIM	System PWM signal input for dimming
24	NC (WPN)	Reserved for the use by LCD manufacturer (WPN)
25	NC	No connection
26	BL_PWR	Backlight power
27	BL_PWR	Backlight power
28	BL_PWR	Backlight power
29	BL_PWR	Backlight power
30	NC	CLR_EN on/off or No connection (optional)

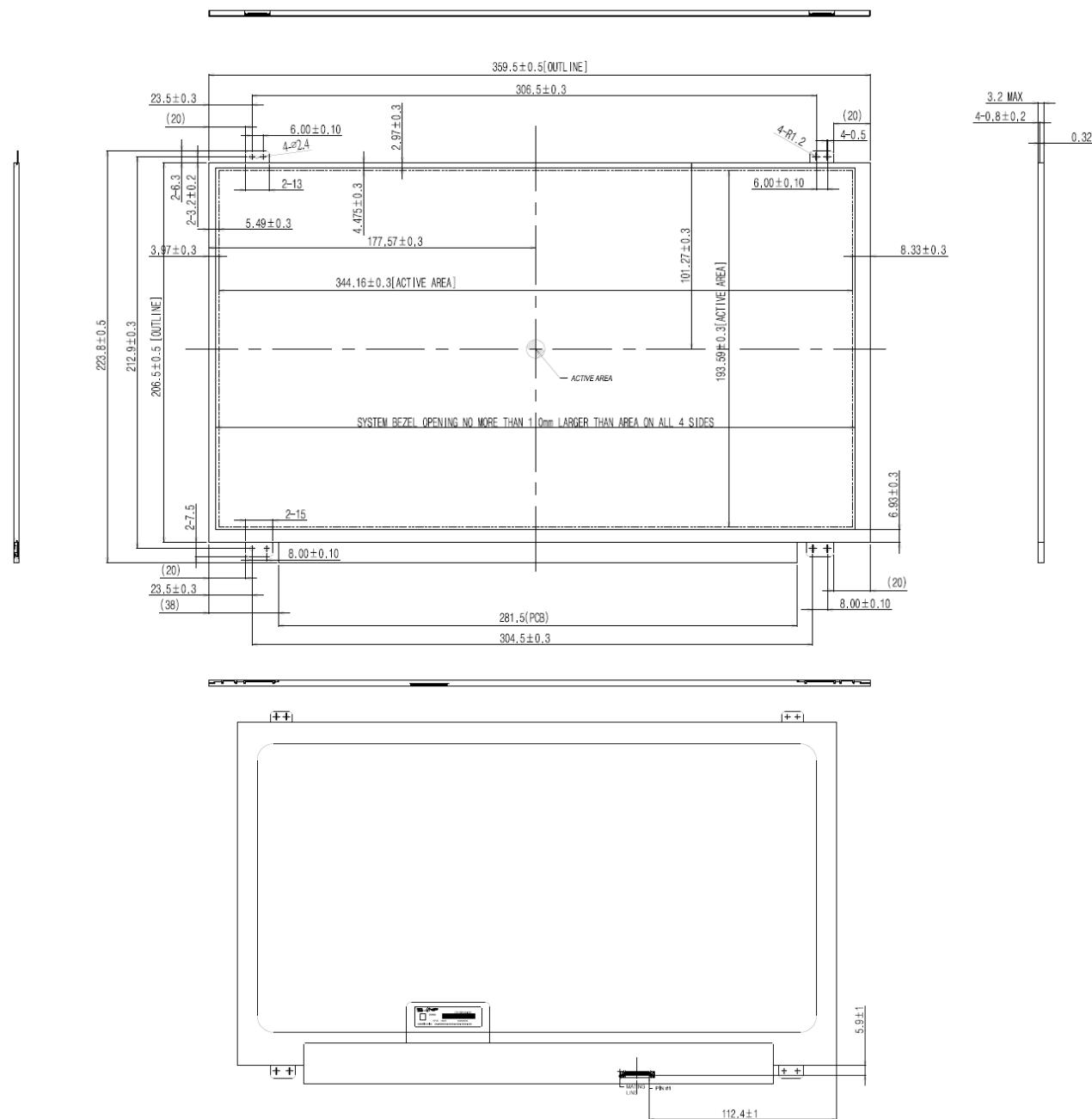
6. PIXEL FORMAT



7. OUTLINE DIMENSION

LTN156HL02-201

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* NOTE

1. INPUT SIGNAL CONNECTOR TO BE SPECIFIED AS BELOW.
-MAKER / PART NO. : I-PEX / 20455-030E-02R (or compatible)
2. CALIFERS MEASURING FORCE : 200gf
3. WEIGHT : 350g MAX
4. IN ORDER TO AVOID IC DAMAGE, IT IS NOT ALLOW THAT OVERLAPPING OF CABLES OR ANTENNAS, CAMERA, WLAN, WWAN, OVER THESE COF LOCATION.
5. THERE MUST BE NO OVERLAP BETWEEN REAL SYSTEM AND MAX THICKNESS OF LCD MODULE

GENERAL TOLERANCE		DRAWING		Internal D.L.C.		REASON		CHECKED BY	
MM	INCH	REF	DATE	DESIGNER	REV	EDITION	COMPT'D BY	APPROV'D BY	MODEL NAME
4.1 ± 0.4	0.162 ± 0.015	4.1	04/06	JAN CHOI	4.1	04/06	JSCK		LTN156HL02-2
4.1 ± 0.4	0.162 ± 0.015	4.2			4.2				
4.1 ± 0.4	0.162 ± 0.015	4.3			4.3				
16.4 ± 0.06	0.645 ± 0.002	16.25	05/06	UNSPECIFIED	16.25	05/06			PART NUMBER
16.4 ± 0.06	0.645 ± 0.002	16.25	05/06	UNSPECIFIED	16.25	05/06			CODE NO.
164.1 ± 0.205	6.453 ± 0.008	164.1	05/06	SAMSUNG DISPLAY	164.1	05/06			REV. 00

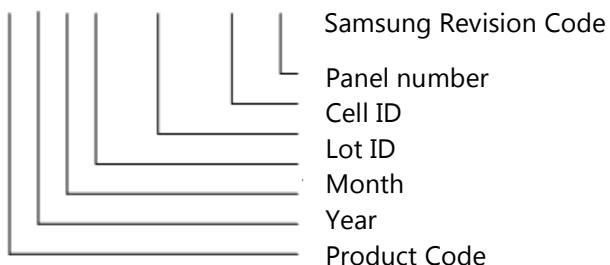
8. MARKING

A nameplate is affixed to the specified location on each product.

(1) Parts number : LTN156HL02

(2) Revision code : 3 letters

(3) Lot number : X X X X XXX XX X 201



9. GENERAL PRECAUTIONS

9.1 STORAGE

We highly recommend to comply with the criteria in the table below.

ITEM	Unit	Min.	Max.
Storage Temperature	(°C)	5	40
Storage Humidity	(%rH)	35	75
Storage Life	12 months		
Storage Condition	<ul style="list-style-type: none">- The storage room should be equipped with a good ventilation facility, which has a temperature controlling system.- Products should be placed on the pallet, which is away from the wall not on the floor.- Prevent products from being exposed to the direct sunlight, moisture, and water; Be cautious not to pile the products up.- Avoid storing products in the environment, which other hazardous material is placed.- If products are delivered or kept in the storage facility more than 3 months, we recommend you to leave products under the condition including a 20°C temperature and a humidity of 50% for 24 hours.- If you store semi-manufactured products for more than 3 months, bake the products under the condition including the 50°C temp. and the 10% humidity for 24hrs after being used.		

10. APPENDIX

Only Internal

[OPTICAL CHARACTERISTICS]

The following items are measured under the stable conditions.* The optical characteristics should be measured in the dark room or the equivalent environment by the methods shown in the Note (5).

Measuring equipment: TOPCON SR-3

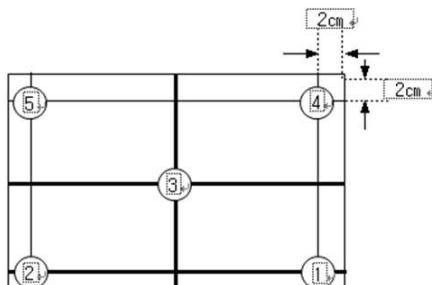
$T_a = 25 \pm 2 \pm C$, $V_{LCD_VCC} = 3.3V$, $f_v = 60Hz$, $f_{DCLK} = 73.44MHz$, IF = 100% duty

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Flicker	F		-	3.2	5.2		(8)

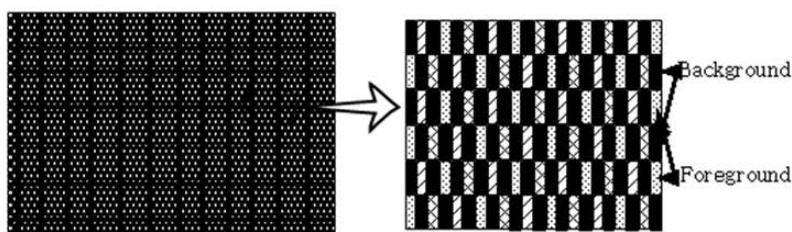
Note (8) The definition of flicker: The phenomenon, which the pixel on the screen of LCD panel blinks.

1) Calculate the figure with observing the standard for the measurement for the flicker.

2) Measurement point



3) Pattern to measure the flicker: Inverting driving of DOT



[LED DRIVER]

The manufacturer of LED driver: Richtek RT8561D

$T_a = 25 \pm 2 \pm C$

2 → C

Item	Symbol	Min.	Typ.	Max.	Unit	Note
OVP driving Voltage	V_{OVP}	-	-	38.8	V	

[ELECTRICAL CHARACTERISTICS]

TFT LCD MODULE

$T_a = 25 \pm 2 ^\circ C$

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Power Supply Voltage Ripple	$V_{LCD_VCC_RI}$ PPLE	-	-	$0.05^* V_{LCD_VCC}$	V	(1)

Note (1) $f_V = 60\text{Hz}$, $f_{DCLK} = 73.44\text{V}_{LCD_VCC} = 3.3\text{V}$, Sub Dot Pattern