NL150A01-L04 TFT-LCD Module

SHARP INSIDE

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No: LD-150N7

1. Application

This specification applies to the color 15.0 XGA TFT-LCD module NL150A01-L04.

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- O Do not use the device for equipment that requires an extreme level of reliability, such as aerospace applications, telecommunication equipment(trunk lines), nuclear power control equipment and medical or other equipment for life support.
- ○We assumes no responsibility for any damage resulting from the use of the device which does not comply with the instructions and the precautions specified in these specification sheets.

2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit, power supply circuit and a back light unit. Graphics and texts can be displayed on a 1024×RGB×768 dots panel with about 16 million colors by using LVDS (Low Voltage Differential Signaling) and supplying +3.3V DC supply voltages for TFT-LCD panel driving and supply voltage for backlight.

3. Mechanical Specifications

Parameter	Specifications	Unit
Diaplay size	38 (Diagonal)	cm
Display size	15.0 (Diagonal)	Inch
Active area	304.1 (H) 228.1 (V)	mm
Divel format	1024 (H) ×768 (V)	Pixel
Pixel Iormat	(1 pixel R+G+B dots)	-
Pixel pitch	0.297 (H) 0.297 (V)	mm
Pixel configuration	R, G, B vertical stripe	-
Display mode	Normally white	-
Brightness	450 (Typ)	cd/m ²
Respons time	Tr=5,Td=20	ms
Contrast ration	350 : 1	-
View angle	55,80,80	Deg.
Unit outline dimensions *1	326.0(W) 252.0(H) 10.55(D)	mm
Mass	1200 (MAX)	g
Surface treatment	Anti-glare and hard-coating 2H	
	(Haze value = 28)	

*1. Note: excluding back light cables, cover and pet sheet.

The thickness of module (D) doesn't contain the projection. Outline dimensions are shown in Fig.1.

4. Input Terminals

4-1. TFT-LCD panel driving

CN1 (Interface signals and +3.3V DC power supply)

Using connectors : DF14H-20P-1.25H (Hirose Electric Co., Ltd.) or compatible

Corresponding connectors : DF14-20S-1.25C(Connector)

DF14-2628SCFA(Terminal)

Corresponding LVDS Transmitter : THC63LVDM83R(Thine) or compatible

Pin No.	Symbol	Function	Remark
1	Vcc	+3.3V Power supply	
2	Vcc	+3.3V Power supply	
3	GND		
4	GND		
5	RXIN0-	Receiver signal (-)	LVDS
6	RXIN0+	Receiver signal (+)	LVDS
7	GND		
8	RXIN1-	Receiver signal (-)	LVDS
9	RXIN1+	Receiver signal (+)	LVDS
10	GND		
11	RXIN2-	Receiver signal (-)	LVDS
12	RXIN2+	Receiver signal (+)	LVDS
13	GND		
14	RXCKIN-	Clock signal (-)	LVDS
15	RXCKIN+	Clock signal (+)	LVDS
16	GND		
17	RXIN3-	Receiver signal (-)	LVDS
18	RXIN3+	Receiver signal (+)	LVDS
19	GND		
20	LVDS_SET	LVDS_SET	note1

4-2 Data Mapping

1) 8 bit input

	Transmitter	20pin LVDS_SET						
Pin No	Data	=L (GND) or Open	=H (3.3V)					
51	TA0	R2	R0 (LSB)					
52	TA1	R3	R1					
54	TA2	R4	R2					
55	TA3	R5	R3					
56	TA4	R6	R4					
3	TA5	R7 (MSB)	R5					
4	TA6	G2	G0 (LSB)					
6	TB0	G3	G1					
7	TB1	G4	G2					
11	TB2	G5	G3					
12	TB3	G6	G4					
14	TB4	G7 (MSB)	G5					
15	TB5	B2	B0 (LSB)					
19	TB6	B3	B1					
20	TC0	B4	B2					
22	TC1	B5	B3					
23	TC2	B6	B4					
24	TC3	B7(MSB)	B5					
27	TC4	(NA)	(NA)					
28	TC5	(NA)	(NA)					
30	TC6	DE	DE					
50	TD0	R0 (LSB)	R6					
2	TD1	R1	R7 (MSB)					
8	TD2	G0(LSB)	G6					
10	TD3	G1	G7 (MSB)					
16	TD4	B0(LSB)	B6					
18	TD5	B1	B7 (MSB)					
25	TD6	(NA)	(NA)					

<LVDS_SET=L or Open>



<LVDS_SET =H>



2) 6 bit input

Trans	smitter	20pin LVDS_SET							
Pin No	Data	=L (GND) or Open	=H (3.3V)						
51	TA0	R0 (LSB)	-						
52	TA1	R1	-						
54	TA2	R2	-						
55	TA3	R3	-						
56	TA4	R4	_						
3	TA5	R5 (MSB)	-						
4	TA6	GO(LSB)	-						
6	TB0	G1	-						
7	TB1	G2	-						
11	TB2	G3	-						
12	TB3	G4	-						
14	TB4	G5 (MSB)	-						
15	TB5	B0(LSB)	-						
19	TB6	B1	-						
20	TC0	B2	-						
22	TC1	B3	-						
23	TC2	B4	-						
24	TC3	B5(MSB)	-						
27	TC4	(NA)	-						
28	TC5	(NA)	-						
30	TC6	DE	-						
50	TD0	GND	-						
2	TD1	GND	-						
8	TD2	GND	-						
10	TD3	GND	-						
16	TD4	GND	-						
18	TD5	GND	-						
25	TD6	(NA)	_						

[note1] pin assignment with LVDS_SET pin (Thine:THC63LVDM83R)





In case of supplying 6 bit signal, it is recommended to connect pin No.17(Rx3-) with VCC(3.3V), and No.18(Rx3+) with GND(0V).

4-3 Interface block diagram

(Computer side)

(TFT-LCD side)

(1) 8bit Mode LVDS_SET=H (20 pin=3.3[v])



(2) 8bit Mode LVDS_SET=L (20 pin=GND or OPEN)



(3) 6bit Mode LVDS_SET=L (20 pin=GND or OPEN)



4-4 Backlight

CN 2, 3, 4,5 The module-side connector : BHSR-02VS-1 (JST) The user-side connector : SM02B-BHSS-1-TB (JST)

Pin no.	symbol	I/O	Function
1	V_{HIGH}	Ι	Power supply for lamp (High voltage side)
2	$\mathbf{V}_{\mathrm{LOW}}$	Ι	Power supply for lamp (Low voltage side)



5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Supply voltage	Vcc	Ta=25	0 ~+ 4.0	V	
Lamp Input voltage	Vhigh	-	0 ~ + 2700	Vrms	
Storage temperature	T_{stg}	-	25 ~ + 60	°C	Note1
Operating temperature	T _{opa}	-	0 ~ + 50	°C	Note1

[Note 1] Humidity : 95%RH Max. (Ta=40)

Maximum wet-bulb temperature at 39 or less. (Ta>40) No condensation.

6. Electrical Characteristics

I	Symbol	Min.	Тур.	Max.	Unit	Remark				
Vaa	Supply voltage		Vcc	+3.0	+3.3	+3.6	V	Note2		
VCC	Current di	issipation	Icc		425	700	mA	Note3		
Permissive input ripple voltage			Vrf			100	mVp-p	Vcc=+3.3V		
Differential	input	Hight	Vth			+100	mV	Vсм=+1.2V		
hreshold vol	tage	Low	VTL	-100			mV	Note1		
Input curren	t (High)		Іон			10	А	VI=2.4V, Vcc=3.6V		
Input current (Low)			Iol			10	А	VI=0V, Vcc=3.6V		
Terminal res	sistor		Rt	100				Differential input		

6-1. TFT-LCD panel driving $Ta=25^{\circ}C$

Note1 VCM: Common mode voltage of LVDS driver.

Note2

1) On-off sequences of Vcc

and data

0<t1<=10ms

0<t2<=10ms

0<t3<=1s

1s<=t4

200ms<=t5



0.9Vcc

0.1Vee

0.1Vec

Power sequence for Backlight is not especially specified, however it is recommended to consider some timing difference between LVDS input and Backlight input as shown above.

0.9Vcc

0 1Vcc

Vcc

If the Backlight lights on before LCD starting, or if the Backlight is kept on after LCD stopping, the screen may look white for a moment or abnormal image may be displayed.

This is caused by variation in output signal from timing generator at LVDS input on or off. It does not cause the damage to the LCD module.

2)Dip conditions for supply voltage		\searrow			
1) $V2 \leq Vcc \leq V1$	V1:3.0V		Î		
$td \le 10ms$	V2 : 2.5V		V2	V1	
2) Vcc <v2< td=""><td></td><td>td</td><td>V</td><td></td><td></td></v2<>		td	V		
Vcc-dip conditions should also		\sim			
follow the on-off conditions.					
[Note3] Typical current situation : 16-gray-ba	ar pattern	RGB RG	B RGB	RGB RGE	í.
Vcc=+3.3V, CK=65MHz		GS 0 6 5	4 658	6556 65	60
Horizontal period =20.7 us					
Gray scale : GS(4n)				-	
n=0~15					

The explanation of each gray scale, GS(4n), is described below section 8-2.

6-2. Backlight

The back light system is an edge-lighting type with 4 CCFTs (Cold Cathode Fluorescent Tube).

The characteristics of the lamp are shown in the following table.

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Lamp current range	I_L	6.5	7.0	-	mArms	Note1
Lamp voltage	V_L	558	572	-	Vrms	IL=4.5 mArms Ta=25 60kHz
Lamp power consumption	PL	-	3.4	4.0	W	Note2 IL=4.5 mArms Ta=25 60kHz
Lamp frequency	F_{L}	49	50	-	kHz	Note3
Kick-off voltage	Vs	562	601	-	Vrms	Ta=0°C 60kHz [Note4]
Lamp life time	TL	-	40,000	-	hour	Note5

The value mentioned below is at the case of one CCFT.

[Note1] A lamp can be light in the range of lamp current shown above.

Maximum rating for current is measured by high frequency current measurement equipment connected to VLOW at circuit showed below.

(Note : To keep enough kick-off voltage and necessary steady voltage for CCFT.) Lamp frequency : 40~70kHz

Ambient temperature : 0~50°C



[Note2] Referential data per one CCFT by calculation (IL×VL).

The data don't include loss at inverter.

- [Note3] Lamp frequency of inverter may produce interference with horizontal synchronous frequency, and this may cause horizontal beat on the display. Therefore, adjust lamp frequency, and keep inverter as far as from module or use electronic shielding between inverter and module to avoid interference.
- [Note4] This is transformer output voltage at 33pF for the ballast capacitor of a DC-AC inverter. The kick-off voltage may rise up in the user set, please decide the open output voltage by checking not to occur lighting failure under operating state.

The open output voltage should be applied to the lamp for more than 1 second to startup. Otherwise the lamp may not be turned on.



[Note5] Lamp life time is defined as the time when either (1) or (2) occurs in the continuous operation under the condition of Ta=25°C and IL=6.0 mA rms.

(1)Brightness becomes 50% of the original value under standard condition. (2) K_{i}^{2} has the set $T_{i} = 0^{\circ}$ converse in 2200 M and K_{i} and K_{i}

(2) Kick-off voltage at Ta=0 $^\circ\!\mathrm{C}$ exceeds 2200 V_{rms} value.

<<Note>>

The performance of the backlight, for example lifetime or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the backlight and the inverter (miss-lighting, flicker, etc.) never occurs. When you confirm it, the module should be operated in the same condition as it is installed in your instrument.

Use the lamp inverter power source incorporating such safeguard as overvoltage / overcurrent protective circuit or lamp voltage waveform detection circuit, which should have individual control of each lamp. In case one circuit without such individual control is connected to more than two lamps, excessive current may flow into one lamp when the other one is not in operation.

<u> </u>							
	Parameter	Symbol	Min.	Typ.	Max.	Unit	
Clock signal	Frequency	1/Tc	50.0	65.0	80.0	MHz	
ENAB signal	Horizontal pariod	тц	1056	1344	1720	clock	
	Holizoitai period	Ш	16.0	20.7	23.4	μs	
	Horizontal period(High)	THd	1024	1024	1024	clock	
	Vertical period	TV	773	806	990	line	
	Vertical period (High)	TVd	768	768	768	line	

7. Timing characteristics of input signals

7-1. Timing characteristics

[Note] In case of using the long vertical period, the deterioration of display quality, flicker etc. may occur.



7-2 Input Data Signals and Display Position on the screen



8. Input Signals, Basic Display Colors and Gray Scale of Each Color

8-1 8bit input

	Colors&	Date signal																								
	Gray scale	Gray	R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	В0	B1	B2	B3	B4	B5	B6	B7
	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1
	Green		0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Cyan		0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1
Color	Red		Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta		Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1
	Yellow		Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White		Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Grav	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	1	\downarrow				,	Ļ							`	ļ							`	/			
of Red	\downarrow	\downarrow	\downarrow					\downarrow						\downarrow												
Reu	Brighter	GS250	0	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\rightarrow	GS251	0	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS252	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	GS1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray	Darker	GS2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	\uparrow	\downarrow				,	Ļ							`	ļ							,	/			
of	\downarrow	\downarrow				,	Ļ							`	ļ							,	,			
Green	Brighter	GS250	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0
	\downarrow	GS251	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0
	Green	GS252	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Gray	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
Scale of	↑	\downarrow				,	Ļ							`	ļ							,	/			
	\downarrow	\downarrow				,	Ļ							`	ļ							,	,			
Blue	Brighter	GS250	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1
	\downarrow	GS251	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1
	Blue	GS252	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1

0 : Low level voltage, 1 : High level voltage. X :Don't care.

Each basic color can be displayed in 253 gray scales from 8 bit data signals. According to the combination of total 24 bit data signals, the 16-million-color display can be achieved on the screen. Basic Color Gray Scale of Red Gray Scale of Green Gray Scale of Blue

8-2 6bit input

r																				
	Colors&	Date signal																		
	Grayscale	Gray Scale	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	В0	B1	B2	В3	B4	B5
	Black	—	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	—	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Basic Color	Green	_	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Cyan	_	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Red	_	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	_	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	_	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	_	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑ (GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Grav	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	↑ (\downarrow			ļ	Ļ					1	ŀ					ļ	Ļ		
of Red	\downarrow	\downarrow	Ļ						Ļ					↓						
	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	↓	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
Gray Scale	↑	Ļ	Ļ						Ļ						↓					
of Green	\downarrow	Ļ				l					1	ļ			↓					
	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
	\downarrow	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑ (GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
Gray Scale of Blue	↑	\downarrow	Ļ						Ļ							ļ	Ļ			
	\downarrow	Ļ	Ļ					\downarrow								ļ	ļ			
	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	1
	\downarrow	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

0 : Low level voltage, 1 : High level voltage. Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

9. Optical Characteristics

 $Ta=25^{\circ}C$, Vcc=+3.3V

Parame	eter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Viewing	Vertical	θ 11	CR>=5	40	55	-	Deg.	[Note1,4]
angle		θ 12		70	80	-	Deg.	
range	Horizontal	θ21, θ 22		70	80	-	Deg.	
	Vertical	θ11	CR>=10	30	45	-	Deg.	
		θ12	-	45	55	-	Deg.	
	Horizontal	θ21, θ 22		50	60	-	Deg.	
Contrast ration		CR	$\theta = 0^0$	300	350	-		[Note2,4]
Response	Rise	$\gamma_{\mathbf{r}}$		-	5	20	ms	[Note3,4]
Time	Fall	γd		-	20	40	ms	
Chromaticity of	of White	X	-	0.283	0.313	0.343		[Note 4]
		у		0.299	0.329	0.359		
Chromaticity of Red		Х		0.551	0.581	0.611		
		у		0.292	0.322	0.352		
Chromaticity of Green		Х	$\theta = 0^0$	0.277	0.307	0.337		
		у		0.516	0.546	0.576		
Chromaticity of Blue		X		0.121	0.151	0.181		
		у		0.097	0.127	0.157		
Luminance of white		YL	-	410	450	-	cd/m^2	IL=6.5mArms
								Fl=60kHz
XV1 ' L 'C	•,	0	-			1.05		[Note 4]
white Uniform	nity	ðw		-	-	1.25	-	[Note 5]

%The measurement shall be executed 30 minutes after lighting at rating.

The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig.2 below.



Fig2-1 Viewing angle measurement method

Fig2-2 Luminance/Contrast ratio/Response time/Chromaticity measurement method

[Note1] Definitions of viewing angle range:



[Note2] Definition of contrast ratio:

The contrast ratio is defined as the following.

Luminance (brightness) with all pixels white

Contrast Ratio (CR) = Luminance (brightness) with all pixels black

[Note3] Definition of response time:

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



[Note4] This shall be measured at center of the screen.

[Note5] Definition of white uniformity:

White uniformity is defined as the following with five measurements (A \sim E).



10. Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
- c) Since the front polarize is easily damaged, pay attention not to scratch it.
- d) Since long contact with water may cause discoloration or spots, wipe off water drop immediately.
- e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- f) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.Handle with care.
- g) Since CMOS LSI is used in this module, take care of static electricity and take the human earth into consideration when handling.
- h) Make sure the four mounting holes of the module are grounded sufficiently. Take electro-magnetic interference (EMI) into consideration.
- i) The module has some printed circuit boards (PCBs) on the back side. Take care to keep them form any stress or pressure when handling or installing the module; otherwise some of electronic parts on the PCBs may be damaged.
- j) Observe all other precautionary requirements in handling components.
- k) When some pressure is added onto the module from rear side constantly, it causes display non-uniformity issue, functional defect, etc. So, please avoid such design.
- When handling LCD modules and assembling them into cabinets, please be noted that long-term storage in the environment of oxidization or deoxidization gas and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the LCD modules.
- 11. Packing form
 - a) Production country : TAIWAN
 - b) Piling number of cartons : maximum 18 cartons
 - c) Packing quantity in one carton : 10 modules
 - d) Carton size : 400.0 mm(W) × 325 mm(H) × 395 mm(D)
 - e) Total mass of one carton filled with full modules : 13.6 kg(typ.)

12 Reliability test items

No.	Test item	Conditions							
1	High temperature storage test	$Ta = 60^{\circ}C$ 240h							
2	Low temperature storage test	$Ta = -25^{\circ}C$ 240h							
3	High temperature	$Ta = 40^{\circ}C$; 95%RH 240h							
	& high humidity operation test	(No condensation)							
4	High temperature operation test	$Ta = 60^{\circ}C$ 240h							
		(The temperature of panel surface)							
5	Low temperature operation test	$Ta = 0^{\circ}C$ 240H							
6	Vibration test(non- operating)	Waveform : Sine wave Frequency : 10 57Hz/Vibration width (one side) : 0.075mm : 58~500Hz/Gravity : 9.8m/s2 Sweep time : 11minutes Test period : 3 hours (1 hour for each direction of X,Y,Z)							
7	Shock test(non- operating)	Max. gravity : 490m/s2 Pulse width : 11ms, sine wave Direction : ±X, ±Y, ±Z, once for each direction.							
8	Thermal shock test(Storage)	Ta= -25° C ~60°C ; 5 cycles Test period : 10 hours (1 hour for each temperature)							
9	Altitude	Ta=50°C,70kPa,3,048m(10,000ft), t=24h (Operating) Ta=70°C,12kPa,15,240m(50,000ft), t=24h (Storage)							

[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

Fig.1. Outline dimensions

