

TFT COLOR LCD MODULE

NL10276BC24-21F

31cm (12.1 Type) XGA LVDS interface (1port)



This DATA SHEET is updated document from DOD-PP-1680(3).

All information is subject to change without notice. Please confirm the sales representative before starting to design your system.



INTRODUCTION

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The products are classified into three grades: "Standard", "Special", and "Specific".

Each quality grade is designed for applications described below. Any customer who intends to use a product for application other than that of Standard is required to contact TMJ sales representative in advance.

The **Standard:** Applications as any failure, malfunction or error of the products are free from any damage to death, human bodily injury or other property (Products Safety Issue) and not related the safety of the public (Social Issues), like general electric devices.

Examples: Office equipment, audio and visual equipment, communication equipment, test and measurement equipment, personal electronic equipment, home electronic appliances, car navigation system (with no vehicle control functions), seat entertainment monitor for vehicles and airplanes, fish finder (except marine radar integrated type), PDA, etc.

The **Special:** Applications as any failure, malfunction or error of the products might directly cause any damage to death, human bodily injury or other property (Products Safety Issue) and the safety of the public (Social Issues) and required high level reliability by conventional wisdom.

Examples: Vehicle/train/ship control system, traffic signals system, traffic information control system, air traffic control system, surgery/operation equipment monitor, disaster/crime prevention system, etc.

The **Specific:** Applications as any failure, malfunction or error of the products might severe cause any damage to death, human bodily injury or other property (Products Safety Issue) and the safety of the public (Social Issues) and developed, designed and manufactured in accordance with the standards or quality assurance program designated by the customer who requires extremely high level reliability and quality.

Examples: Aerospace system (except seat entertainment monitor), nuclear control system, life support system, etc.

The quality grade of this product is the "Standard" unless otherwise specified in this document.



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1. OUTLINE

1.1 STRUCTURE AND PRINCIPLE

Color LCD module NL10276BC24-21F is composed of the amorphous silicon thin film transistor liquid crystal display (a-Si TFT LCD) panel structure with driver LSIs for driving the TFT (Thin Film Transistor) array and a backlight.

The a-Si TFT LCD panel structure is injected liquid crystal material into a narrow gap between the TFT array glass substrate and a color-filter glass substrate.

Color (Red, Green, Blue) data signals from a host system (e.g. signal generator, etc.) are modulated into best form for active matrix system by a signal processing board, and sent to the driver LSIs which drive the individual TFT arrays.

The TFT array as an electro-optical switch regulates the amount of transmitted light from the backlight assembly, when it is controlled by data signals. Color images are created by regulating the amount of transmitted light through the TFT array of red, green and blue dots.

1.2 APPLICATION

• For industrial use

1.3 FEATURES

- High luminance
- High contrast
- Wide viewing angle
- Wide temperature range
- LVDS interface
- Reversible-scan direction
- Selectable 8-bit or 6-bit digital signals for data of RGB
- Long life LED backlight
- Replaceable lamp for backlight
- Acquisition product for UL60950-1/CSA C22.2 No.60950-1-03 (File number: E170632)
- Compliant with the European RoHS directive (2011/65/EU)

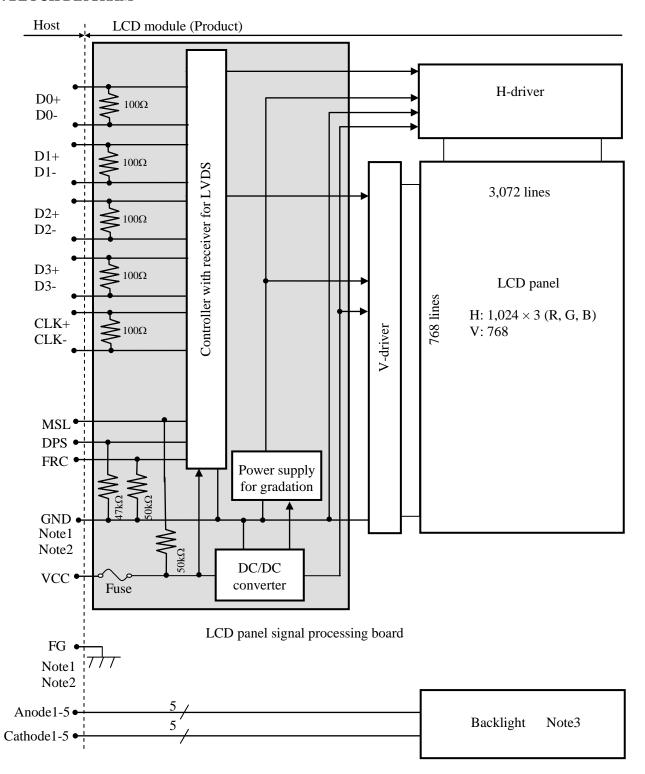


2. GENERAL SPECIFICATIONS

Display area 245.76 (H) × 184.32 (V) mm Diagonal size of display 31cm (12.1 inches) 32cm (14.1 colors (At 8-bit input, FRC terminal= Low or Open) 16.777,216 colors (At 8-bit input, FRC terminal= Low or Open) 16.777,216 colors (At 8-bit input, FRC terminal= Low or Open) 10.24 (H) × 788 (V) pixels RGB (Red dot, Green dot, Blue dot) vertical stripe 0.08 (H) × 0.24 (V) mm 0.08 (H) × 0.24 (V) mm 0.08 (H) × 0.24 (V) mm 0.24 (H) × 0.24 (H)							
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Drive system a-Si TFT active matrix	Diagonal size of display	31cm (12.1 inches)					
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Display color 262.144 colors (At 6-bit input, FRC terminal= Low or Open) Pixel 1,024 (H) × 768 (V) pixels Pixel arrangement RGB (Red dot, Green dot, Blue dot) vertical stripe Dot pitch 0.08 (H) × 0.24 (V) mm Module size 260.5 (W) × 203.0 (H) × 8.7 (D) mm (typ.) Weight 490 g (typ.) Contrast ratio 900:1 (typ.) At the contrast ratio ≥10:1 + Horizontal: Right side 80° (typ.), Left side 80° (typ.) Viewing angle • Vertical: Up side 80° (typ.), Down side 80° (typ.) At DPS= Low or Open: Normal scan • Viewing direction without image reversal: Up side (12 o'clock) Polarizer surface • Viewing direction with contrast peak: Down side (6 o'clock) • Viewing angle with optimum grayscale (γ= 2.2): Normal axis (perpendicular) Polarizer pencil-hardness 4H (min.) [by JIS K5600] At LCD panel center 40 % (typ.) [against NTSC color space] Color gamut Ton+Toff (10%←→90%) Response time Ton+Toff (10%←→90%) Is ms (typ.) Luminance 1 Luminance Luminance 1 Luminance 1 No0cd/m² (typ.), At IL= 50mA/One circuit LVDS interface (1 port) Recommended LED driver board:	·	16,777,216 colors (At 8-bit input, FRC terminal= High)					
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Pixel pitch 0.24 (H) × 0.24 (V) mm	Pixel arrangement	RGB (Red dot, Gre	en dot, Blue dot) vertical stripe				
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LED backlight: Replaceable part • Lamp holder set: 121LHS32 Recommended LED driver board (Option) • LED driver board: Luminance 1 104PW03F Luminance 2 user • Corresponding wiring harness: Luminance 1 121CBL03 Luminance 2 user Power consumption At IL= 50mA/One circuit, Checkered flag pattern 7.5 W (typ.) Luminance 2 At IL= 70mA/One circuit, Checkered flag pattern 7.5 W (typ.)	Power supply voltage		rocessing board: 3.3V				
Power consumption • Lamp holder set: 121LHS32 Recommended LED driver board (Option) • LED driver board: Luminance 1 104PW03F Luminance 2 user • Corresponding wiring harness: Luminance 1 121CBL03 Luminance 2 user At IL= 50mA/One circuit, Checkered flag pattern 7.5 W (typ.) Luminance 2 At IL= 70mA/One circuit, Checkered flag pattern 7.5 W (typ.)	11 2 0						
Power consumption • Lamp holder set: 121LHS32 Recommended LED driver board (Option) • LED driver board: Luminance 1 104PW03F Luminance 2 user • Corresponding wiring harness: Luminance 1 121CBL03 Luminance 2 user At IL= 50mA/One circuit, Checkered flag pattern 7.5 W (typ.) Luminance 2 At IL= 70mA/One circuit, Checkered flag pattern 7.5 W (typ.)							
Recommended LED driver board (Option) • LED driver board: Luminance 1 104PW03F Luminance 2 user • Corresponding wiring harness: Luminance 1 121CBL03 Luminance 2 user Luminance 2 user Luminance 2 for a large of the large pattern of t			. 1011 11022				
Power consumption • LED driver board: Luminance 1 104PW03F Luminance 2 user • Corresponding wiring harness: Luminance 1 121CBL03 Luminance 2 user At IL= 50mA/One circuit, Checkered flag pattern 7.5 W (typ.) At IL= 70mA/One circuit, Checkered flag pattern 7.5 W (typ.)		• Lamp holder se	t: 121LHS32				
Power consumption • LED driver board: Luminance 1 104PW03F Luminance 2 user • Corresponding wiring harness: Luminance 1 121CBL03 Luminance 2 user At IL= 50mA/One circuit, Checkered flag pattern 7.5 W (typ.) At IL= 70mA/One circuit, Checkered flag pattern 7.5 W (typ.)		Recommended LE	D driver board (Option)				
Luminance 1 104PW03F Luminance 2 user • Corresponding wiring harness: Luminance 1 121CBL03 Luminance 2 user Luminance 2 user Luminance 1 At IL= 50mA/One circuit, Checkered flag pattern 7.5 W (typ.) Luminance 2 At IL= 70mA/One circuit, Checkered flag pattern	Backlight		· •				
Luminance 2 user • Corresponding wiring harness: Luminance 1 121CBL03 Luminance 2 user Luminance 2 user Luminance 1 At IL= 50mA/One circuit, Checkered flag pattern 7.5 W (typ.) At IL= 70mA/One circuit, Checkered flag pattern 7.5 W (typ.)	3						
Power consumption • Corresponding wiring harness: Luminance 1 121CBL03 Luminance 2 user Luminance 1 At IL= 50mA/One circuit, Checkered flag pattern 7.5 W (typ.) At IL= 70mA/One circuit, Checkered flag pattern 7.5 W (typ.)		Luminance 2	user				
Power consumption Luminance 1 Luminance 2 Luminance 2 Luminance 1 Luminance 1 Luminance 1 Luminance 2 At IL= 50mA/One circuit, Checkered flag pattern 7.5 W (typ.) At IL= 70mA/One circuit, Checkered flag pattern			<u> </u>				
Power consumption Luminance 2 Luminance 1 Luminance 1 At IL= 50mA/One circuit, Checkered flag pattern 7.5 W (typ.) At IL= 70mA/One circuit, Checkered flag pattern							
Power consumption Luminance 1 At IL= 50mA/One circuit, Checkered flag pattern 7.5 W (typ.) At IL= 70mA/One circuit, Checkered flag pattern							
Power consumption Luminance 1 7.5 W (typ.)							
Luminance 2 At IL= 70mA/One circuit, Checkered flag pattern	Dawan aangree	Luminance I					
10.3 W (typ.)	rower consumption	Luminanaa 2					
		Lummance 2	10.3 W (typ.)				



3. BLOCK DIAGRAM



Note1: Relation between GND (Signal ground) and FG (Frame ground) in the LCD module is as follows.

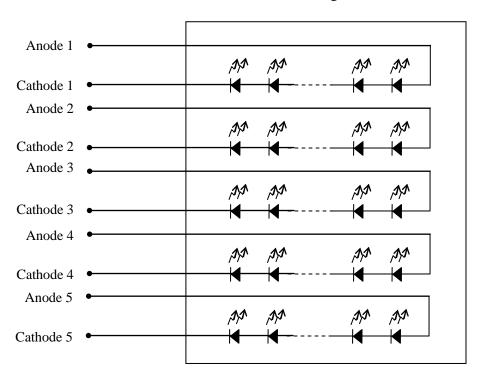
GND- FG Not connected

Note2: GND and FG must be connected to customer equipment's ground, and it is recommended that these grounds to be connected together in customer equipment.



Note3: Backlight detail

Backlight





4. DETAILED SPECIFICATIONS

4.1 MECHANICAL SPECIFICATIONS

Parameter	Specification		Unit
Module size	$260.5 \pm 0.5 \text{ (W)} \times 203.0 \pm 0.5 \text{ (H)} \times 8.7 \pm 0.5 \text{ (D)}$	Note1	mm
Display area	245.76 (H) × 184.32 (V)	Note1	mm
Weight	490(typ.), 540(max.)		g

Note1: See "8. OUTLINE DRAWINGS".

4.2 ABSOLUTE MAXIMUM RATINGS

Parameter				Rating	Unit	Remarks
Power supply voltage	LCD panel sign	nal processing board	VCC	-0.3 to +4.0	V	
Input voltage	_	ay signals Note1	VD	-0.3 to VCC +0.3	V	-
for signals		ion signals Note2	VF	-0.3 to VCC +0.3	V	
Dooldiaht	Forward	Luminance 1	П	60	mA	man ana ainavit
Backlight	current	Luminance 2	IL	80	IIIA	per one circuit
	Storage tempera	ture	Tst	-30 to +80	°C	-
	Luminance 1	Front surface	TopF	-30 to +80	°C	Note3
Operating		Rear surface	TopR	-30 to +80	°C	Note4
temperature	Luminance 2	Front surface	TopF	-30 to +60	°C	Note5
		Rear surface	TopR	-30 to +60	°C	Note6
				≤ 95	%	$Ta \le 40^{\circ}C$
				≤ 85	%	$40^{\circ}C < Ta \leq 50^{\circ}C$
	Relative humid Note7	ıty	RH	≤ 55	%	$50^{\circ}\text{C} < \text{Ta} \le 60^{\circ}\text{C}$
				≤ 36	%	$60^{\circ}\text{C} < \text{Ta} \le 70^{\circ}\text{C}$
				≤ 24	%	$70^{\circ}\text{C} < \text{Ta} \le 80^{\circ}\text{C}$
	Absolute humic Note7	lity	АН	≤ 70 Note8	g/m	Ta= 80°C

Note1: D0+/-, D1+/-, D2+/-, D3+/-, CLK+/-

Note2: DPS, FRC, MSL

Note3: Measured at LCD panel surface (including self-heat)

Note4: Measured at LCD module's rear shield surface (including self-heat)

Note5: Measured at center of LCD panel surface (including self-heat)

Note6: Measured at center of LCD module's rear shield surface (including self-heat)

Note7: No condensation

Note8: Water amount at Ta= 80°C and RH= 24%



4.3 ELECTRICAL CHARACTERISTICS

4.3.1 LCD panel signal processing board

 $(Ta= 25^{\circ}C, Note 1)$

Parameter	Symbol	min.	typ.	max.	Unit	Remarks	
Power supply voltage		VCC	3.0	3.3	3.6	V	-
Power supply current		ICC	-	460 Note2	720 Note3	mA	at VCC= 3.3V
Permissible ripple voltage		VRP	-	-	100	mVp-p	for VCC
Differential input threshold	High	VTH	-	-	+100	mV	at VCM= 1.2V
voltage	Low	VTL	-100	-	-	mV	Note4
Terminating resistance	Terminating resistance		-	100	-	Ω	-
Input voltage for DPS,	High	VFH	0.7VCC	-	VCC	V	CMOS level
FRC and MSL signals	Low	VFL	0	-	0.3VCC	V	Civios level
Input current for DPS,	High	IFH	-	-	300	μΑ	
FRC and MSL signals	Low	IFL	-300	-	-	μΑ	-

Note1: When designing of the power supply, take the measures for the prevention of surge voltage.

Note2: Checkered flag pattern [by IEC 61747-6]

Note3: Pattern for maximum current

Note4: Common mode voltage for LVDS receiver





4.3.2 Backlight

(1) Luminance 1: 800cd/m² (typ.)

(Ta= 25°C, Note1, Note2, Note3, Note4)

Parameter	Symbol	min.	typ.	max.	Unit	Remarks	
Forward current	IL	-	50.0	55.0	mA	-	
	VL -	21.2	24.0	27.2	V	Ta= +25°C at IL= 50 mA /One circuit	
Formulard Voltage		19.0	-	-	V	Ta= +80°C at IL= 50 mA /One circuit	
Forward Voltage			-	-	29.8	V	Ta= -30°C at IL= 50 mA /One circuit
		-	-	30.1	V	Ta= -30°C at IL= 55 mA /One circuit	

(2) Luminance 2: 1000cd/m² (typ.)

(Ta= 25°C, Note1, Note2, Note3, Note4)

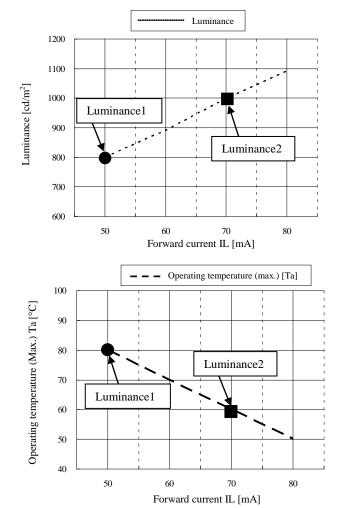
Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Forward current	IL	-	70.0	77.0	mA	-
		22.0	25.1	28.3	V	Ta= +25°C at IL= 70 mA /One circuit
Forward Voltage	371	20.3	-		V	Ta= +60°C at IL= 70 mA /One circuit
rotward voltage	VL .	1	-	31.1	V	Ta= -30°C at IL= 70 mA /One circuit
		-	-	31.4	V	Ta= -30°C at IL= 77 mA /One circuit

Note1: Please drive with constant current.

Note2: The above specifications are for one LED circuit of the backlight.

Note3: The Luminance uniformity may be changed depending on the current variation between 5 circuits. It is recommended that the current value difference amongst the circuits be less than 5%.





Note4: Forward Current vs Luminance / Operating temperature

4.3.3 Power supply voltage ripple

This product works even if the ripple voltage levels are over the permissible values as the following table, but there might be noise on the display image.

Power supp	ply voltage	Ripple voltage Note1 (Measure at input terminal of power supply)	Unit
VCC	3.3V	≤ 100	mVp-p

Note1: The permissible ripple voltage includes spike noise.

4.3.4 Fuse

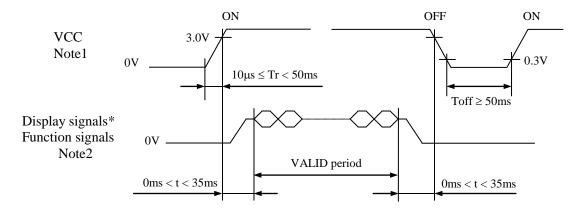
Dawasasasa		Fuse	Datin -	E	Damada	
Parameter	Туре	Supplier	Rating	Fusing current	Remarks	
VCC	ECC1/202AB	KAMAYA ELECTRIC	2.0A	4.04	Note1	
VCC	FCC16202AB	Co., Ltd.	36V	4.0A		

Note1: The power supply's rated current must be more than the fusing current. If it is less than the fusing current, the fuse may not blow in a short time, and then nasty smell, smoke and so on may occur.



4.4 POWER SUPPLY VOLTAGE SEQUENCE

4.4.1 LCD panel signal processing board



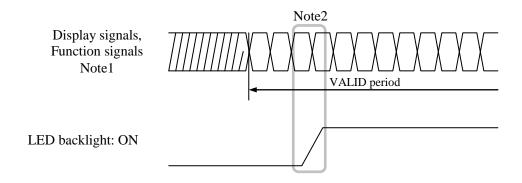
^{*} These signals should be measured at the terminal of 100Ω resistance.

Note1: If there is a voltage variation (voltage drop) at the rising edge of VCC below 3.0V, there is a possibility that a product does not work due to a protection circuit.

Note2: Display signals (D0+/-, D1+/-, D2+/-, D3+/- and CLK+/-) and function signals (DPS, FRC and MSL) must be set to Low or High-impedance, except the VALID period (See above sequence diagram), in order to avoid the circuitry damage.

If some of display and function signals of this product are cut while this product is working, even if the signal input to it once again, it might not work normally. If a customer stops the display and function signals, VCC also must be shut down.

4.4.2 LED driver



Note1: These are the display and function signals for LCD panel signal processing board.

Note2: The backlight should be turned on within the VALID period of display and function signals, in order to avoid unstable data display.



4.5 CONNECTIONS AND FUNCTIONS FOR INTERFACE PINS

4.5.1 LCD panel signal processing board

CN1 socket (LCD module side): FI-SE20P-HFE (Japan Aviation Electronics Industry Limited (JAE))
Adaptable plug: FI-S20S (Japan Aviation Electronics Industry Limited (JAE))

Adaptable plug:				-S20S (Japan	Industry Limit	eu (JAE))	
Pin No. Symbol		Symbol	Signal	Input data s	Input data	Remarks	
TIII	110.	Symbol	Signai	MAP A	MAP B	signal: 6-bit	
1	A	D3+	Pixel data	R0-R1,G0-G1,B0-B1	R6-R7,G6-G7,B6-B7	-	Note1, Note2
•	В	GND	Ground		-	Ground	Note3
2	A	D3-	Pixel data	R0-R1,G0-G1,B0-B1	R6-R7,G6-G7,B6-B7	-	Note1, Note2
2	В	GND	Ground		-	Ground	Note3
3	3	DPS	Selection of scan direction		Reverse scan Normal scan		Note4
۷	1	FRC	Selection of the number of colors	Hi	gh	Low or Open	Note1 Note5
5	5	GND	Ground		Ground		Note3
ć	5	CLK+	Di1 -11-		Direct also de		N-4-2
7	7	CLK-	Pixel clock		Pixel clock		Note2
8	3	GND	Ground		Ground		Note3
Ģ		D2+	Pixel data	D4 D7 DE	D2 D5 D	JE.	Note2
1	0	D2-	rixei data	B4-B7,DE	B2-B5,D	'E	Note2
1	1	GND	Ground		Ground		Note3
1	2	D1+	Pixel data	G3-G7,B2-B3	G1-G5,B0	D1	Note2
1	3	D1-	1 ixel data	G3-G7,B2-B3	ОТ-ОЗ,ДО	-D1	Note2
1	4	GND	Ground		Ground		Note3
1	5	D0+	Dival data	D2 D7 C2	D0 D5 C		Note?
1	6	D0-	Pixel data	R2-R7,G2 R0-R5,G0			Note2
1	7	GND	Ground	Ground			Note3
1	8	MSL	Selection of LVDS input map	Low High or Open Low			Note5
1	19 VCC				Power supply		
20 VCC			Power supply		Note3		

Note1: See "4.6 DISPLAY COLORS AND INPUT DATA SIGNALS".

Note2: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note3: All GND and VCC terminals should be used without any non-connected lines.

Note4: See "4.8 SCANNING DIRECTIONS".

Note5: See "4.5.4 Connection between receiver and transmitter for LVDS".



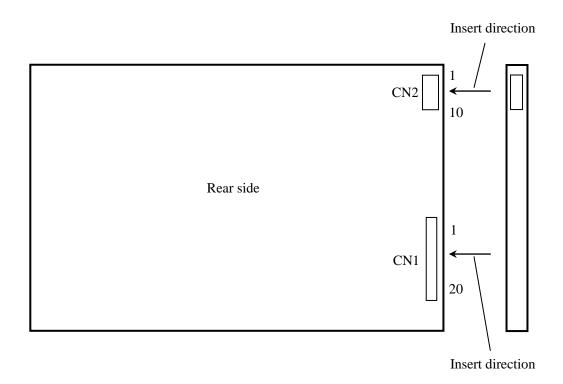


4.5.2 Backlight

CN2 socket (LCD module side): SM10B-SRSS-TB (J.S.T. Mfg. Co., Ltd.)
Adaptable plug: SHR-10V-S (J.S.T. Mfg. Co., Ltd.)

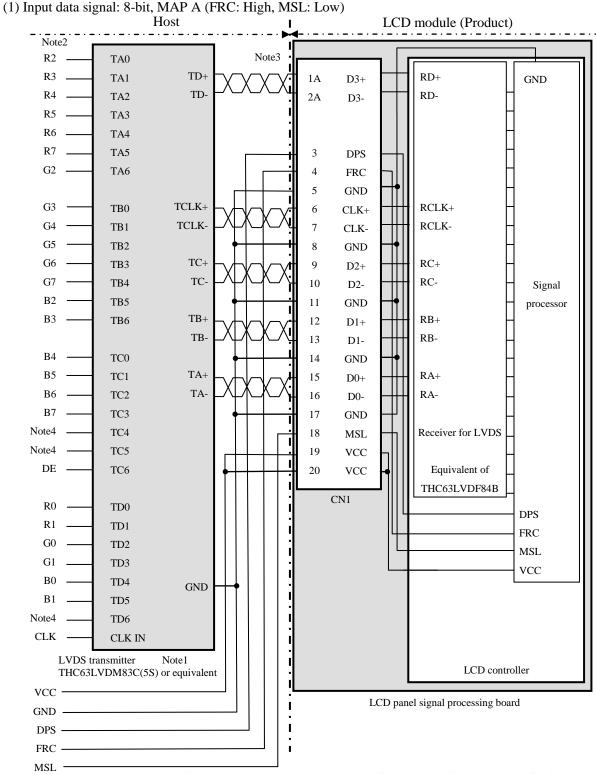
Pin No.	Symbol	Signal	Remarks
1	A1	Anode1	-
2	K1	Cathode1	-
3	A2	Anode2	-
4	K2	Cathode2	-
5	A3	Anode3	-
6	K3	Cathode3	-
7	A4	Anode4	-
8	K4	Cathode4	-
9	A5	Anode5	-
10	K5	Cathode5	-

4.5.3 Positions of socket





4.5.4 Connection between receiver and transmitter for LVDS



Note1: Recommended transmitter: THC63LVDM83C (5S) (THine Electronics Inc.) or equivalent

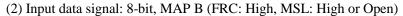
Note2: LSB (Least Significant Bit) – R0, G0, B0 MSB (Most Significant Bit) – R7, G7, B7

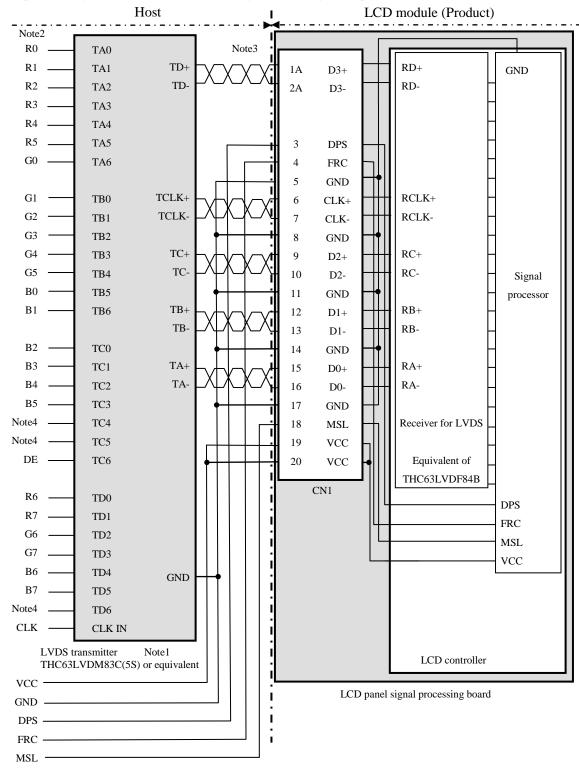
Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note4: Input signals to TC4, TC5 and TD6 are not used inside the product, but do not keep them open to avoid noise problem.

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Note1: Recommended transmitter: THC63LVDM8C (5S) (THine Electronics Inc.) or equivalent

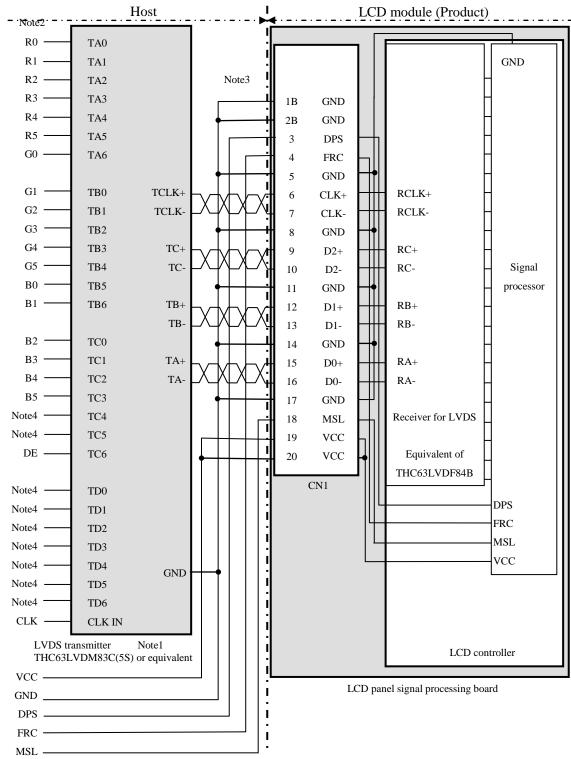
Note2: LSB (Least Significant Bit) – R0, G0, B0 MSB (Most Significant Bit) – R7, G7, B7

Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note4: Input signals to TC4, TC5 and TD6 are not used inside the product, but do not keep them open to avoid noise problem.







Note1: Recommended transmitter THC63LVDM83C (5S) (THine Electronics Inc.) or equivalent

Note2: LSB (Least Significant Bit) – R0, G0, B0 MSB (Most Significant Bit) – R5, G5, B5

Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

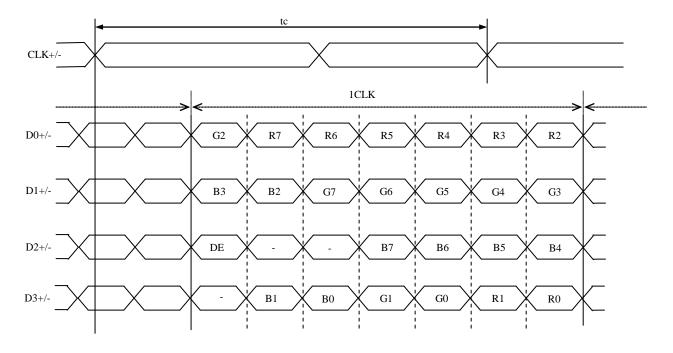
Note4: Input signals to TC4, TC5 and TD0-6 are not used inside the product, but do not keep them open to avoid noise problem.

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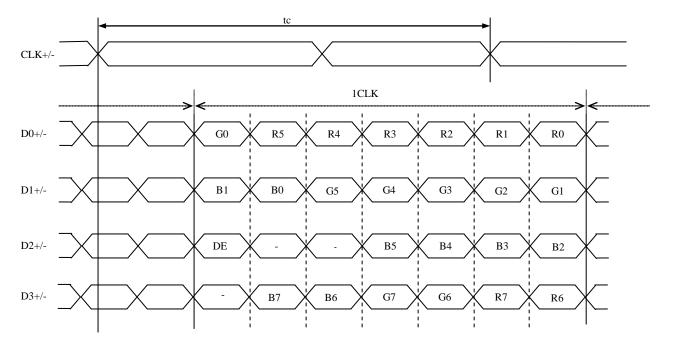


4.5.5 Input data mapping

(1) Input data signal: 8-bit, MAP A

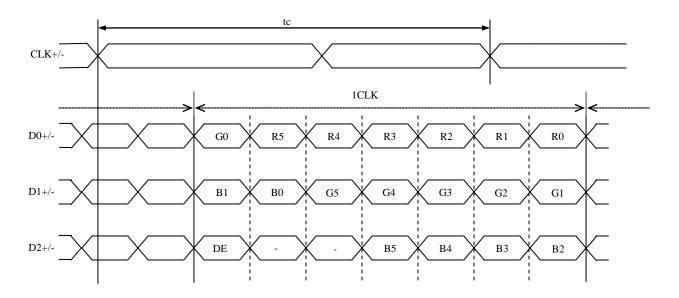


(2) Input data signal: 8-bit, MAP B





(3) Input data signal: 6-bit



4.6 DISPLAY COLORS AND INPUT DATA SIGNALS

4.6.1 Combinations of input data signals, FRC and MSL signals

This product can display equivalent of 16,777,216 colors and 262,144 colors by combination of input data signals, FRC and MSL signals. See the following table.

Combination	Input data signals	Input Data mapping	CN1- Pin No.1 and 2	FRC terminal	MSL terminal	Display colors	Remarks
1	8-bit	MAP A	D3+/-	High	Low	16,777,216	Note1
2	8-bit	MAP B	D3+/-	High	High or Open	16,777,216	Note1
3	6-bit	-	GND	Low or Open	Low	262,144	Note2

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Note1: See "**4.6.2 16,777,216 colors**". Note2: See "**4.6.3 262,144 colors**".



4.6.2 16,777,216 colors

This product can display equivalent of 16,777,216 colors with 256 gray scales by combination ① or ②. (See "**4.6.1 Combinations of input data signals, FRC and MSL signals**".)

Also the relation between display colors and input data signals is as follows.

Display	, anlows	Data signal (0: Low level,				el, 1	: Hiş	gh le	evel))															
Display	COIOIS	R7	R6	R5	R4	R3	R2	R1	R0	G7	' G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	В3	B2	В1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
ors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Basic Colors	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
sic	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Ba	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
e		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
scal	dark	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ay s	\uparrow					:								:								:			
Red gray scale	\downarrow					:								:								:			
Rec	bright	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ale		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
' sc.	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
gray	↑					:								:								:			
Green gray scale	\downarrow			_		:								:								:	_		
Gre	bright	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	C	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
lle		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
sca	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue gray scale	↑					:								:								:			
ie g	\	_	0	0	0	:	0	0	0	0	0	0	0	:	0	0	0	,	1	1	1	:	1	0	1
Βlτ	bright	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	D1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



4.6.3 262,144 colors

This product can display 262,144 colors with 64 gray scales by combination ③. (See "**4.6.1 Combinations of input data signals, FRC and MSL signals** ".)

Also the relation between display colors and input data signals is as follows.

Display colors							Data		al (0:				ligh le	evel)					
Display	COIOIS	R 5	R4	R3	R 2	R 1	R0	G5	G4	G3	G2	G1	G0	B 5	B4	В3	B 2	B 1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
OIS	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Basic colors	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
ısic	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
B	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
e		0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
scal	dark	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red gray scale	↑			:	:						:						:		
d gı	\downarrow			:	:						:						:		
Re	bright	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	ъ.	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ale		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
s sc	dark	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
gra	↑																:		
Green gray scale	•	0	0			0	0		1			0	1	0	0	0	:	0	0
Gre	bright	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
ale	1 1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
' sc	dark ↑	U	U	0.	·	U	U	U	U	U		U	U	U	U	U		1	U
Blue gray scale	1																		
ne 8		0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	. 1	0	1
BI	bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1



4.7 DISPLAY POSITIONS

The following table is the coordinates per pixel (See "4.8 SCANNING DIRECTIONS".).

C (0,	0)					
R G	В					
$\begin{pmatrix} C(&0,&0) \end{pmatrix}$	C(1, 0)	• • •	C(X, 0)	• • •	C(1022, 0)	C(1023, 0)
C(0, 1)	C(1, 1)	• • •	C(X, 1)	• • •	C(1022, 1)	C(1023, 1)
•	•	•	•	•	•	•
•	•	• • •	•	• • •	•	• • •
•	•	•	•	•	•	•
C(0, Y)	C(1, Y)	• • •	C(X, Y)	• • •	C(1022, Y)	C(1023, Y)
•	•	•	•	•	•	•
•	•	• • •	•	• • •	•	•
•	•	•	•	•	•	•
C(0, 766)	C(1, 766)	• • •	C(X, 766)	• • •	C(1022, 766)	C(1023, 766)
C(0, 767)	C(1, 767)	• •	C(X, 767)	• •	C(1022, 767)	C(1023, 767)

4.8 SCANNING DIRECTIONS

The following figures are seen from a front view.

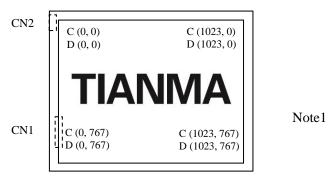


Figure 1. Normal scan (DPS: Low or Open)

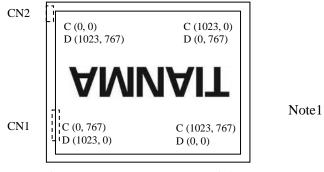


Figure 2. Reverse scan (DPS: High)

Note1: Meaning of C (X, Y) and D (X, Y)

C (X, Y): The coordinates of the display position (See "4.7 DISPLAY POSITIONS".)

D (X, Y): The data number of input signal for LCD panel signal processing board

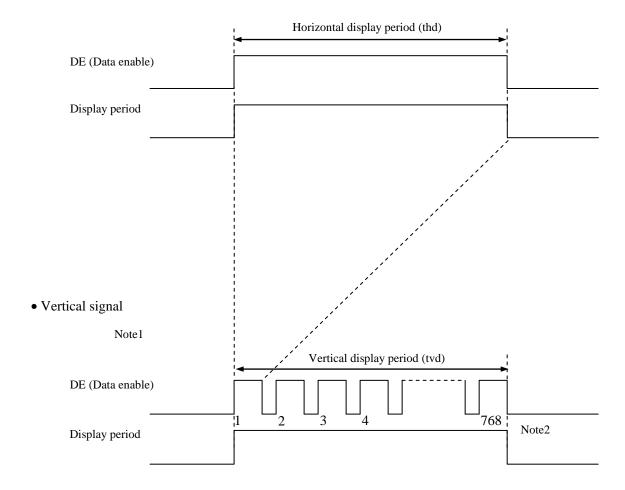


4.9 INPUT SIGNAL TIMINGS

4.9.1 Outline of input signal timings

• Horizontal signal

Note1



Note1: This diagram indicates virtual signal for set up to timing.

Note2: See "4.9.3 Input signal timing chart" for the pulse number.



4.9.2 Timing characteristics

(Note1, Note2, Note3)

	Parameter		Symbol	min.	typ.	max.	Unit	Remarks		
I		quency	1/tc	60.0	65.0	68.0	MHz	15.385ns (typ.)		
CLK	Du	Duty ratio					-			
	Rise tim	-		-		ns	-			
	CLK-DATA	Setup time	-				ns			
DATA	CLK-DATA	Hold time	-		-		ns	-		
	Rise tim	ne, Fall time	-				ns			
		Cycle	th	19.67	20.676	22.4	μs	48.363kHz (typ.)		
	Horizontal	Сусіе	ui	-	1,344	1	CLK	48.303KHZ (typ.)		
		Display period	thd		1,024		CLK	-		
	57 .* 1	Cycle	tv	13.3	16.666	18.5	ms	60.0Hz (typ.)		
DE	Vertical (One frame)	Сусіе	tv	780	806	1	Н	00.0Hz (typ.)		
	(One frame)	Display period	tvd		768		Н	-		
	CLK-DE	Setup time	-		•		ns			
	CLK-DE	Hold time	-	-			ns	-		
	Rise tim	ne, Fall time	-				ns			

Note1: Definition of parameters is as follows.

tc= 1CLK, th= 1H

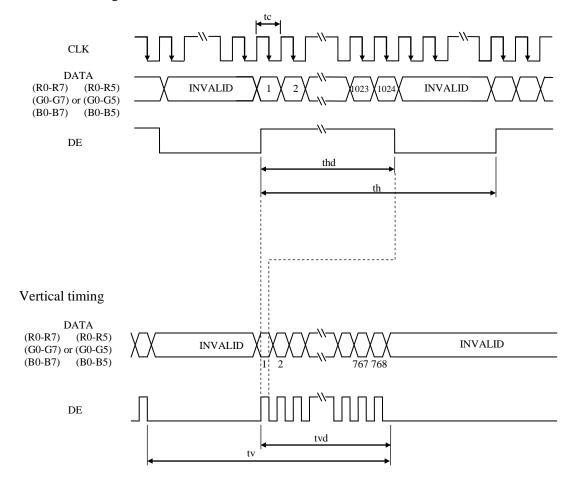
Note2: See the data sheet of LVDS transmitter.

Note3: Vertical cycle (tv) should be specified in integral multiple of Horizontal cycle (th).



4.9.3 Input signal timing chart

Horizontal timing





4.10 OPTICS

4.10.1 Optical characteristics

(Note1, Note2)

Parameter		Condition	Symbol	min.	typ.	max.	Unit	Measuring instrument	Remarks
Luminance 1		White at center $\theta R=0^{\circ}$, $\theta L=0^{\circ}$, $\theta U=0^{\circ}$, $\theta D=0^{\circ}$	L	460	800	-	cd/m ²	BM-5A or equivalent	Note3
Luminance 2		White at center $\theta R=0^{\circ}$, $\theta L=0^{\circ}$, $\theta U=0^{\circ}$, $\theta D=0^{\circ}$	L	575	1,000	-	cd/m ²	BM-5A or equivalent	Note4
Contrast ra	ntio	White/Black at center $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	CR	540	900	-	-	BM-5A or equivalent	Note5
Luminance uni	formity	White $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	LU	-	1.25	1.4	-	BM-5A or equivalent	Note6
	White	x coordinate	Wx	0.263	0.313	0.363	-		
	winte	y coordinate	Wy	0.279	0.329	0.379	-		
	Red	x coordinate	Rx	-	0.564	-	-		
Chromaticity	Reu	y coordinate	Ry	-	0.346	-	-		
Cinomaticity	Green	x coordinate	Gx	ı	0.348	-	1	SR-3 or	Note7
	Green	y coordinate	Gy	1	0.541	-	-	equivalent	Note/
	Blue	x coordinate	Bx	ı	0.151	-	1		
	Blue	y coordinate	By	1	0.134	-	1		
Color gam	nut	$\theta R = 0^{\circ}, \theta L = 0^{\circ}, \theta U = 0^{\circ}, \theta D = 0^{\circ}$ at center, against NTSC color space	С	35	40	-	%		
D 4	·	White to Black	Ton	-	3	5	ms	BM-5A or	Note8
Response to	ime	Black to White	Toff	-	15	21	ms	equivalent	Note9
	Right	θ U= 0°, θ D= 0°, CR \geq 10	θR	70	80	-	0	_	
17:i1-	Left	θU= 0°, θD= 0°, CR≥ 10	θL	70	80	-	0	EZ	Note1
Viewing angle	Up	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}, \ CR \ge 10$	θU	70	80	-	0	Contrast	0
	Down	$\theta R=0^{\circ}, \theta L=0^{\circ}, CR \geq 10$	θD	70	80	-	0		

Note1: These are initial characteristics.

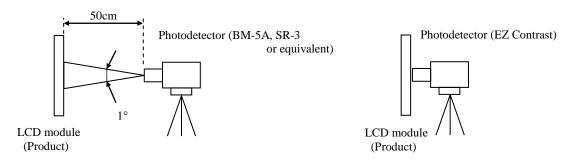
Note2: Measurement conditions are as follows.

Ta= 25°C, VCC= 3.3V, IL= 50mA and 70mA / One circuit, Display mode: XGA,

Horizontal cycle= 1/48.363kHz, Vertical cycle= 1/60.0Hz,

DPS= Low or Open: Normal scan

Optical characteristics are measured at luminance saturation 20minutes after the product works in the dark room. Also measurement methods are as follows.



Note3: At IL= 50mA/One circuit Note4: At IL= 70mA/One circuit

Note5: See "4.10.2 Definition of contrast ratio".

Note6: See "4.10.3 Definition of luminance uniformity".

Note7: These coordinates are found on CIE 1931 chromaticity diagram.

Note8: Product surface temperature: TopF= 30 °C Note9: See "**4.10.4 Definition of response times**". Note10: See "**4.10.5 Definition of viewing angles**".



4.10.2 Definition of contrast ratio

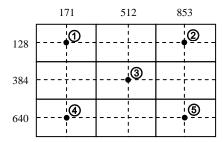
The contrast ratio is calculated by using the following formula.

4.10.3 Definition of luminance uniformity

The luminance uniformity is calculated by using following formula.

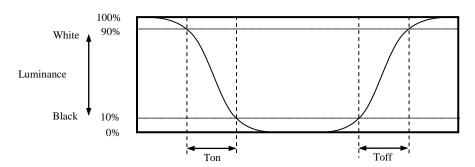
$$Luminance \ uniformity \ (LU) = \ \frac{Maximum \ luminance \ from \ \textcircled{1} \ to \ \textcircled{5}}{Minimum \ luminance \ from \ \textcircled{1} \ to \ \textcircled{5}}$$

The luminance is measured at near the 5 points shown below.

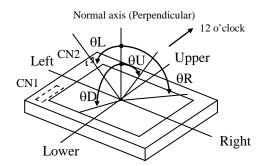


4.10.4 Definition of response times

Response time is measured at the time when the luminance changes from "white" to "black", or "black" to "white" on the same screen point, by photo-detector. Ton is the time when the luminance changes from 90% down to 10%. Also Toff is the time when the luminance changes from 10% up to 90% (See the following diagram.).



4.10.5 Definition of viewing angles





5. ESTIMATED LUMINANCE LIFETIME

The luminance lifetime is the time from initial luminance to half-luminance.

This lifetime is the estimated value, and is not guarantee value.

(1) Luminance 1: 800cd/m² (typ.)

	(31)		
	Estimated luminance lifetime (Life time expectancy) Note1, Note2, Note3	Unit	
LED elementary substance	25°C (Ambient temperature of the product) Continuous operation, IL= 50mA/One circuit	70,000	la la
	80°C (Surface temperature at screen) Continuous operation, IL= 50mA/One circuit	60,000	п

(2) Luminance 2: 1000cd/m² (typ.)

	Estimated luminance lifetime (Life time expectancy) Note1, Note2, Note3	Unit	
LED 1	25°C (Ambient temperature of the product) Continuous operation, IL= 70mA/One circuit	60,000	1.
LED elementary substance	60°C (Surface temperature at screen) Continuous operation, IL= 70mA/One circuit	-	n

Note1: Life time expectancy is mean time to half-luminance.

Note2: Estimated luminance lifetime is not the value for LCD module but the value for LED elementary substance.

Note3: By ambient temperature, the lifetime changes particularly. Especially in case the product works under high temperature environment, the lifetime becomes short.

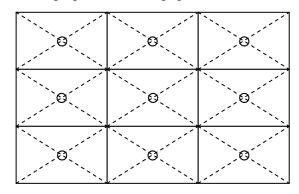


6. RELIABILITY TESTS

Test item	Condition	Judgment Note1
High temperature and humidity (Operation)	 ① 60 ± 2°C, RH= 90%, 240hours ② Display data is black. 	
High temperature (Operation)	 ① 80 ± 3°C, 240hours ② Display data is black. 	
Heat cycle (Operation)	① -30 ± 3°C1hour 80 ± 3°C1hour ② 50cycles, 4 hours/cycle ③ Display data is black.	
Thermal shock (Non operation)	① -30 ± 3°C30minutes 80 ± 3°C30minutes ② 100cycles, 1hour/cycle ③ Temperature transition time is within 5 minutes.	No display malfunctions
ESD (Operation)	 ① 150pF, 150Ω, ±10kV ② 9 places on a panel surface Note2 ③ 10 times each place at 1 sec interval 	
Dust (Operation)	 Sample dust: No. 15 (by JIS-Z8901) 15 seconds stir 8 times repeat at 1 hour interval 	
Vibration (Non operation)	 5 to 100Hz, 19.6m/s² 1 minute/cycle X, Y, Z directions 120 times each direction 	No display malfunctions
Mechanical shock (Non operation)	 539m/ s², 11ms ±X, ±Y, ±Z directions 5 times each direction 	No physical damages

Note1: Display and appearance are checked under environmental conditions equivalent to the inspection conditions of defect criteria.

Note2: See the following figure for discharge points.





7. PRECAUTIONS

7.1 MEANING OF CAUTION SIGNS

The following caution signs have very important meaning. **Be sure to read "7.2 CAUTIONS" and "7.3 ATTENTIONS"!**



This sign has the meaning that a customer will be injured or the product will sustain damage if the customer practices wrong operations.



This sign has the meaning that a customer will be injured if the customer practices wrong operations.

7.2 CAUTIONS



* Do not shock and press the LCD panel and the backlight! There is a danger of breaking, because they are made of glass. (Shock: Equal to or no greater than 539m/s² and equal to or no greater than 11ms, Pressure: Equal to or no greater than 19.6 N (\$\phi\$16mm jig))

7.3 ATTENTIONS 1

7.3.1 Handling of the product

- ① Take hold of both ends without touching the circuit board when the product (LCD module) is picked up from inner packing box to avoid broken down or misadjustment, because of stress to mounting parts on the circuit board.
- ② When the product is put on the table temporarily, display surface must be placed downward.
- 3 When handling the product, take the measures of electrostatic discharge with such as earth band, ionic shower and so on, because the product may be damaged by electrostatic.
- ⓐ The torque for product mounting screws must never exceed 0.23N·m. Higher torque might result in distortion of the bezel. And the length of product mounting screws must be \leq 2.0mm.
- ⑤ The product must be installed using mounting holes without undue stress such as bends or twist (See outline drawings). And do not add undue stress to any portion (such as bezel flat area). Bends or twist described above and undue stress to any portion may cause display mura.
- ⑤ Do not press or rub on the sensitive product surface. When cleaning the product surface, wipe it with a soft dry cloth.
- ② Do not push or pull the interface connectors while the product is working.
- When handling the product, use of an original protection sheet on the product surface (polarizer) is recommended for protection of product surface. Adhesive type protection sheet may change color or characteristics of the polarizer.
- ① Usually liquid crystals don't leak through the breakage of glasses because of the surface tension of thin layer and the construction of LCD panel. But, if you contact with liquid crystal by any chance, please wash it away with soap and water.



7.3.2 Environment

- ① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in packing box with antistatic pouch in room temperature to avoid dusts and sunlight, when storing the product.
- ② In order to prevent dew condensation occurred by temperature difference, the product packing box must be opened after enough time being left under the environment of an unpacking room. Evaluate the storage time sufficiently because dew condensation is affected by the environmental temperature and humidity. (Recommended leaving time: 6 hours or more with the original packing state after a customer receives the package)
- 3 Do not operate in high magnetic field. If not, circuit boards may be broken.
- ④ This product is not designed as radiation hardened.

7.3.3 Characteristics

The following items are neither defects nor failures.

- ① Characteristics of the LCD (such as response time, luminance, color uniformity and so on) may be changed depending on ambient temperature. If the product is stored under condition of low temperature for a long time, it may cause display mura. In this case, the product should be operated after enough time being left under condition of operating temperature.
- ② Display mura, flickering, vertical streams or tiny spots may be observed depending on display patterns.
- 3 Do not display the fixed pattern for a long time because it may cause image sticking. Use a screen saver, if the fixed pattern is displayed on the screen.
- 4 The display color may be changed depending on viewing angle because of the use of condenser sheet in the backlight.
- ⑤ Optical characteristics may be changed depending on input signal timings.

7.3.4 Others

- ① All VCC and GND terminals should be used without any non-connected lines.
- ② Do not disassemble a product or adjust variable resistors.
- ③ See "REPLACEMENT MANUAL FOR LAMP HOLDER SET", when replacing lamp holder set.
- 4 Pack the product with the original shipping package, in order to avoid any damages during transportation, when returning the product to TMJ for repairing and so on.
- (5) The information of China RoHS (II) six hazardous substances or elements in this product is as follows.

	China RoHS (II) six hazardous substances or elements									
Lead (Pb)	Mercury (Hg)	Cadmium (Cd)	Hexavalent Chromium (Cr VI)	Polybrominated Biphenys (PBB)	Polybrominated Biphenyl Ethers (PBDE)					
×	0	0	0	0	0					

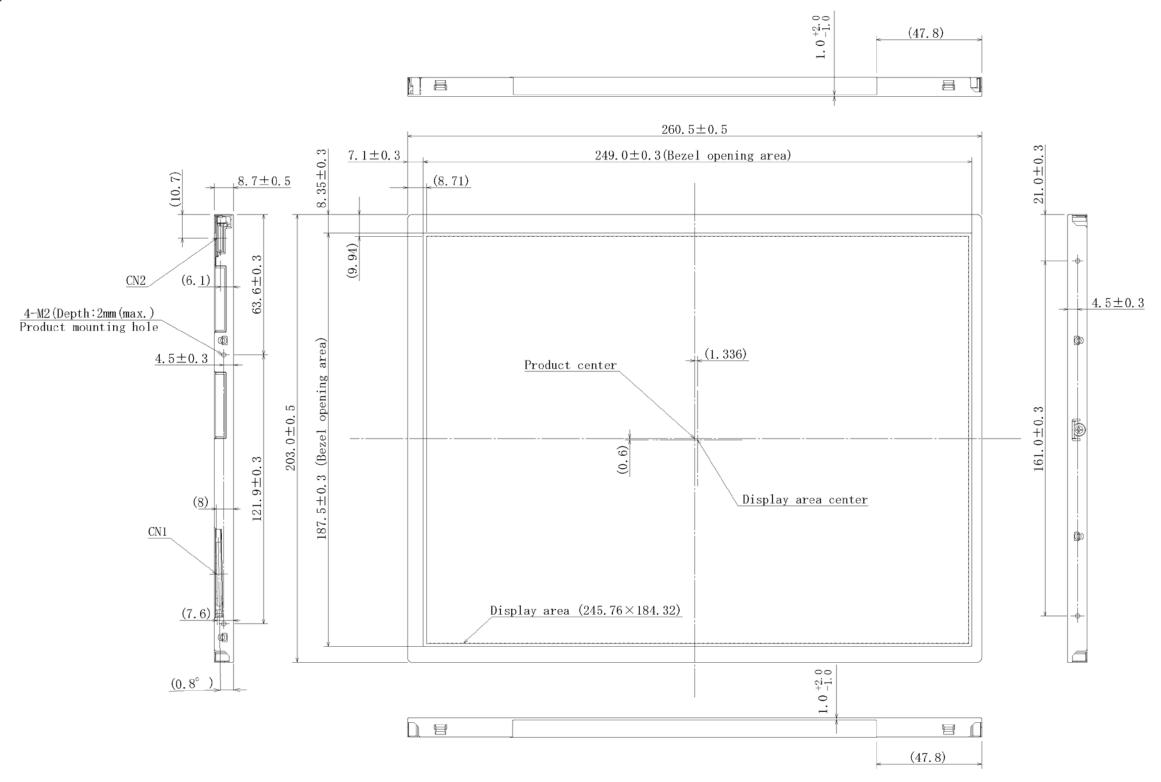
Note1: (): This indicates that the poisonous or harmful material in all the homogeneous materials for this part is equal or below the limitation level of GB/T26572-2011 standard regulation.

X: This indicates that the poisonous or harmful material in all the homogeneous materials for this part is above the limitation level of GB/T26572-2011 standard regulation.



8. OUTLINE DRAWINGS

8.1 FRONT VIEW

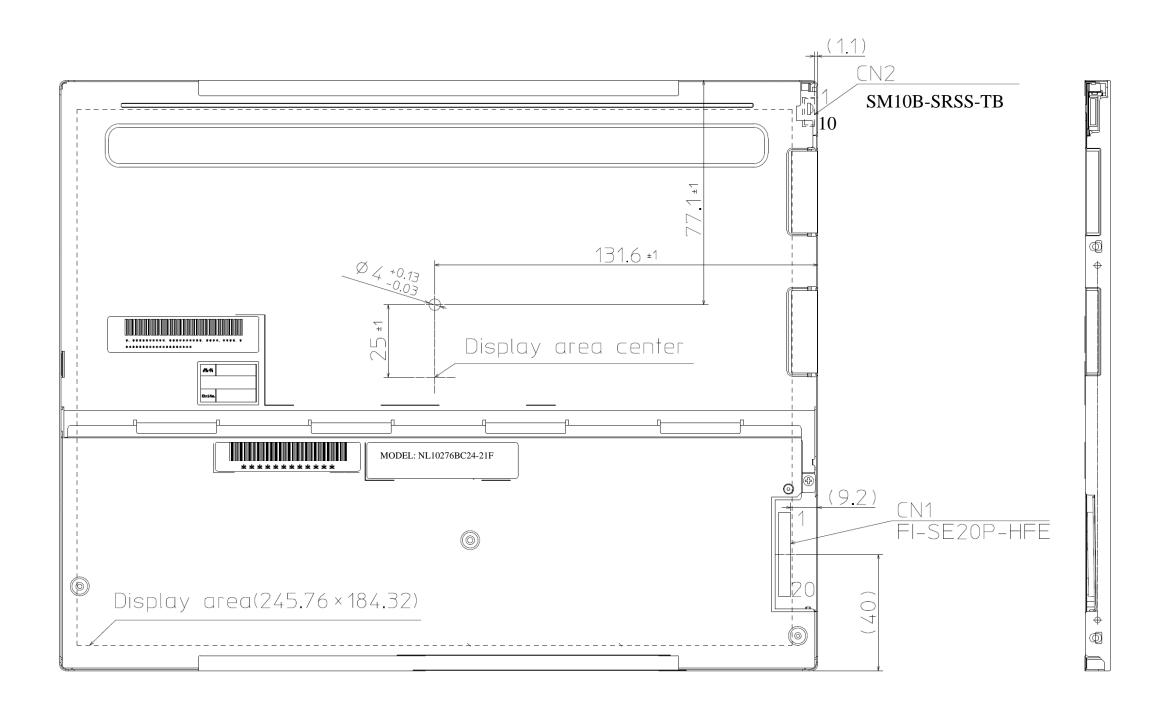


Note1: The values in parentheses are for reference.

Note2: The torque for product mounting screws must never exceed $0.23N \cdot m$. And the length of product mounting screws must be $\leq 2.0mm$.

Unit: mm

8.2 REAR VIEW



Note1: The values in parentheses are for reference.

Note2: The torque for product mounting screws must never exceed $0.23N \cdot m$. And the length of product mounting screws must be $\leq 2.0mm$.

Unit: mm