

MODEL NO. : TM023KDH19ISSUED DATE: 2010-05-22VERSION : Ver 1.0☒ Preliminary Specification☐ Final Product Specification

Customer : \_\_\_\_\_

| Approved by | Notes |
|-------------|-------|
|             |       |

SHANGHAI TIANMA Confirmed :

| Prepared by | Checked by | Approved by |
|-------------|------------|-------------|
|             |            |             |

This technical specification is subjected to change without notice



## Table of Contents

|  |    |
|--|----|
| Coversheet.....                            | 1  |
| Table of Contents .....                    | 2  |
| Record of Revision.....                    | 3  |
| 1 General Specifications .....             | 4  |
| 2 Input/Output Terminals .....             | 5  |
| 3 Absolute Maximum Ratings .....           | 7  |
| 4 Electrical Characteristics.....          | 8  |
| 5 Timing Chart.....                        | 10 |
| 6 Power On/Off Sequence .....              | 17 |
| 7 Optical Characteristics .....            | 19 |
| 8 Environmental / Reliability Tests.....   | 23 |
| 9 Mechanical Drawing.....                  | 24 |
| 10 Packing Drawing .....                   | 25 |
| 11 Precautions for Use of LCD Modules..... | 26 |

[illegible]



## 1 General Specifications

| Feature                    |                                 | Spec                   |
|----------------------------|---------------------------------|------------------------|
| Display Spec               | Size                            | 2.30 inch              |
|                            | Resolution                      | 320(RGB) x 240         |
|                            | Interface                       | CPU 8/16 bits          |
|                            | Color Depth                     | 65K/262K               |
|                            | Technology Type                 | a-Si                   |
|                            | Pixel Pitch (mm)                | 0.1461x 0.1461         |
|                            | Pixel Configuration             | R.G.B Vertical Stripe  |
|                            | Display Mode                    | TM with Normally White |
|                            | Surface Treatment(Up Polarizer) | Clear Type (3H)        |
|                            | Viewing Direction               | 6 o'clock              |
|                            | Gray Scale Inversion Direction  | 12 o'clock             |
| Mechanical Characteristics | LCM (W x H x D) (mm)            | 50.90x45.80x2.25       |
|                            | Active Area(mm)                 | 46.752x35.064          |
|                            | With /Without TSP               | Without TSP            |
|                            | Weight (g)                      | 8.44                   |
|                            | LED Numbers                     | 4 LED                  |
| Electronic                 | Driver IC                       | ILI9342                |

Note 1: Viewing direction for best image quality is different from TFT definition, there is a 180 degree shift.

Note 2 : Requirements on Environmental Protection: Q/S0002

Note 3: LCM weight tolerance:  $\pm 5\%$



## 2 Input/Output Terminals

### 2.1 TFT LCD Panel

| No | Symbol | I/O | Description        | Remark |
|----|--------|-----|--------------------|--------|
| 1  | LEDK   | P   | LED Cathode        |        |
| 2  | LEDA4  | P   | LED Anode          |        |
| 3  | LEDA3  | P   | LED Anode          |        |
| 4  | LEDA2  | P   | LED Anode          |        |
| 5  | LEDA1  | P   | LED Anode          |        |
| 6  | GND    | P   | Power Ground       |        |
| 7  | RESET  | I   | Input RESET signal |        |
| 8  | DB15   | I   | Data Bus           |        |
| 9  | DB14   | I   | Data Bus           |        |
| 10 | DB13   | I   | Data Bus           |        |
| 11 | DB12   | I   | Data Bus           |        |
| 12 | DB11   | I   | Data Bus           |        |
| 13 | DB10   | I   | Data Bus           |        |
| 14 | DB9    | I   | Data Bus           |        |
| 15 | DB8    | I   | Data Bus           |        |
| 16 | DB7    | I   | Data Bus           |        |
| 17 | DB6    | I   | Data Bus           |        |
| 18 | DB5    | I   | Data Bus           |        |
| 19 | DB4    | I   | Data Bus           |        |
| 20 | DB3    | I   | Data Bus           |        |
| 21 | DB2    | I   | Data Bus           |        |
| 22 | DB1    | I   | Data Bus           |        |
| 23 | DB0    | I   | Data Bus           |        |
| 24 | IM0    | I   | Mode select        |        |



|    |       |    |  |  |
|----|-------|----|--|--|
| 25 | RD    | I  | A read strobe signal and enables an operation to read out data when the signal is low. |  |
| 26 | WR    | I  | A write strobe signal and enables an operation to write data when the signal is low.   |  |
| 27 | RS    | I  | A register select signal   |  |
| 28 | CS    | I  | A chip select signal   |  |
| 29 | IOVCC | P  | IO Pad and Digital power supply  |  |
| 30 | VCC   | P  | Analog power supply  |  |
| 31 | VCI   | P  | Analog power supply  |  |
| 32 | GND   | P  | Power Ground   |  |
| 33 | NC    | -- | Not Connected  |  |
| 34 | NC    | -- | Not Connected  |  |
| 35 | NC    | -- | Not Connected  |  |
| 36 | NC    | -- | Not Connected  |  |

Table 2.1 input terminal pin assignment

Note2-1: I/O definition:

I----Input

O---Output

P----Power/ Ground

NC--- Not Connected

| IM0 | Interface | DB pin   |
|-----|-----------|----------|
| 0   | 8 bit     | DB0~DB7  |
| 1   | 16bit     | DB0~DB15 |

Table 2.2 8/16 bit selection pin



### 3 Absolute Maximum Ratings

#### 3.1 Driving TFT LCD Panel

 $T_a = 25^{\circ}\text{C}$ 

| Item                       | Symbol                           | Min  | Max           | Unit               | Remark |
|----------------------------|----------------------------------|------|---------------|--------------------|--------|
| Logic Supply Voltage       | IOVCC                            | -0.3 | 3.6           | V                  |        |
| Analog Supply Voltage      | VCI/VCC                          | -0.3 | 4.8           | V                  |        |
| Input Signal Voltage       | DB0~DB15, IM0,WR, RS,CS,RESET,RD | -0.3 | IOVCC<br>+0.3 | V                  |        |
| Back Light Forward Current | $I_{LED}$                        | --   | 25            | mA                 |        |
| Operating Temperature      | $T_{OPR}$                        | -20  | 70            | $^{\circ}\text{C}$ |        |
| Storage Temperature        | $T_{STG}$                        | -30  | 80            | $^{\circ}\text{C}$ |        |



## 4 Electrical Characteristics

### 4.1 Driving TFT LCD Panel

GND=0V, Ta=25°C

| Item                           |            | Symbol            | Min       | Typ | Max       | Unit | Remark                               |
|--------------------------------|------------|-------------------|-----------|-----|-----------|------|--------------------------------------|
| Logic Supply Voltage           |            | IOVCC             | 1.65      | 1.8 | 3.3       | V    |                                      |
| Analog Supply Voltage          |            | VCI               | 2.3       | 2.8 | 4.8       | V    |                                      |
| Input Signal Voltage           | Low Level  | V <sub>IL</sub>   | GND       | --  | 0.2xIOVCC | V    | DB0~DB15, WR, IM0, RD, RS, CS, RESET |
|                                | High Level | V <sub>IH</sub>   | 0.8xIOVCC | --  | IOVCC     | V    |                                      |
| Output Signal Voltage          | Low Level  | V <sub>OL</sub>   | GND       | --  | 0.2xIOVCC | V    |                                      |
|                                | High Level | V <sub>OH</sub>   | 0.8xIOVCC | --  | IOVCC     | V    |                                      |
| (Panel+ LSI) Power Consumption |            | Black Mode (60Hz) | --        | --  | --        | mW   |                                      |
|                                |            | Sleeping Mode     | --        | --  | --        | mW   |                                      |

Note: We will provide the power consumption after we test the samples.

### 4.2 Driving Backlight Ta=25°C

| Item              | Symbol          | Min | Typ | Max | Unit | Remark             |
|-------------------|-----------------|-----|-----|-----|------|--------------------|
| Forward Current   | I <sub>F</sub>  | --  | 15  | 25  | mA   |                    |
| Forward Voltage   | V <sub>F</sub>  | --  | 3.2 | --  | V    | 4 LEDs in parallel |
| Power Consumption | W <sub>BL</sub> | --  | 192 | --  | mW   |                    |

Note1: Figure below shows the connection of backlight LED.

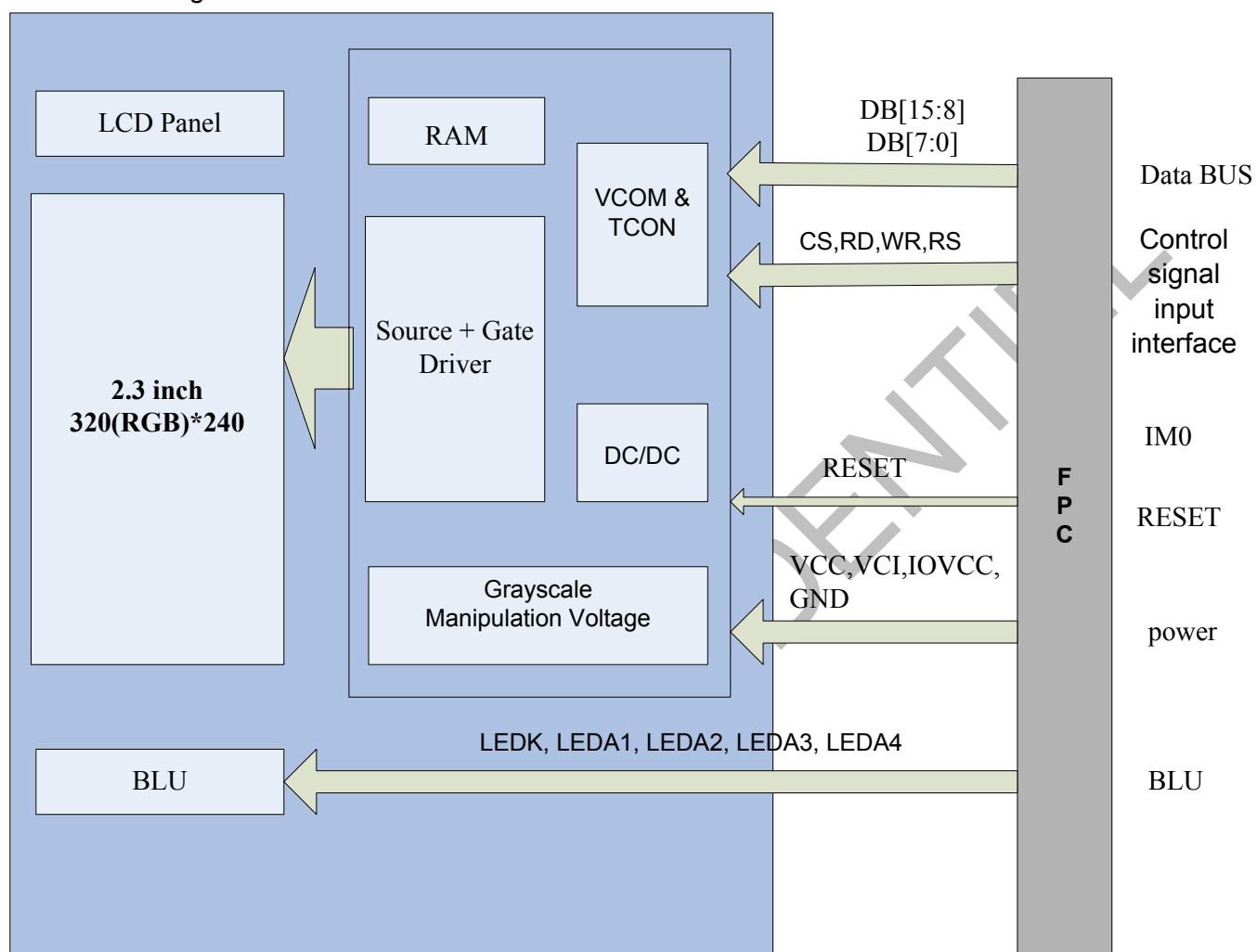


Note 2: One LED: I<sub>F</sub> =15 mA, V<sub>F</sub> =3.2V





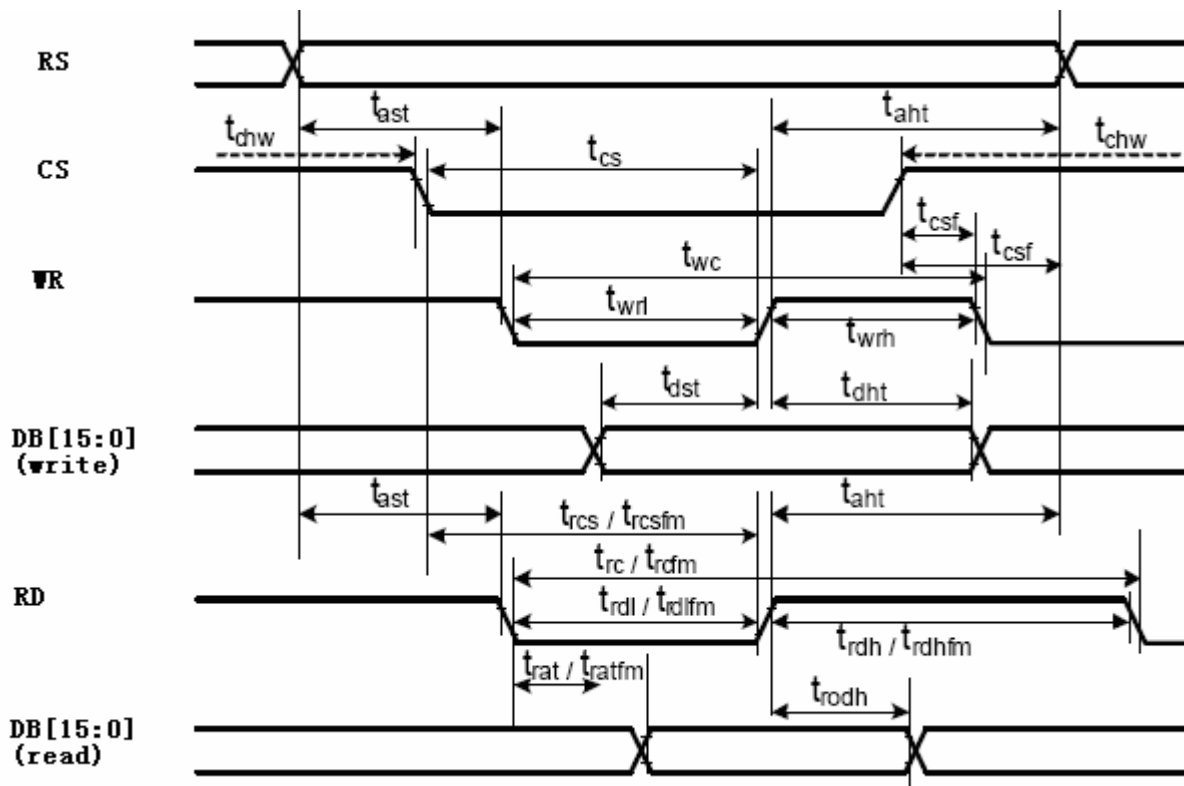
## 4.3 Block Diagram





## 5 Timing Chart

### 5.1 Interface Characteristics



### 5.2 Interface Timing Parameters

#### Normal Write Mode

| Signal  | Symbol      | Parameter                         | Spec |     |      | Description       |
|---------|-------------|-----------------------------------|------|-----|------|-------------------|
|         |             |                                   | Min  | Max | Unit |                   |
| RS      | $t_{AST}$   | Address setup time                | 0    | -   | ns   | -                 |
|         | $t_{AHT}$   | Address hold time(Write/Read)     | 10   | -   | ns   | -                 |
| CS      | $t_{CHW}$   | Chip select "H" pulse width       | 0    | -   | ns   | -                 |
|         | $t_{CS}$    | Chip select setup time (Write)    | 15   | -   | ns   | -                 |
|         | $t_{RCS}$   | Chip select setup time (Read ID)  | 45   | -   | ns   | -                 |
|         | $t_{RCSFM}$ | Chip select setup time (Read FM)  | 355  | -   | ns   | -                 |
|         | $t_{CSF}$   | Chip select wait time(Write/Read) | 10   | -   | ns   | -                 |
| WR      | $t_{WC}$    | Write cycle                       | 65   | -   | ns   | -                 |
|         | $t_{WRH}$   | Control pulse "H" duration        | 15   | -   | ns   | -                 |
|         | $t_{WRL}$   | Control pulse "L" duration        | 15   | -   | ns   | -                 |
| RD (ID) | $t_{RC}$    | Read cycle (ID)                   | 160  | -   | ns   | When read ID data |
|         | $t_{RDH}$   | Control pulse "H" duration (ID)   | 90   | -   | ns   | -                 |

The information contained herein is the exclusive property of SHANGHAI TIANMA MICRO-ELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI TIANMA MICRO-ELECTRONICS Corporation.



|           |             |                                 |     |     |    |   |
|-----------|-------------|---------------------------------|-----|-----|----|---|
|           | $t_{RDL}$   | Control pulse "L" duration (ID) | 45  |     |    |   |
| RD (FM)   | $t_{RCFM}$  | Read cycle (FM)                 | 450 | -   | ns | When read from frame memory                           |
|           | $t_{RDHFM}$ | Control pulse "H" duration (FM) | 90  |     |    |   |
|           | $t_{RDLFM}$ | Control pulse "L" duration (FM) | 355 |     |    |   |
| DB[15:0], | $t_{DST}$   | Data setup time                 | 10  | -   | ns | For maximum<br>$C_L=30pF$<br>For minimum<br>$C_L=8pF$ |
|           | $t_{DHT}$   | Data hold time                  | 10  | -   |    |   |
|           | $t_{RAT}$   | Read access time (ID)           | -   | 40  |    |   |
|           | $t_{RATFM}$ | Read access time (FM)           | -   | 340 |    |   |
|           | $t_{ODH}$   | Output disable time             | 20  | 80  |    |   |

Table 5.2 CPU Interface Timing Parameters

### 5.3 Interface Register write/read timing

#### 5.3.1 System Bus Interface Register or GRAM Write Timing

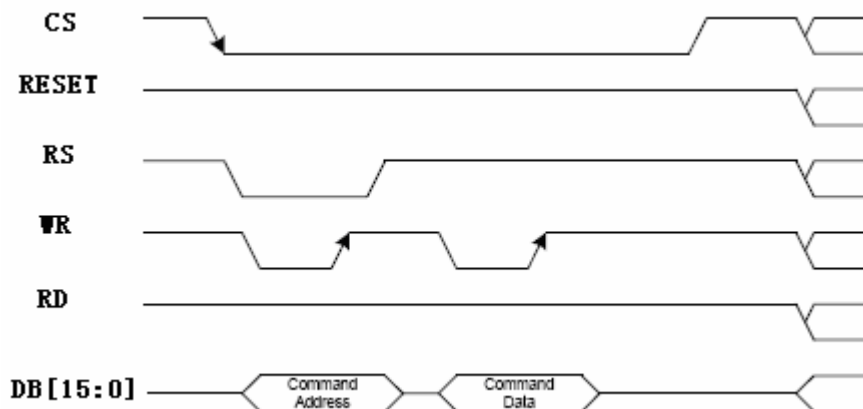


Figure 5.3.1 System Bus Interface Register or GRAM Write Timing



## 5.3.2 System Bus Interface Register or GRAM Read Timing

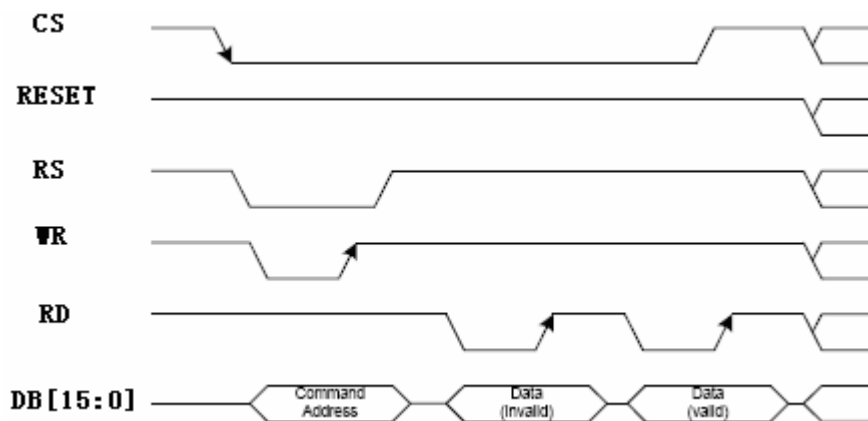


Figure 5.3.1 System Bus Interface Register or GRAM Read Timing

## 5.4 GRAM Write/Read Data Format

## 5.4.1 Write data for RGB 5-6-5 (65k colors) bits input in 8-bit parallel Interface

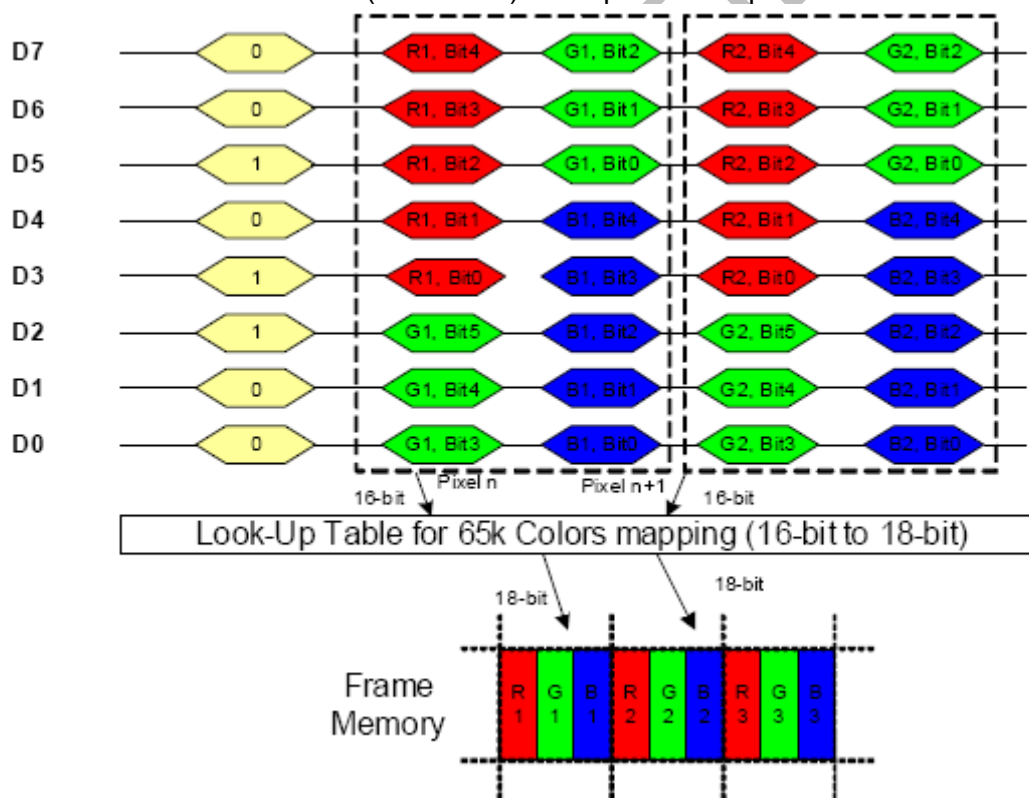


Figure 5.4.1 Write data for RGB 5-6-5 (65k colours) bits input in 8-bit parallel Interface



## 5.4.2 Write data for RGB 6-6-6 (262k colours) bits input in 8-bit parallel Interface

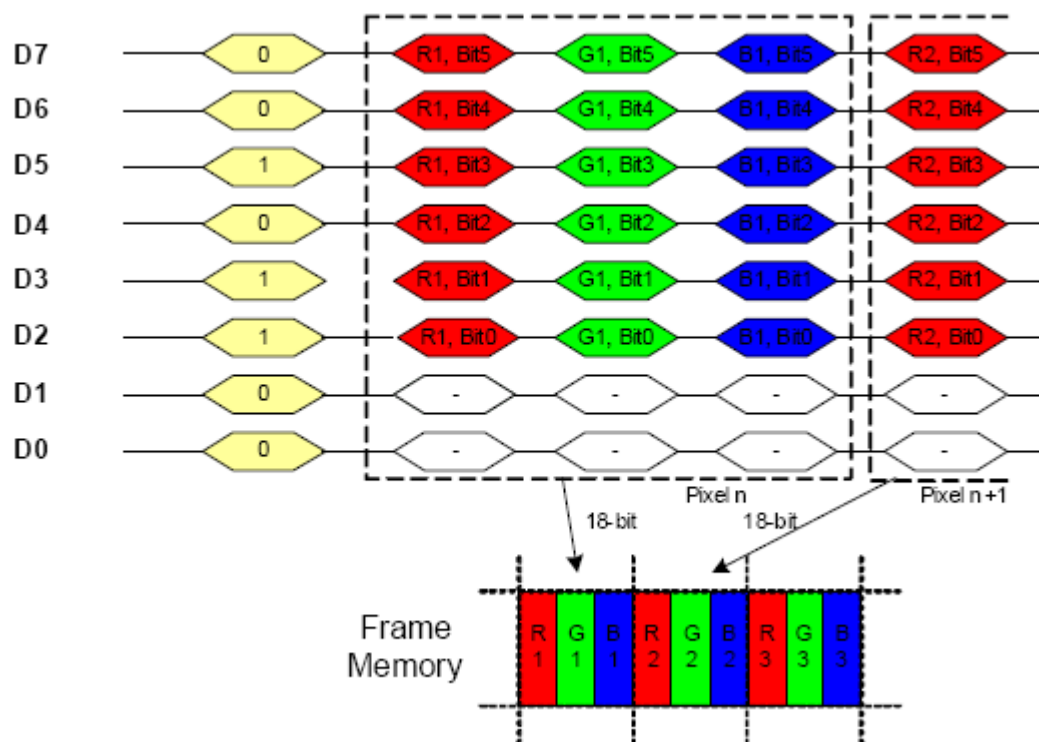


Figure 5.4.2 Write data for RGB 6-6-6 (262k colours) bits input in 8-bit parallel Interface



## 5.4.3 Write data for RGB 5-6-5 (65k colors) bits input in 16-bit parallel Interface

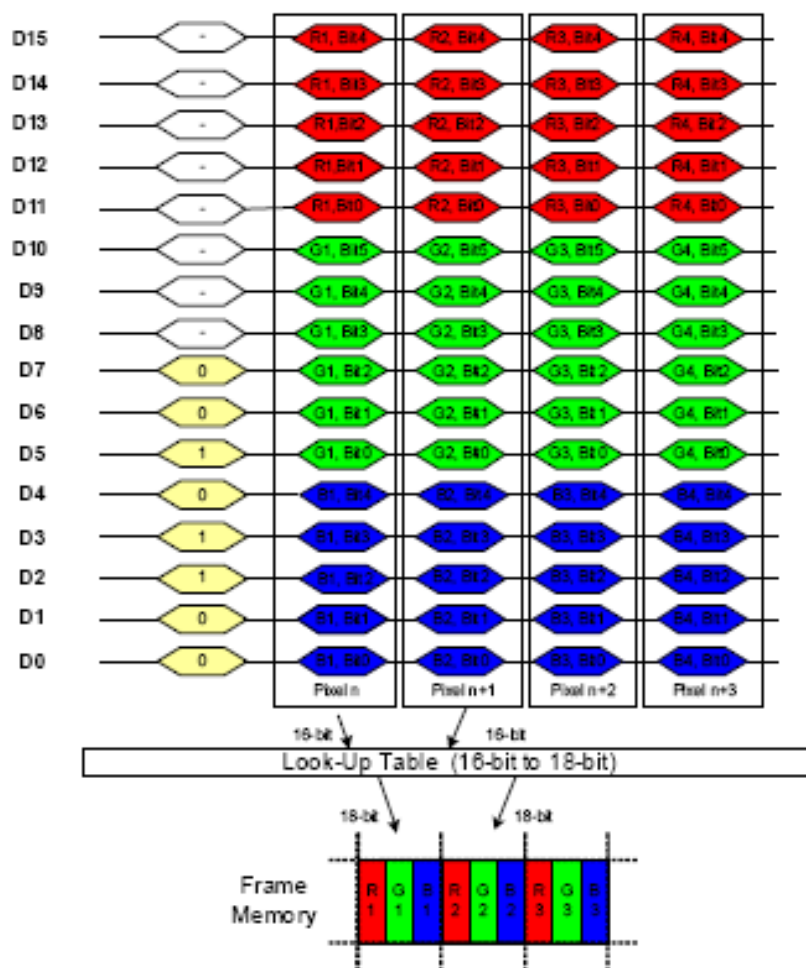


Figure 5.4.3 Write data for RGB 5-6-5 (65k colors) bits input in 16-bit parallel Interface



## 5.4.4 Write data for RGB 6-6-6 (262k colors) bits input in 16-bit parallel Interface

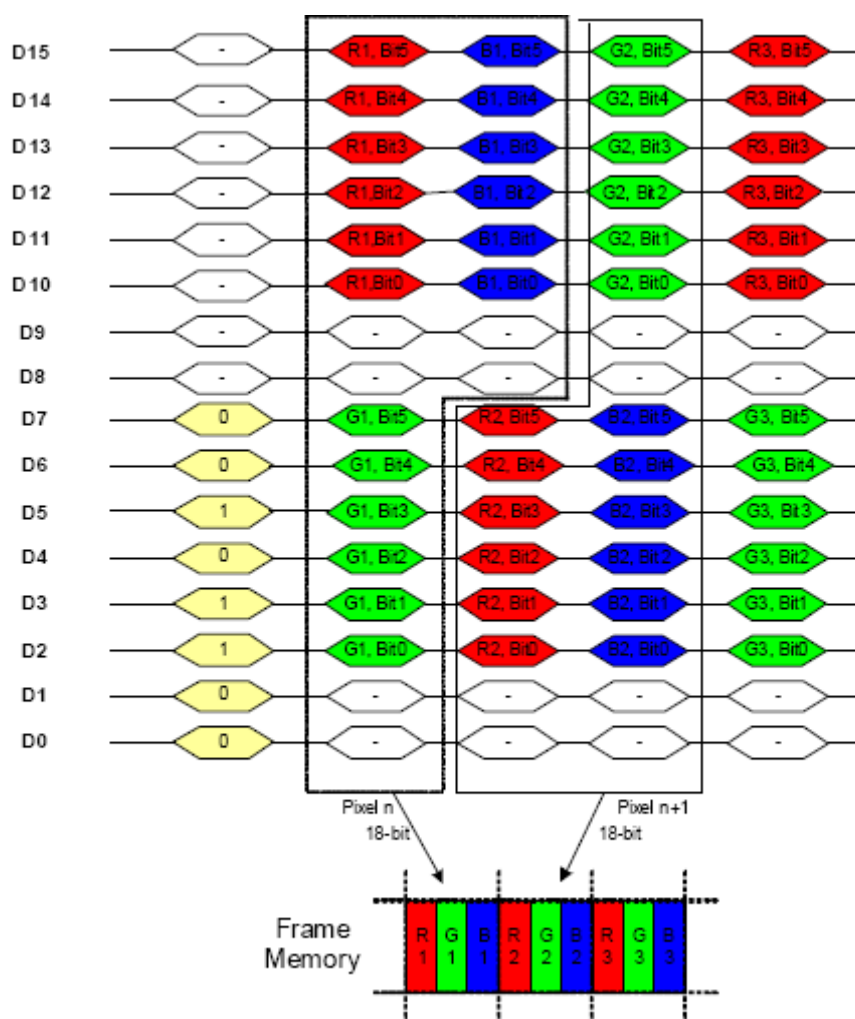


Figure 5.4.4 Write data for RGB 6-6-6 (262k colors) bits input in 16-bit parallel Interface

## 5.5 Reset Timing Characteristics

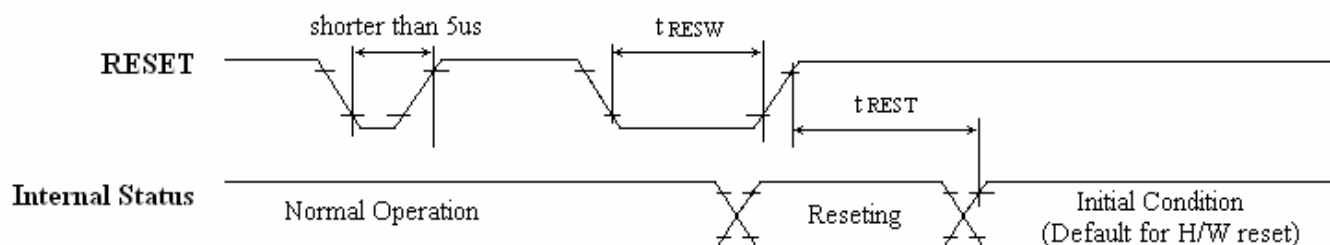


Figure 5.6.1 Reset Input Timing

| Symbol     | Parameter       | Related Pins | Spec. |      |      | Note | Unit |
|------------|-----------------|--------------|-------|------|------|------|------|
|            |                 |              | Min.  | Typ. | Max. |      |      |
| $t_{RESW}$ | Reset low pulse | RESET        | 10    | -    | -    | -    | us   |

The information contained herein is the exclusive property of SHANGHAI TIANMA MICRO-ELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI TIANMA MICRO-ELECTRONICS Corporation.



|                   | width               |   |   |   |     |  |    |
|-------------------|---------------------|---|---|---|-----|--|----|
| $t_{\text{REST}}$ | Reset complete time | - | - | - | 5   | When reset applied during "Sleep In mode"  | ms |
|                   |                     | - |   | - | 120 | When reset applied during "Sleep Out mode" | ms |

Table 5.6 Reset Timing Parameters

Note 1:

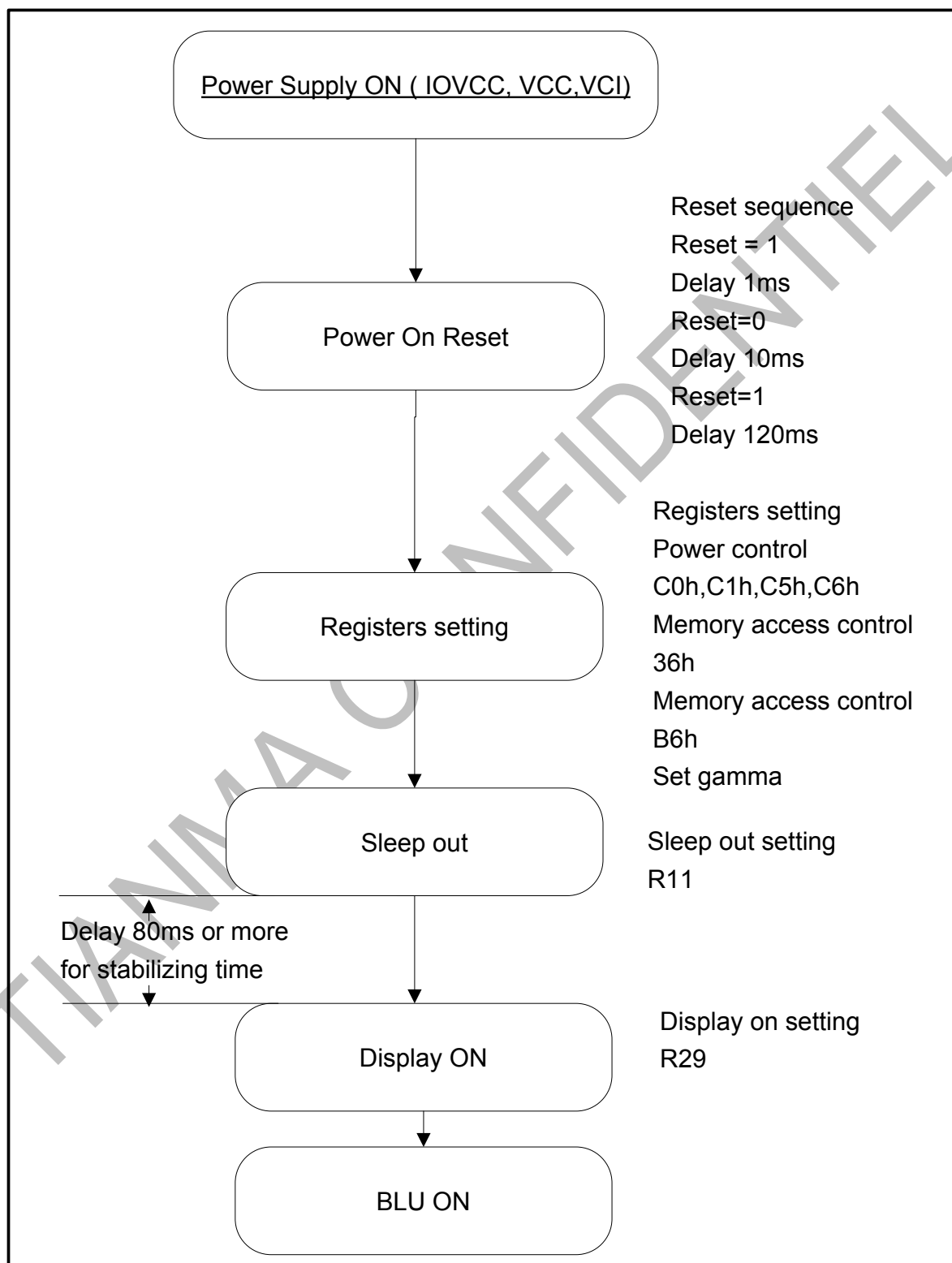
| RESET Pulse                      | Action         |
|----------------------------------|----------------|
| Shorter than 5 $\mu$ s           | Reset Rejected |
| Longer than 10 $\mu$ s           | Reset          |
| Between 5 $\mu$ s and 10 $\mu$ s | Reset Start    |

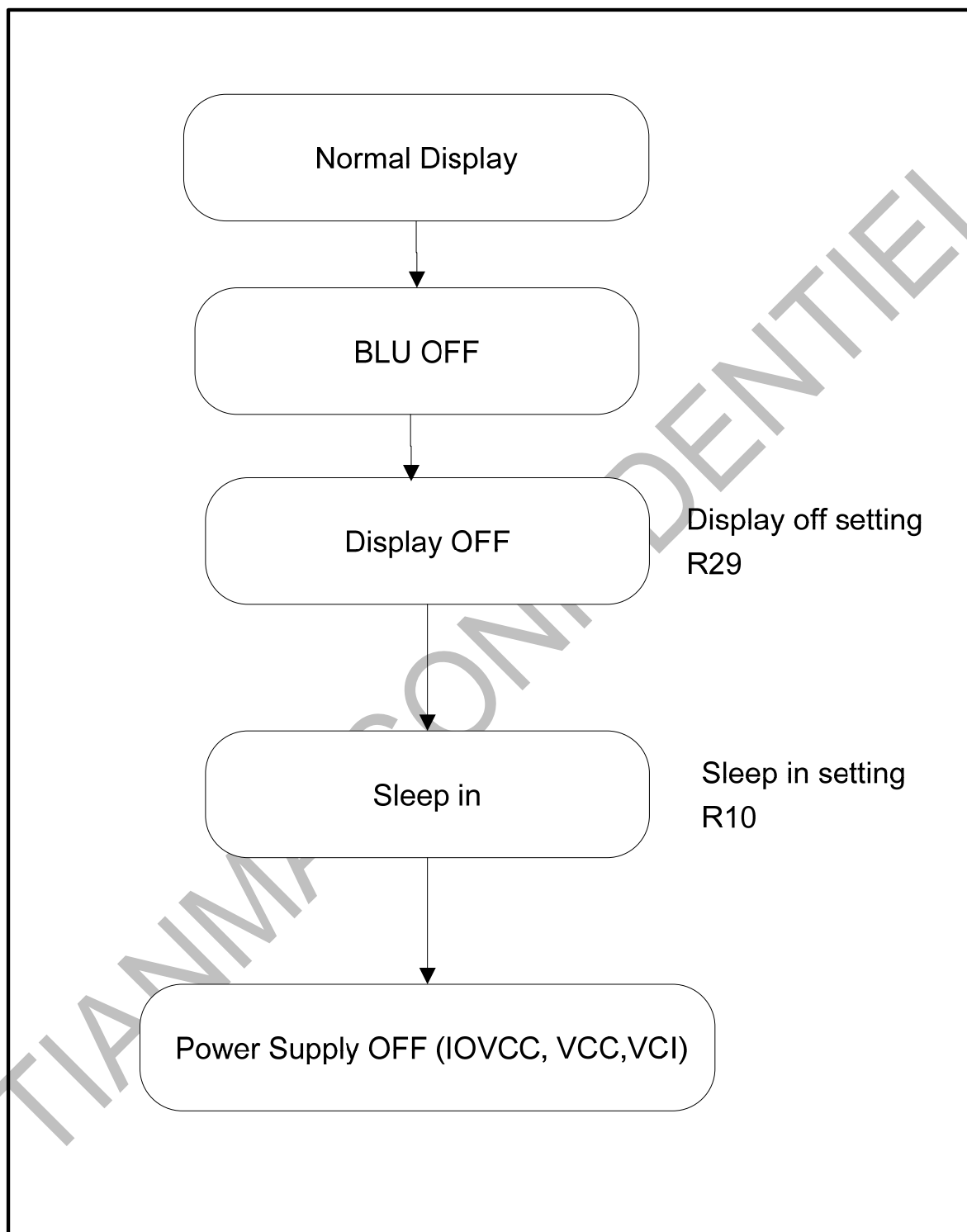




## 6 Power On/Off Sequence

### 6.1 Power on Sequence







## 7 Optical Characteristics

Ta=25°C

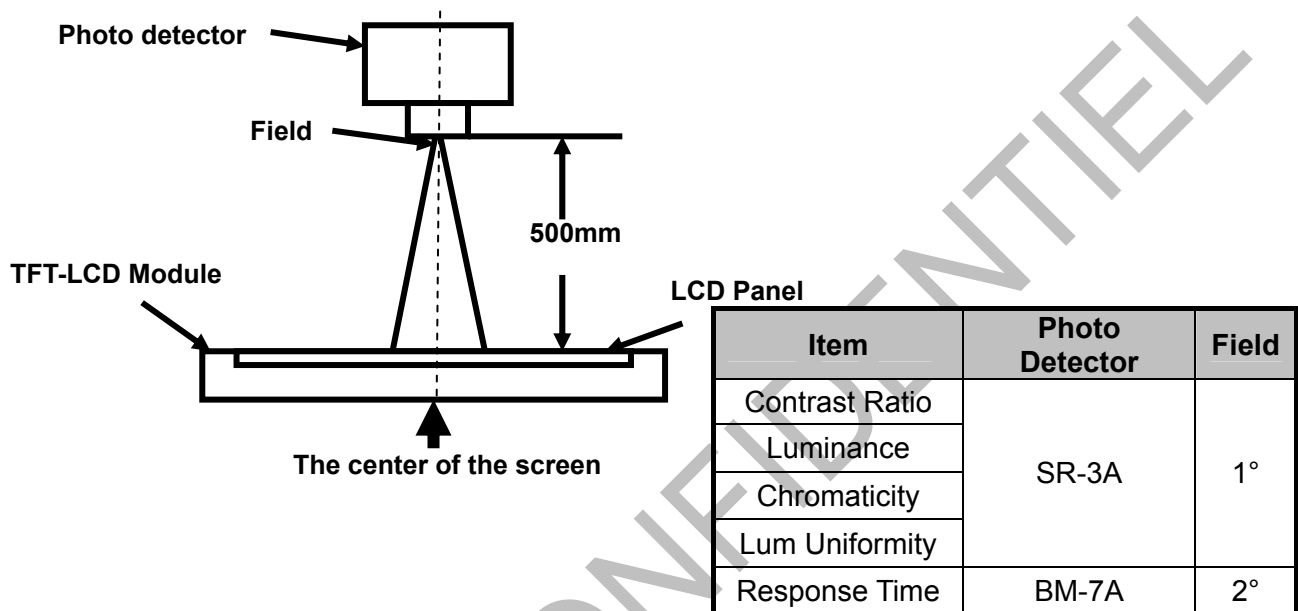
| Item           |       | Symbol | Condition       | Min   | Typ   | Max   | Unit              | Remark          |
|----------------|-------|--------|-----------------|-------|-------|-------|-------------------|-----------------|
| View Angles    |       | θT     | CR≥10           | 60    | 70    | -     | Degree            | Note 2          |
|                |       | θB     |                 | 50    | 60    | -     |                   |                 |
|                |       | θL     |                 | 60    | 70    | -     |                   |                 |
|                |       | θR     |                 | 60    | 70    | -     |                   |                 |
| Contrast Ratio |       | CR     | θ=0°            | 400   | 500   | -     |                   | Note1<br>Note3  |
| Response Time  |       | Ton    | 25℃             | -     | 20    | 30    | ms                | Note1           |
|                |       | Toff   |                 |       |       |       |                   | Note4           |
| Chromaticity   | White | x      | Backlight is on | 0.247 | 0.297 | 0.347 |                   | Note5,<br>Note1 |
|                |       | y      |                 | 0.263 | 0.313 | 0.363 |                   |                 |
|                | Red   | x      |                 | 0.536 | 0.586 | 0.636 |                   |                 |
|                |       | y      |                 | 0.292 | 0.342 | 0.392 |                   |                 |
|                | Green | x      |                 | 0.288 | 0.338 | 0.388 |                   |                 |
|                |       | y      |                 | 0.518 | 0.568 | 0.618 |                   |                 |
|                | Blue  | x      |                 | 0.098 | 0.148 | 0.198 |                   |                 |
|                |       | y      |                 | 0.032 | 0.082 | 0.132 |                   |                 |
| Uniformity     |       | U      |                 | -     | 80    | -     | %                 | Note1<br>Note6  |
| NTSC           |       |        |                 | -     | 50    | -     | %                 | Note 5          |
| Luminance      |       | L      |                 | 200   | 250   | -     | cd/m <sup>2</sup> | Note1<br>Note7  |

Test Conditions:

1.  $V_F=3.2V$ ,  $I_F=15mA$ , the ambient temperature is 25°C.
2. The test systems refer to Note 1 and Note 2.

**Note 1: Definition of optical measurement system.**

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.

**Note 2: Definition of viewing angle range and measurement system.**

viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).

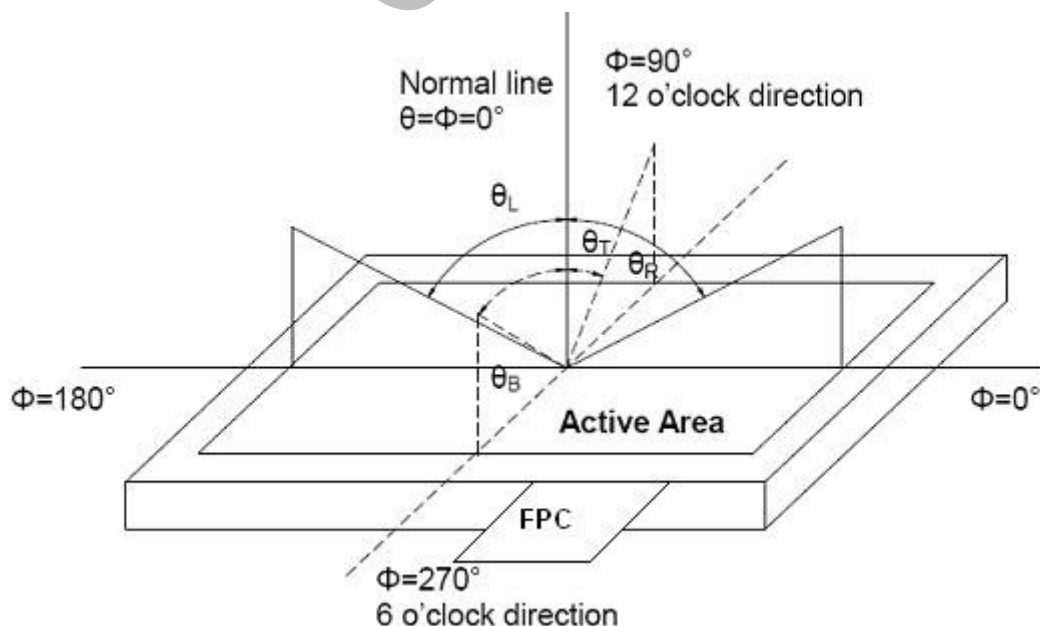


Fig. 1 Definition of viewing angle

**Note 3: Definition of contrast ratio**

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

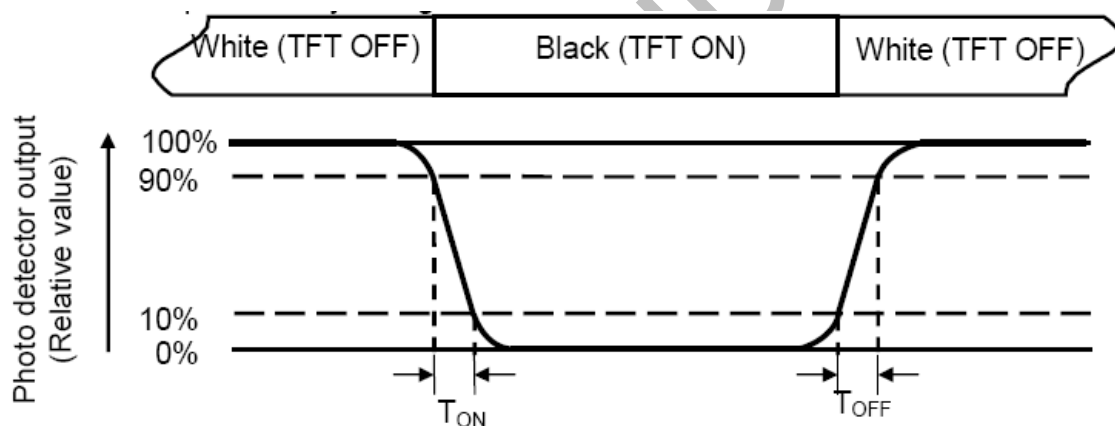
"White state": The state is that the LCD should driven by  $V_{\text{white}}$ .

"Black state": The state is that the LCD should driven by  $V_{\text{black}}$ .

$V_{\text{white}}$ : To be determined       $V_{\text{black}}$ : To be determined.

**Note 4: Definition of Response time**

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time ( $T_{\text{ON}}$ ) is the time between photo detector output intensity changed from 90% to 10%. And fall time ( $T_{\text{OFF}}$ ) is the time between photo detector output intensity changed from 10% to 90%.

**Note 5: Definition of color chromaticity (CIE1931)**

Color coordinates measured at center point of LCD.

**Note 6: Definition of Luminance Uniformity**

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity}(U) = L_{\min} / L_{\max}$$

L-----Active area length W----- Active area width

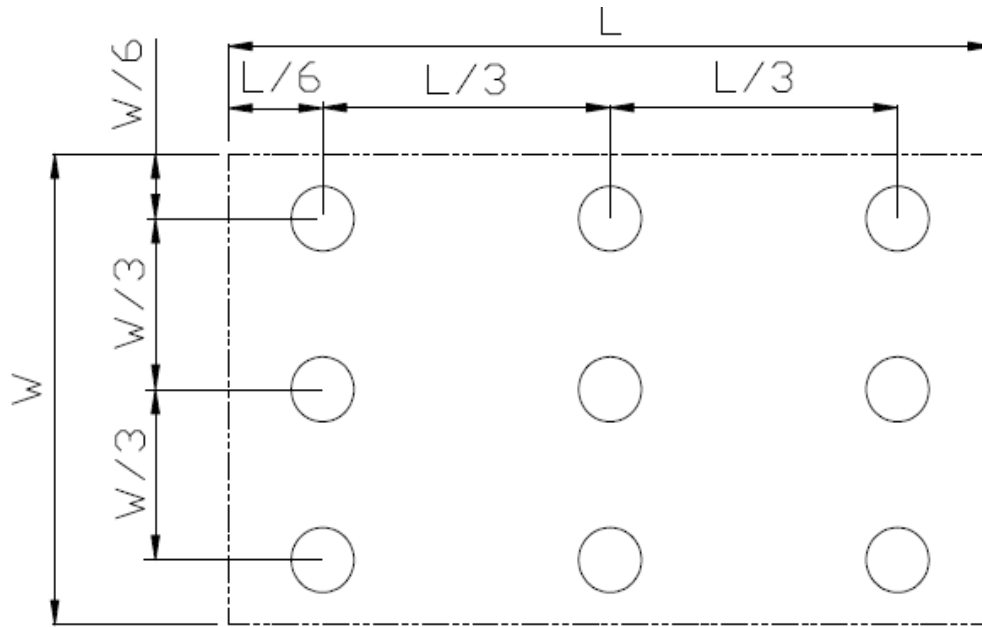


Fig. 2 Definition of uniformity

$L_{\max}$ : The measured maximum luminance of all measurement position.

$L_{\min}$ : The measured minimum luminance of all measurement position.

**Note 7: Definition of Luminance:**

Measure the luminance of white state at center point.



## 8 Environmental / Reliability Tests

| No | Test Item                                | Condition  | Remarks   |
|----|--|--|---|
| 1  | High Temperature Operation               | Ts=+70℃, 48hrs<br>Restore4H at 25℃   | Note1<br>IEC60068-2-1,GB2423.2  |
| 2  | Low Temperature Operation                | Ta=-20℃, 48hrs<br>Restore4H at 25℃   | IEC60068-2-1<br>GB2423.1  |
| 3  | High Temperature Storage                 | Ta=+80℃, 96hrs<br>Restore4H at 25℃   | IEC60068-2-1<br>GB2423.2  |
| 4  | Low Temperature Storage                  | Ta=-30℃, 96hrs<br>Restore4H at 25℃   | IEC60068-2-1<br>GB2423.1  |
| 5  | High Temperature & High Humidity Storage | +40℃, 90% RH<br>48 hours   | Note2<br>IEC60068-2-78<br>GB/T2423.3  |
| 6  | Thermal Shock (Non-operation)            | -30℃ 30 min~+80℃ 30 min,<br>Change time:5min,<br>After 10 cycle, Restore4H at 25℃  | Start with cold temperature,<br>End with high temperature,<br>IEC60068-2-14,GB2423.22 |
| 7  | Vibration (Non-operation)                | Frequency range:10~55Hz,<br>Stroke:1.5mm<br>Sweep:10Hz~55Hz~10Hz<br>1 hours<br>for each direction of X.Y.Z.(3 hours for total) | IEC61000-4-2<br>GB/T17626.2   |
| 8  | Package Drop Test                        | Height:80 cm, 1 corner, 3 edges,<br>6 surfaces   | IEC60068-2-6<br>GB/T2423.10   |

Note1: Ts is the temperature of panel's surface.

Note2: Ta is the ambient temperature of sample.

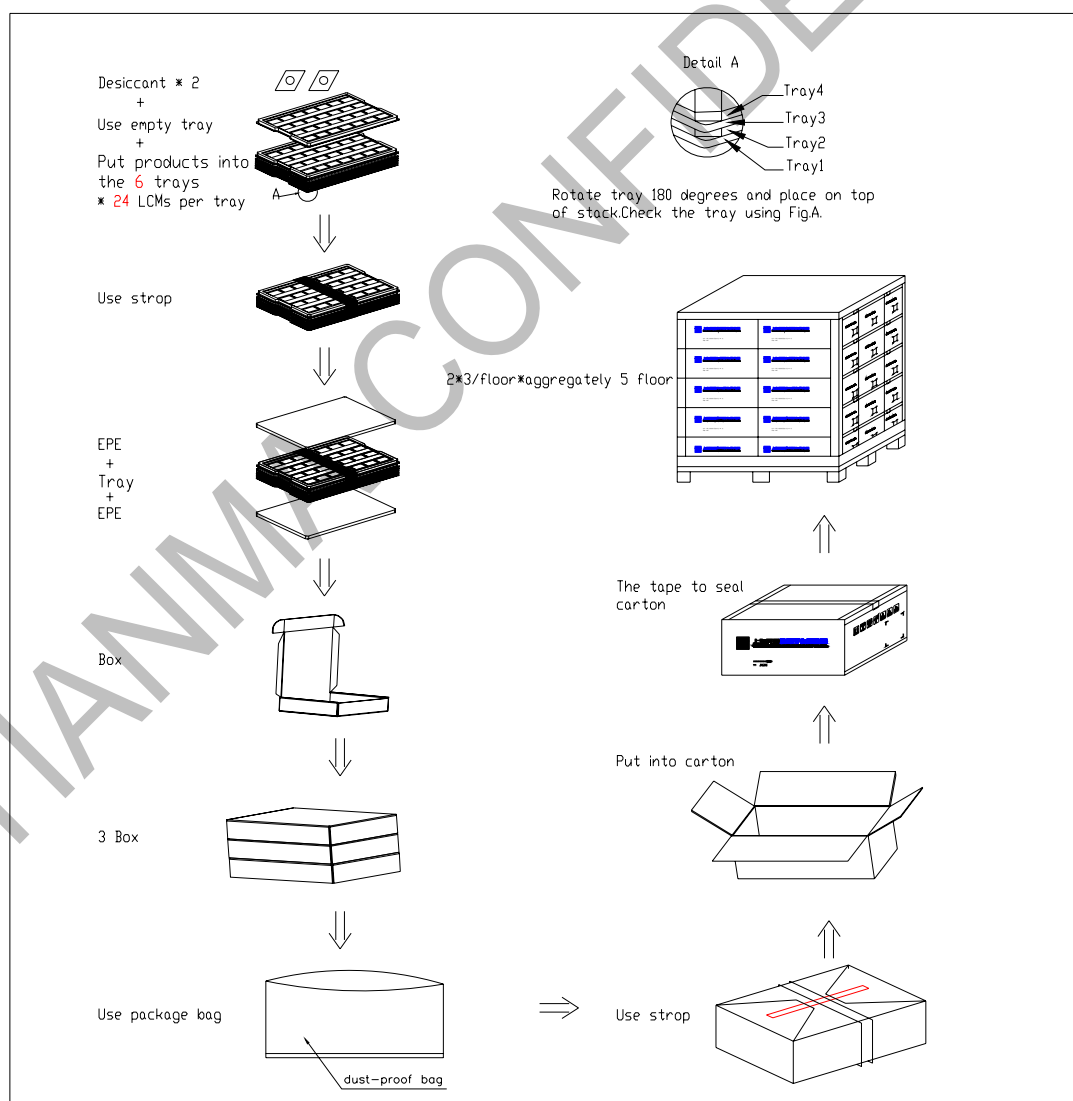






## 10 Packing Drawing

| No | Item             | Model(Material)  | Dimensions(mm ) | Unit Weight(Kg) | Quantity | Remark      |
|----|------------------|------------------|-----------------|-----------------|----------|-------------|
| 1  | LCM module       | TM023KDH19-00    | 51×45.8×2.25    | 0.00844         | 576      |             |
| 2  | Tray             | PET ( Transmit ) | 315×247×10.8    | 0.086           | 54       | Anti-static |
| 3  | EPE              | EPE              | 315×247×5       | 0.009           | 12       |             |
| 4  | Anti-Static Bag  | PE               | 700×540         | 0.021           | 1        |             |
| 5  | BOX              | Corrugated Paper | 345×260×70      | 0.227           | 6        |             |
| 6  | Desiccant        | Desiccant        | 45×50           | 0.0035          | 12       |             |
| 7  | Carton           | Corrugated Paper | 544×365×250     | 1.01            | 1        |             |
| 8  | Total Weight(Kg) | 12.048           |                 |                 |          |             |





## 11 Precautions for Use of LCD Modules

### 11.1 Handling Precautions

- 11.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 11.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 11.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 11.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 11.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
  - Isopropyl alcohol、
  - Ethyl alcoholSolvents other than those mentioned above may damage the polarizer. Especially, do not use the following:
  - Water
  - Ketone
  - Aromatic solvents
- 11.1.6 Do not attempt to disassemble the LCD Module.
- 11.1.7 If the logic circuit power is off, do not apply the input signals.
- 11.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - 11.1.8.1 Be sure to ground the body when handling the LCD Modules.
  - 11.1.8.2 Tools required for assembly, such as soldering irons, must be properly ground.
  - 11.1.8.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
  - 11.1.8.4 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

### 11.2 Storage precautions

- 11.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 11.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:  
Temperature : 0℃ ~ 40℃    Relatively humidity: ≤80%
- 11.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

### 11.3 Transportation Precautions:

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.