ISSUED DAT	E: <u>20</u>	09-9-2			
VERSION	: <u>Ve</u>	r 1.0			
□Final	ninary Sp Product				
Customer :			Notes		
Approved by	Approved by Notes				
SHANGHAI TIANMA Confirm	ed:				
Prepared by	Check	ed by	Approved by		

MODEL NO. : TM032LBH02

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This technical specification is subjected to change without notice



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Record of Revision

Rev	Issued Date	Description	Editor
1.0	2009-9-2	Preliminary Specification Release	Jianchuan_wang



1 General Specifications

	Feature	Spec		
	Size	3.2 inch		
	Resolution	240(RGB) x 400		
	Interface	CPU 18 bits/16 bits/8 bits		
	Color Depth	262K/65K		
	Technology Type	a-Si		
Display Spec.	Pixel Pitch (mm)	0.174X0.174		
	Pixel Configuration	R.G.B. Vertical Stripe		
	Display Mode	TM with Normally White		
Resolution 240(RGB Interface CPU 18 by Color Depth 262K/65K Technology Type a-Si Pixel Pitch (mm) 0.174X0.7 Pixel Configuration R.G.B. Verical Display Mode TM with Note Surface Treatment(Up Polarizer) Clear Type Viewing Direction 6 o'clock Gray Scale Inversion Direction 12 o'clock Gray Scale Inversion Direction 41.76x69 With /Without TSP Weight (g) TBD LED Numbers 4 LEDs	Surface Treatment(Up Polarizer)	Clear Type(3H)		
	Viewing Direction	6 o'clock		
	12 o'clock			
	LCM (W x H x D) (mm)	80.90x47.60x3.90		
Manhaniaal	Active Area(mm)	41.76x69.60		
	With /Without TSP	With TSP		
Gilaraotoriotico	Weight (g)	TBD		
	LED Numbers	4 LEDs		
Electronic	Driver IC	HX8352-A		

Note 1: Viewing direction for best image quality is different from TFT definition, there is a 180 degree shift.

Note 2: Requirements on Environmental Protection: Q/S0002

Note 3: LCM weight tolerance: +/- 5%



2 Input/Output Terminals

2.1 TFT LCD Panel

No	SYMBOL	I/O	Description	Remark
1	GND	Р	Ground	
2	YD	Р	Touch Panel Bottom Side	
3	XR	Р	Touch Panel Right Side	
4	YU	Р	Touch Panel Up Sid	
5	XL	Р	Touch Panel Left Side	
6	GND	Р	Ground	
7	BS1	I	Interface selection	Note 2
8	BS0	I	Interface selection	Note 2
9	FMARK/NC	0	Tearing effect output. If not used, please open this pin	
10	PWM_OUT/N C	0	PWM control signal output	
11	LCD_ID	0	ID pin,2.783V(under typical input voltage 2.8V)	
12	RESET	ı	Reset signal; Must be reset after power is supplied	
13	D17	-	Data input	
14	D16	I	Data input	
15	D15	I	Data input	
16	D14	I	Data input	
17	D13	ı	Data input	
18	D12	ı	Data input	
19	D11	I	Data input	
20	D10		Data input	
21	D9		Data input	
22	D8		Data input	
23	D7	I	Data input	
24	D6	I	Data input	
25	D5	I	Data input	
26	D4	I	Data input	
27	D3	I	Data input	
28	D2	I	Data input	
29	D1	I	Data input	
30	D0	I	Data input	
31	RD	I	Read signal	
32	WR	I	Write signal	
33	RS	_	Command or parameter select signal; Low: command; High: parameter	
34	CS	I	Chip select signal, low: chip can be accessed; Must be connected to GND if not used	
35	GND	Р	Ground	
36	IOVCC	Р	Digital I/O power supply	
37	VCC	Р	Digital power supply	



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38	NC		No connection	
39	LEDK4	Р	Back light cathode LEDK4	
40	LEDK3	Р	Back light cathode LEDK3	
41	LEDK2	Р	Back light cathode LEDK2	
42	LEDK1	Р	Back light cathode LEDK1	
43	LEDA	Р	Back light cathode LEDA	
44	GND	Р	Ground	

Note 1: I/O definition:

I---Input O---Output P---Power(Ground) NC---No connection

Note 2: Interface selection:

BS1	BS0	Interface Mode	DB Pins
0	0	16-bit bus interface,80-system, 65K-color	D15-D0: Data ; D17-D16: Unused
0	1	16-bit bus interface,80-system, 262K-color	D15-D0: Data ; D17-D16: Unused
1	0	18-bit bus interface,80-system, 262K-color	D17-D0: Data
1	1	8-bit bus interface,80-system, 262K-color	D7-D0: Data ; D17-D8: Unused



3 Absolute Maximum Ratings

3.1 Driving TFT LCD Panel

 $Ta = 25^{\circ}C$

Item	Symbol	Min.	Max.	Unit	Remark
Supply Voltage	VCC	-0.3	4.6	٧	
Supply Voltage	IOVCC	-0.3	4.6	٧	
Input Signal Voltage	D17~D0, CS, RD, WR, RS, RESET, BS1,BS0	-0.3	VCC +0.3	V	
Touch Panel Pin Voltage	YD,XR,YU,XL		7	V	
Back Light Forward Current	I _{LED}		25	mA	For each LED
Operating Temperature	T_{OPR}	-20	60	ပ	
Storage Temperature	T_{STG}	-30	70	${\mathbb C}$	

4 Electrical Characteristics

4.1 Driving TFT LCD Panel

GND=0V, Ta=25℃

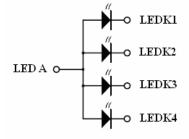
Iter	n	Symbol	Min.	Тур.	Max.	Unit	Remark
Supply \	/oltage	VCC	2.5	2.8	3.3	V	
Supply v	oltage	IOVCC	1.65	2.8	3.3	٧	
Input Signal	Low Level	VIL	-0.3		0.2xIOVCC	V	D17~D0, CS, RD, WR,
Voltage	High Level	VIH	0.8xIOVCC		IOVCC	V	RS, RESET, BS1,BS0
Output Signal	Low Level	V_{OL}	-0.3	I	0.3xIOVCC	٧	FMARK/NC,
Voltage	High Level	V _{OH}	0.7xIOVCC		IOVCC	V	PWM_OUT/NC
		Black Mode (60Hz)		TBD		mA	
(Panel+ LSI) Power Consumption		8 color Mode	1	TBD	-	mA	
		Standby Mode		TBD		uA	

4.2 **Driving Backlight**

Ta=25°C

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Forward Current	I _F		20	25	mA	
Forward Voltage	V_{F}		3.2		V	For one LED
Power Consumption	W_{BL}		256		mW	

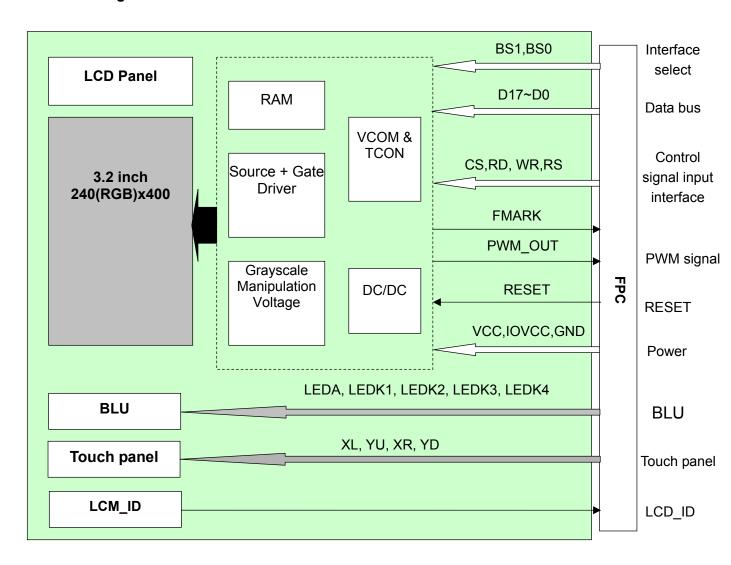
Note1: Figure below shows the connection of backlight LED.



Note 2: One LED : I_F =20mA, V_F =3.2V Note 3: The life of LED : 20,000 hours

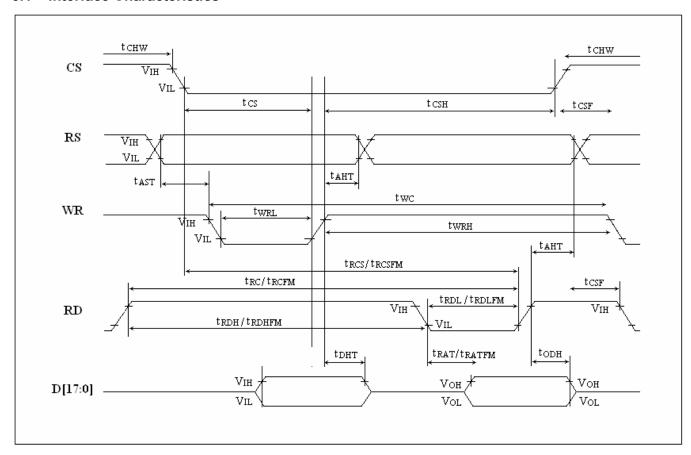


4.3 Block Diagram



5 Timing Chart

5.1 Interface Characteristics



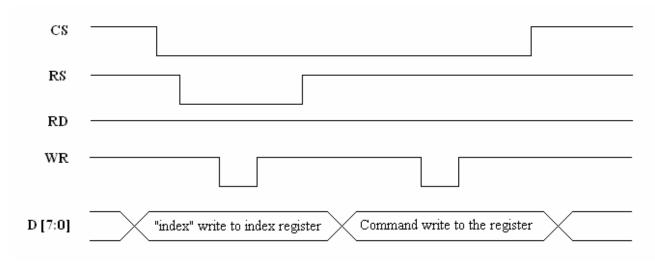
5.2 Interface Timing Parameters

Normal Write Mode

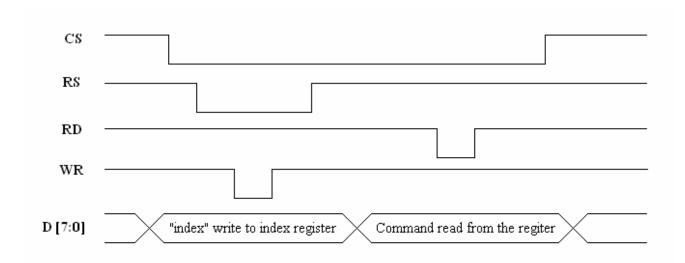
Cianal	Cumbal	Devemeter		Spec.		Description
Signal	Symbol	Parameter	Min.	Max.	Unit	Description
RS	t_{AST}	Address setup time	10		ns	
110	t _{AHT}	Address hold time(Write/Read)	10	_	113	_
	t_CHW	Chip select "H" pulse width	0			
	t_{CS}	Chip select setup time (Write)	35			
cs	t_{RCS}	Chip select setup time (Read ID)	100		ns	
03	t _{RCSFM}	Chip select setup time (Read FM)	100	_	113	_
	t_{CSF}	Chip select wait time(Write/Read)	10			
	t _{CSH}	Chip select hold time	10			
	t_WC	Write cycle	100			
WR	t_WRH	Control pulse "H" duration	20	-	ns	-
	t_{WRL}	Control pulse "L" duration	20			
	t_RC	Read cycle (ID)	150			
RD	t_{RDH}	Control pulse "H" duration (ID)	40	-	ns	When read ID data
	t_{RDL}	Control pulse "L" duration (ID)	50			
	t_{RCFM}	Read cycle (FM)	250			When read from
RD	t_{RDHFM}	Control pulse "H" duration (FM)	50	-	ns	frame memory
	t _{RDLFM}	Control pulse "L" duration (FM)	150			maine memory
	t_{DST}	Data setup time	20	-		For maximum
	t_DHT	Data hold time	20	-		C _L =30pF
D[17:0]	t_RAT	Read access time (ID)	-	70	ns	For minimum
	t_{RATFM}	Read access time (FM)	-	100		C _L =8pF
	t_ODH	Output disable time	20	80		OL-ohi

5.3 Interface Register Write/Read Timing

5.3.1 System Bus Interface Register Write Timing

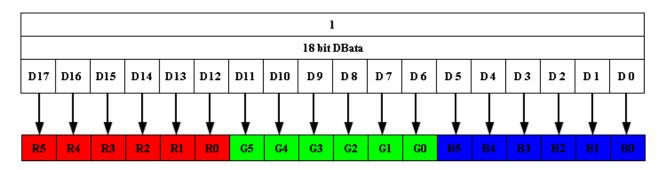


5.3.2 System Bus Interface Register Read Timing

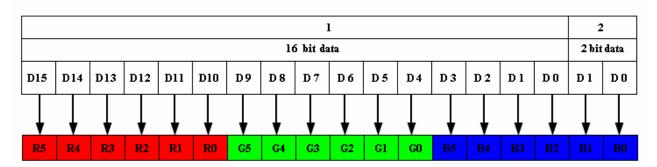


5.4 GRAM Write/Read Data Format

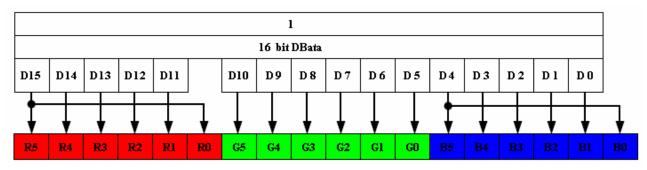
5.4.1 18-bit Read/Write GRAM Data Format(262K)



5.4.2 16-bit Read/Write GRAM Data Format(262K/65K)



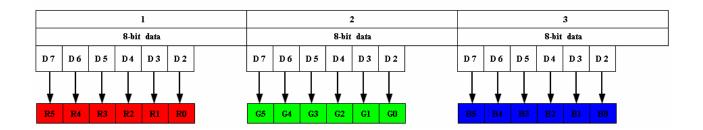
16-bit Data Bus GRAM Write/Read Data Format (16+2bit, 262k)



16-bit Data Bus GRAM Write/Read Data Format (16bit, 65k)

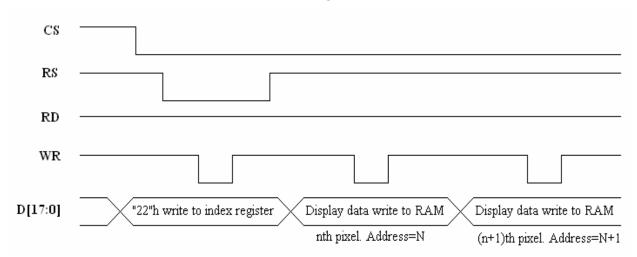


5.4.3 8-bit Read/Write GRAM Data Format(262K)

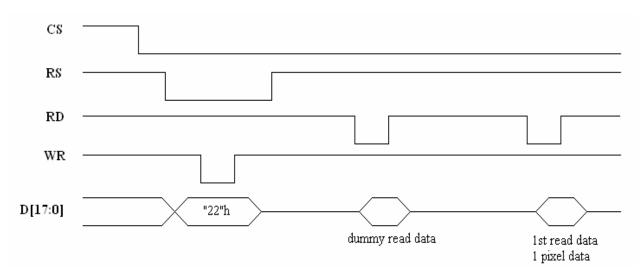


5.5 Data Bus GRAM Write/Read Timing

5.5.1 18-bit Data Bus GRAM Write/Read Timing(262k)

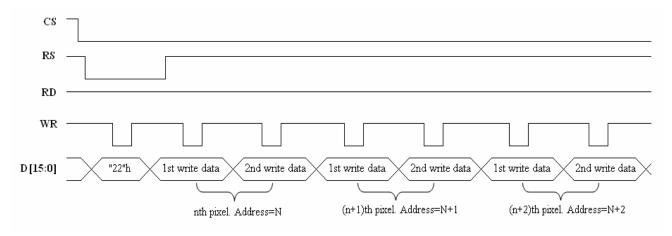


18-bit Data Bus GRAM Write Timing(262k)

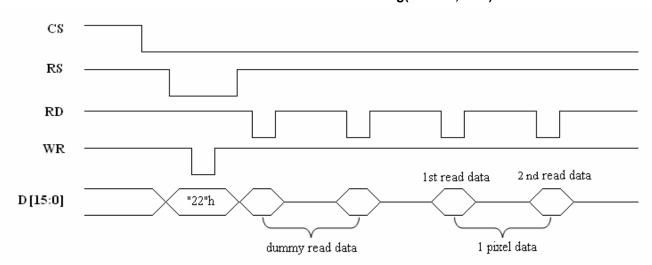


18-bit Data Bus GRAM Read Timing(262k)

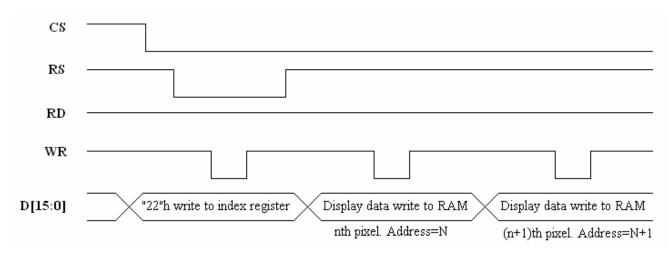
5.5.2 16-bit Data Bus GRAM Write/Read Timing(262k/65k)



16-bit Data Bus GRAM Write Timing(16+2bit,262k)

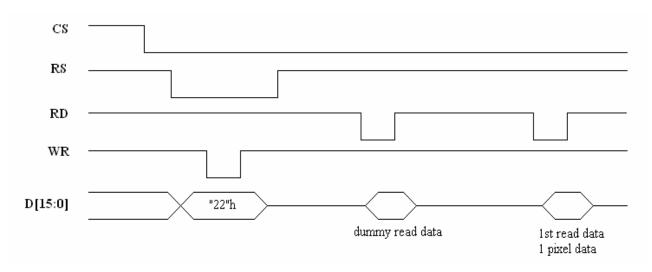


16-bit Data Bus GRAM Read Timing(16+2bit,262k)



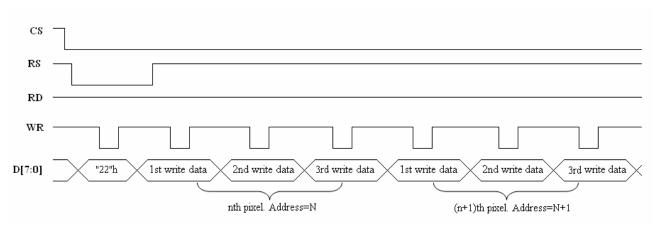
16-bit Data Bus GRAM Write Timing(16bit,65k)



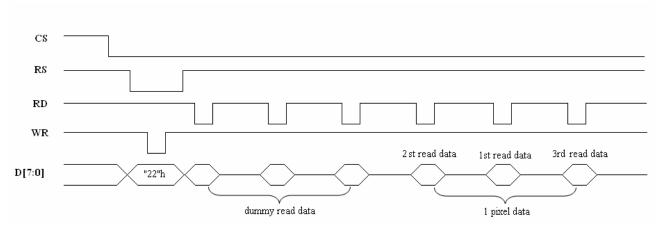


16-bit Data Bus GRAM Read Timing(16bit,65k)

5.5.3 8-bit Data Bus GRAM Write/Read Timing(262k)



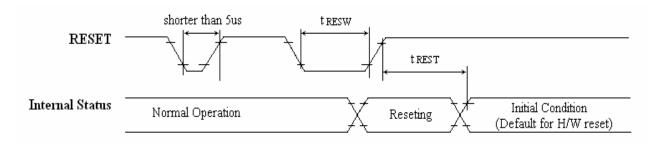
8-bit Data Bus GRAM Write Timing(6+6+6bit,262k)



8-bit Data Bus GRAM Read Timing(6+6+6bit,262k)



5.6 Reset Timing Characteristics



Reset Input Timing

Reset Timing Parameters

Symbol	Parameter	Related Spec.			Note	Unit	
Syllibol	raidilletei	Pins	Min.	Тур.	Max.	Note	Offic
t _{RESW}	Reset low pulse width	RESET	10	-	-	-	us
+	Reset complete	-	-	-	5	When reset applied during "Sleep In mode"	ms
t _{REST}	time	-		-	120	When reset applied during "Sleep Out mode"	ms

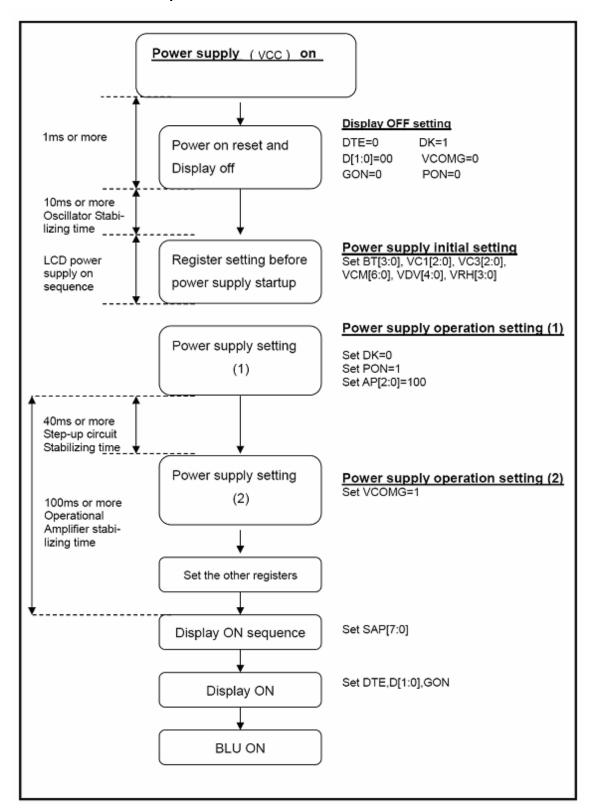
Note 1:

Action
Shorter than 5µs
Reset
Reset Start

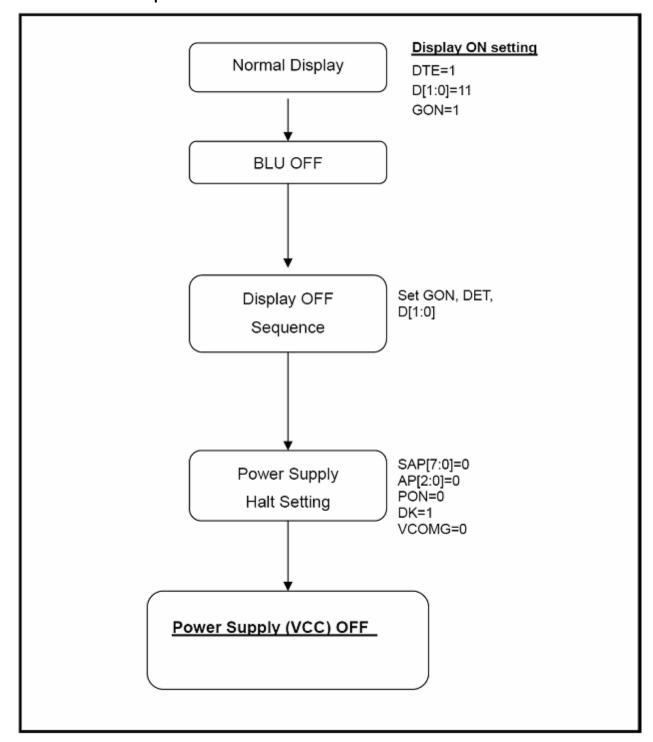


5.7 Power On/Off Sequence

5.7.1 Power on Sequence



5.7.2 Power off Sequence



6 Optical Characteristics

6.1 Optical Specification

Ta=25°C

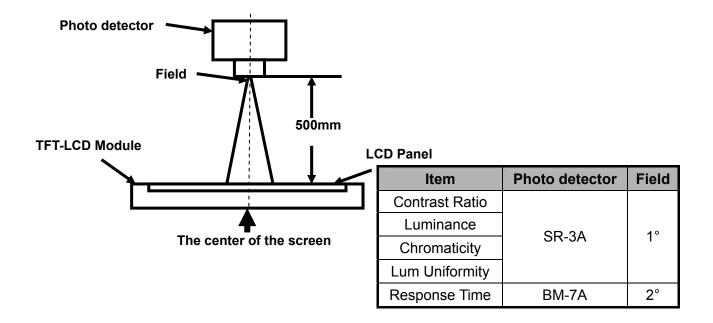
Item	1	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark	
View Angles		θТ	- CR≧10	60	70	-	Degree	Note 2	
		θВ		45	55	-			
		θL		60	70	-			
		θR		60	70	-			
Contrast Ratio		CR	θ=0°	400	500	-		Note1 Note3	
				-					Note1
Response Time		T _{OFF}	25 ℃		25	30	ms	Note4	
	White	х	Backlight is on	0.250	0.300	0.350			
		у		0.270	0.320	0.370			
	Red	х		0.555	0.605	0.655			
Chromaticity		у		0.285	0.335	0.385		Note5, Note1	
Chilomaticity	Green	х		0.286	0.336	0.386			
		у		0.566	0.616	0.666			
	Blue	х		0.102	0.152	0.202			
		у		0.027	0.077	0.127			
Uniformity		U		-	80	-	%	Note1 Note6	
NTSC				55	60	-	%	Note 5	
Luminance(With TSP)		L		200	250	-	cd/m ²	Note1 Note7	

Test Conditions:

- 1. $V_F = 3.2V$, $I_{BL} = 80 \text{mA}$ (Backlight current), the ambient temperature is 25°C.
- 2. The test systems refer to Note 1 and Note 2.

Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).

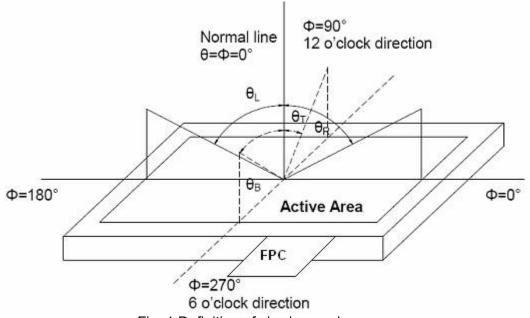


Fig. 1 Definition of viewing angle

Note 3: Definition of contrast ratio

Contrast ratio (CR) = Luminance measured when LCD is on the "White" state

Luminance measured when LCD is on the "Black" state

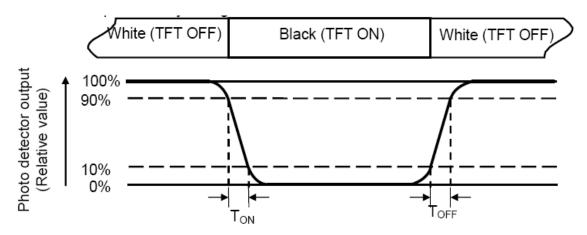
"White state ": The state is that the LCD should be driven by Vwhite.

"Black state": The state is that the LCD should be driven by Vblack.

Vwhite: To be determined Vblack: To be determined.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity(U) = Lmin/Lmax

L----- Active area length W----- Active area width

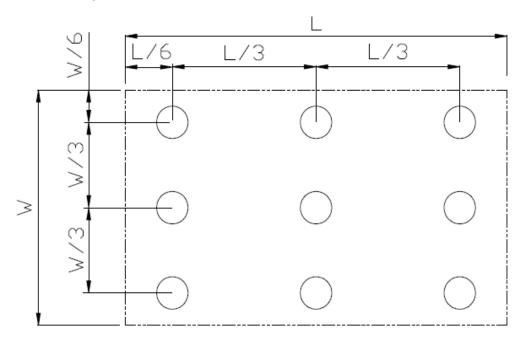


Fig. 2 Definition of uniformity

Lmax: The measured maximum luminance of all measurement position.

Lmin: The measured minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.

7 Environmental / Reliability Tests

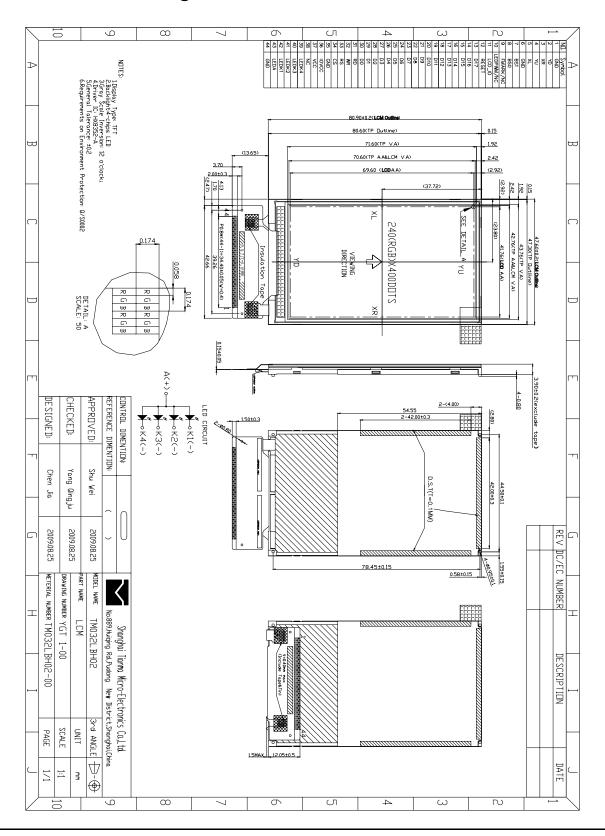
No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts=+60℃, 120hrs	Note1 IEC60068-2-1:2007,GB2423.2-2008
2	Low Temperature Operation	Ta=-20℃, 120hrs	IEC60068-2-1:2007 GB2423.1-2008
3	High Temperature Storage	Ta=+70℃, 120hrs	IEC60068-2-1:2007 GB2423.2-2008
4	Low Temperature Storage	Ta=-30℃, 120hrs	IEC60068-2-1:2007 GB2423.1-2008
5	High Temperature & High Humidity Storage	Ta=+60℃, 90% RH 120 hours	Note2 IEC60068-2-78 :2001 GB/T2423.3—2006
6	Thermal Shock (Non-operation)	-30℃ 30 min~+70℃ 30 min, Change time:5min, 20 Cycles	Start with cold temperature, End with high temperature, IEC60068-2-14:1984,GB2423.22-2002
7	Electro Static Discharge (Operation)	C=150pF, R=330 Ω , 5points/panel Air:±8KV, 5times; Contact:±4KV, 5 times; (Environment: 15 $^{\circ}$ C $^{\circ}$ 30% $^{\circ}$ 60%, 86Kpa $^{\circ}$ 106Kpa)	IEC61000-4-2:2001 GB/T17626.2-2006
8	Vibration (Non-operation)	Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z. (6 hours for total)(Package condition)	IEC60068-2-6:1982 GB/T2423.10—1995
9	Shock (Non-operation)	60G 6ms, ±X,±Y,±Z 3times, for each direction	IEC60068-2-27:1987 GB/T2423.5—1995
10	Package Drop Test	Height:80 cm, 1 corner, 3 edges, 6 surfaces	IEC60068-2-32:1990 GB/T2423.8—1995

Note1: Ts is the temperature of panel's surface.

Note2: Ta is the ambient temperature of sample.



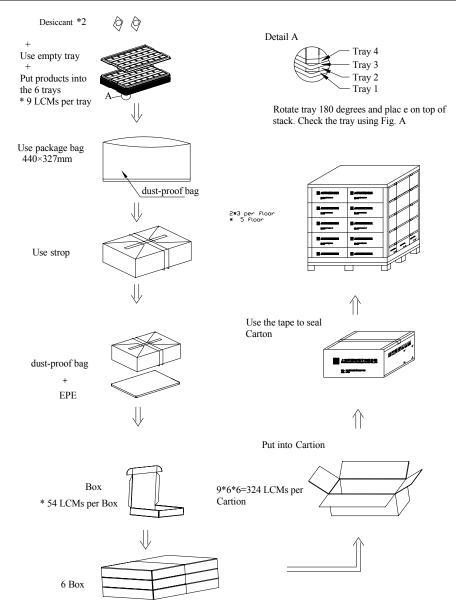
8 Mechanical Drawing





9 Packing Drawing

No	Item	Model (Material)	Dimensions(mm)	Unit Weight(Kg)	Quantity	Remark	
1	LCM module	TM032LBH02	47.60x80.90x3.90	0.0262	324		
2	Tray	PET(Transmit)	315x247x11.8	TBD	42	Anti-static	
3	EPE	EPE	315x247x5	0.009	6		
4	Desiccant	Desiccant	45x50	0.0035	12		
5	Anti-static bag	PE	327x440	0.021	6		
6	BOX	Corrugated paper	345x260x70	0.227	6		
7	Carton	Corrugated paper	544x365x250	1.01	1		
8	Total Weight(Kg)	TBD					



10 Precautions for Use of LCD Modules

- 10.1 Handling Precautions
- 10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 10.1.6 Do not attempt to disassemble the LCD Module.
- 10.1.7 If the logic circuit power is off, do not apply the input signals.
- 10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- 10.1.8.1 Be sure to ground the body when handling the LCD Modules.
- 10.1.8.2 Tools required for assembly, such as soldering irons, must be properly ground.
- 10.1.8.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
- 10.1.8.4 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.
- 10.2 Storage precautions
- 10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0°C ~ 40°C Relatively humidity: ≤80%

- 10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 10.3 Transportation Precautions:

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.