SPECIFICATION

Model : TFT LCD MODULE LTA070B410A-351

Customer:

AV- USE Marketing&Engineering Dept.				ISSUE	
Customer	ESIT	Design	Production	Sales	DATE
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Revision History

Document Title

Date	Sheet (New)	Item	Old	New	Reason
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Caution and Handling Precaution

For your end user's safety, it is strongly advised that the items with "*" should be included in the instruction manual of the system which may be issued by your organization.

For Safety



Warning

(1) This Standard LCD modules have not been customized for operation in extreme environments or for use in applications where performance failures could be life-threatening or otherwise catastrophic.

Since they must never be installed in aircraft navigation control systems (such as, but not limited to Traffic Collision System and Air Traffic Indicator), in military defense or weapons systems, in critical industrial process-control systems (e.g., those involved in the production of nuclear energy), or in critical medical device or patient life-support systems.



- (1) DO NOT DISASSEMBLE OR MODIFY the module.
 - Sensitive parts inside LCD module may be damaged, and dusts or scratches may mar the displays. SVIT does not warrant the module, if customer disassembled or modified them.
- *(2) DO NOT INGEST liquid crystal material, DO NOT INHALE this material, and DO NOT PERMIT this material to contact the skin, if glass of LCD panel is broken and liquid crystal material spills out. If liquid crystal material contacts the skin, mouth or clothing, take the following actions immediately. In case contact to the eye or mouth, rise with large amount of running water for more than 15 minutes. In case contact to the skin or clothing, wipe it off immediately and wash with soap and large amount of running water for more than 15 minutes. The skin or closing may be damaged if liquid crystal material left adhered.
 - Incase ingestion, rise out the mouth well with water. After spewing up by drinking large amount of water, get medical.
- *(3) BE CAREFUL WITH CHIPS OF GLASS that may cause injuring fingers or skin, when the glass is broken. Since FL is also made of glass, when FL is built in, handle with due cationsas well.
- (4) DO NOT EXCEED the absolute maximum rating values under the worst probable conditions caused by the supply voltage variation, input voltage variation, variation in parts constants, ambient-temperature, etc., otherwise LCD module may be damaged.
- (5) Suitable protection circuit should be applied for each system design.
 DO NOT MODIFY the fuse used in the module. It may cause overheat and/or burning if dusts or metal particles are on the PCBs in the LCD module.

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(6) Be sure that power supply output from the system should be limited to smaller values than listed shown below.

It is because this LCD module explained in this specification has a current limiter, with such function at power input lines. But it may be some possibility of overheat and/or burning of LCD module and its peripheral devices before current limiter of the module when open-short test of the module is performed by using power supply higher than following recommended value.

Power supply	Recommended maximum output current of power supply
Voo1	≤0.5A
Vooz	≤1.0A
Vgн	≤0.5A
VGL	≤0.5A

(7) Always comply all applicable environmental regulations, when disposing of LCD module.

For Designing the System

DESIGNING ENCLOSURE

(1) When including a module in a set, Please fix the outside of the active area of array glass or a polarizing plate on a double-sided tape etc., and confirm the intensity of set. Be careful so that direct pressurization does not start a cell.

DESIGNING POWER SUPPLIES AND INPUT SIGNALS TO LCD MODULE

(2) Power supply lines should be designed as follows.

Power supplies should always be turned on before the input signals are supplied to LCD module, and the input signals should be disconnected before power supplies are turned off.

DESIGNING FOR BETTER VISIBILITY

(3) In case of severe environmental condition like outdoor usage, a proper transparent protective cover over LCD module is recommended to apply in order to prevent scratches, and invasion of dusts, water, etc., from the system's window onto LCD module.

Ultra-violet ray cut filter is recommended to apply onto LCD module for outdoor operation. Strong ultra-violet ray may cause damage the panel. However, in that case, tranmittance-luminance will decrease. Careful selection of material is required.

For Installation in Assembly

ESD(ELECTRO-STATIC DISCHARGE)PREVENTION

(1) The C-MOS LSIs used in LCD module are very sensitive to ESD (electro-static Discharge). Ambient humidity of working area is recommended to be higher than 50%RH.

Person handling LCD modules should be grounded with wrist band. Tools like soldering iron and screw driver, and working benches should be grounded.

The grounding should be done through a resister of 0.5-1M ohms in order to prevent spark of ESD.

(2) When remove protection film from LCD panel, peel off the film slowly (more than three seconds) from the edge of the panel, using a soft-pointed tweezers covered by teflon or adherent tape.

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DUST AND STAIN PREVENTION

- (3) Reduce dust level in working area. Especially the level of metal particle should be decreased. Use finger stalls or soft and dust-free gloves in order to keep clean appearance of LCD module when handled for incoming inspection and assembly.
- *(4) When LCD panel becomes dirty, wipe off the panel surface softly with absorbent cotton or another soft cloth.

If necessary, breathe upon the panel surface and then wipe off immediately and softly again. If the dirt can not be wiped off, follow the instructions described in individual specification. Be careful not to spill organic solvents into the inside of LCD module. The solvents may damage driver IC and PCB area used inside module. The polarizer laminated to LCD panel and adhesives amy be damaged by the solvents, so do not use any organic solvents for wiping off LCD panel.

*(5) AVOID THE DEWING OF WATER CONDENSATION.

Wipe off a spot or spots of water of mist on LCD panel softly with absorbent cotton or another cloth as soon as possible if happened, otherwise discoloration or stain may be caused. If water invade into LCD module, it may cause LCD module damages.

BENDING / TWISTING OF LCD MODULE DURING ASSEMBLY

*(6) DO NOT APPLY MECHANICAL FORCES.

Do not bend or twist LCD module even momentary when LCD module is installed an enclosure of the system. Bending or twisting LCD module may cause its damages. Make sure to design the enclosure that bending/twisting forces are not applied to LCD module when it is installed in the system. Refrain from strong mechanical shock like dropping from the working bench or knocking against hard

object. These may cause the panel crack, damage of FL or other mis-operation.

MECHANICAL FORCES

- *(7) Refrain from excessive force like pushing the surface of LCD panel. This may cause scratches or breakage of the panel, or a failure od the module.
- *(8) Do not put heavy object such as tools, books, etc., and do not pile up LCD modules.
 Be careful not to touch surface of the polarizer laminated to the panel with any hard and sharp object.
 The polarizer is so soft that it can be easily scratched, even the protect film covers it.
- (9) When inserting or disconnecting the connectors to LCD module, be sure not to apply force against PCB, not connecting cables, otherwise internal connection of PCB may be damages.

OPERATION

(10) Power supplies should always be turned off in assembling process.

Do not connect or disconnect the power cables and connectors with power applied to LCD module. This may cause damage of module circuit.

The signal should be applied after power supplies are turned on. And the signal should be removed before power supplies are turned off.

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For Transportation and Storage

- (1) Do not store LCD module in high temperature, especially in high humidity for a long time approximately more than one month).
 - It is recommended to store LCD module where the temperature is in the range of 0 to 35 degrees Celsius and the relative humidity is lower than 70%.
- (2) Low temperature
 - Liquis crystal material may be coagulates and LCD panel may be damaged at the lower temperature than storage teperaturerange described in individual specification.
- (3) Store LCD module without exposure to direct sunlight or fluorescent lamps in order to prevent the module from strong ultra violet ray.
- *(4) Avoid condensation of water on LCD module, otherwise it may cause mis-operation or defects. Keep away LCD module from such ambient.
- (5) In case of transportation of storage after opening the original packing. LCD module are recommended to be repacked into the original packaging with the same method, especially with same kind of desiccant.

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1. Scope

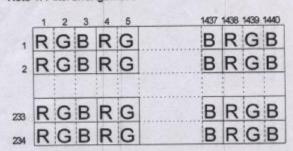
This draft specification is applicable to this Full Color TFT-LCD module "LTA070B410A-351" designed for Portable DVD and TV monitor use.

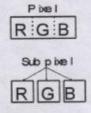
2. Specifications

2.1 General specifications

Items	specifications	Specifications	Note
Screen s	ize	18cm (7.0-inch) diagonal screen	
Display r		TN full color, Transmissive type	Normally white
Contents		a-Si TFT active matrix panel Line sequential scanning Non-Interlace drive	
50 75	Sync.	Horizontal/Vertical start puls and clock	
Input	Signal	Specified analog RGB signals (H-line alternate signal)	
signal	Common	H-line alternate signal	
Dimensi	onal outline	163.08(W) x 97.65(H) x 1.9(D) max.	[unit:mm]
Active a		154.08(W) x 87.048(H)	[unit:mm]
7 14 4 4 4	angement	RGB stripe	Note 1
	of pixels	480(W) x 234(H)	Note 1
	of sub-pixels	1440(W) x 234(H)	Note 1
Pixel pit		0.3210(W) x 0.3720(H)	[unit:mm]
	direction	6 o'clock (in direction of maximum CR)	Refer to figure A (Next sheet)
Weight	and the	865g(typ.)	g
and the same of th	treatment	Anti-glare coating on LCD panel surface. wide view film	

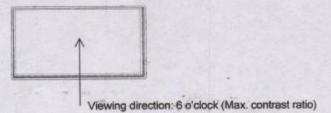
Note 1: Pixel arrangement





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Figure A: Viewing direction



This product be used as 12 o'clock viewing direction by setting LCD module upside down, and switching scanning direction.

2.2 Absolute maximum ratings

Items		Symbol Conditions		Absolute maximum ratings		Unit	Remarks
				Min.	Max.		
Supply Voltage		V001, V002		- 0.3	6.5	V	
		Vgн		15.0	29.0 Vss 45.0	V	
		VGL	Ta=25 ± 5°C	-17.0			
		VGH-VGL		15.0			
	Video	VR,VG,VB	Vss=0V	Vss+0.2	V002-0.3	٧	
Input signal	Sync.	STH1,etc		Vss-0.3	Vpp+0.3	V	
voltage	Common	Voom		-10.0	13.0	V	1
Operating temperature		Тор	-	-30	85	°C	Note 2,3
Storage tempe		Tstg	-	-40	85	°C	Note 2

Note 1 : Do not exceed the maximum rating values under the worst probable conditions taking into account the supply voltage variation, input voltage variation, variation in part constants, and ambient temperature and so on. Otherwise the module may be damaged.

Note 2: The temperature on TFT-LCD panel surface.

Note 3: The temperature of the module rises under the influence of the backlight.

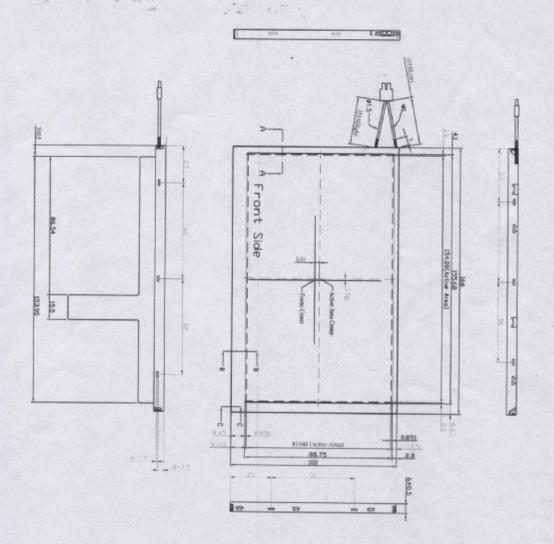
Please design that all parts fo this module does not to exceed 85°

Note 4: Operation time < 15min. Screen quality should not have distinguished unevenness.

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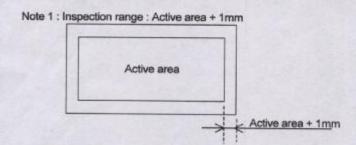
- 2.3 Mechanical specifications
- 2.3.1 Weight: 865g (typ.)
 2.3.2 Dimensional outline and label indication
 - (a) Dimensional outline (Front view)

Unit: mm



2.3.3 Appearance

Inspection parts	Items		Standard		Remarks
	Front	Line	Line width (mm) W< 0.05 0.05 ≤ W< 0.15 0.15 ≤ W	Acceptable count / length neglect total length ≤ 50mm NG	
Cell	scratch	Spot	Size (sub pixel) D < 1 1 ≤ D < 3 3 ≤ D	Judgement neglect 1 dark sub pixel NG	Note 1
		cratch	Same as front scratch (Condition : front view)	
	Flaw			should be maintained.	
	Dirt Discolora	tion	No dirt which can not No conspicuous disco		-
Interface	Scratch		No scratch.	TO GROUP TO THE PARTY OF THE PA	
connector	Distortion		No distortion.		-



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2.4 Interface

[CN1] LCD FPC Cable: (0.5pich,30pin)

No.	Symbol	Functions	1/0	Remarks
1	NC	No Connect		
2	V GL	Input power supply -12.0V	Input	Note 1
3	NC	No Connect		
4	Vss	Ground (0V)		
5	STV1	Vertical start pulse 1	Input	Note 2
6	U/D	Scanning direction switch	Input	Note 2
7	CPV	Vertical clock pulse	Input	Note 2
8	OE3	Vertical(gate) enable 3	Input	Note 2
9	OE2	Vertical(gate) enable 2	Input	Note 2
10	OE1	Vertical(gate) enable 1	Input	Note 2
11	STV2	Vertical start pulse 2	Input	Note 2
12	VDD1	Power Supply +3.3V	Input	Note 1
13	VDD2	Power Supply +5.0V	Input	Note 1
14	NC	No Connect	- Inpat	110.01
15	V GH	Input power supply 18.5V	Input	Note 1
16	NC	No Connect		110101
17	Vcom	Common voltage	Input	Note 2
18	VSS	Ground (0V)	- Input	11010 2
19	VSS	Ground (0V)		
20	CPH	Horizontal clock pulse	Input	Note 2
21	VSS	Ground (0V)	- Input	11010 2
22	CX	Column driver latch channel control	Input	Note 2
23	L/R	Scanning direction switch	Input	Note 2,3
24	STH1	Horizontal start pulse 1	Input	Note 2,3
25	VSS	Ground (0V)	mput	140(6 2,5
26	VB	Video input B	Input	Note 2
27	VG	Video input G	Input	Note 2
28	VR	Video input R	Input	Note 2
29	STH2	Horizontal start pulse 2	Input	Note 2,3
30	NC	No Connect	mput	14016 2,5

Note 1 : Power supply sequence

Power ON	Voor -> Vol> Voce -> VoH
Power OFF	VgH -> Vp02 -> VgL -> Vp01

Note 2 : Refer to "2.6.2 Indication Timing diagram "

Note 3 : U/D(pin 6)=Lo , STV1(pin 10)=input , STV2(pin 4)=opening L/R(pin 23)=Lo ,STH1(pin 17)=input , STH2(pin 33)=opening

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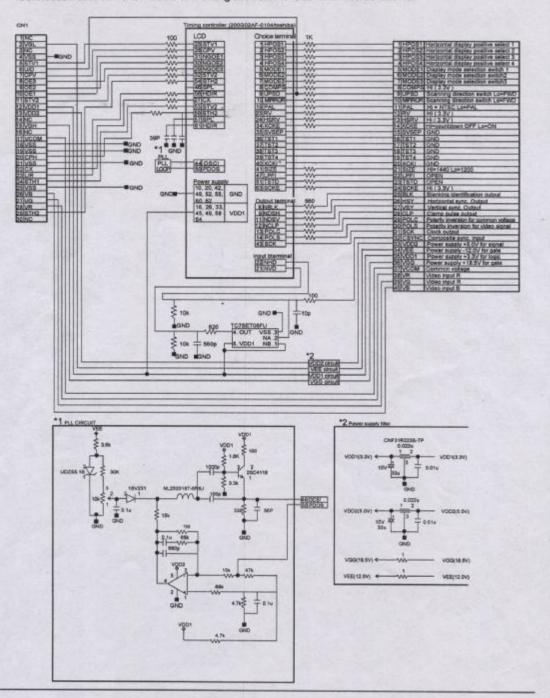
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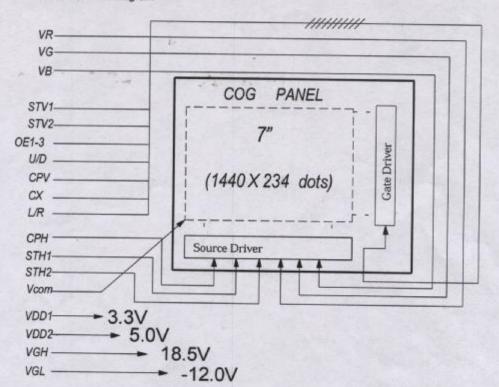
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2.5 Application

Connection between this module and timing controller is recommended as follows.



2.6 Electrical specifications 2.6.1 Circuit block diagram



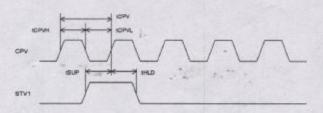
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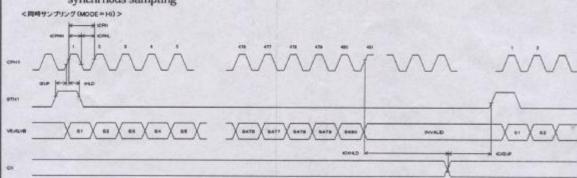
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2.6.2 Indication Timing diagram vertical timing



2.6.3 horizontal timing diagram

synchrnous sampling

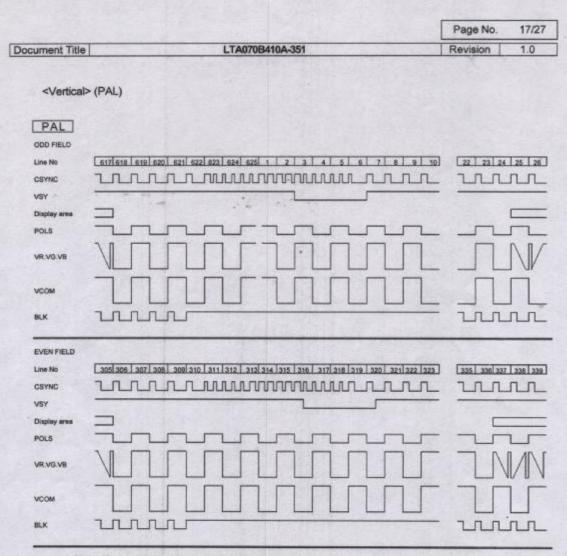


Page No. 16/27 Document Title LTA070B410A-351 Revision 1.0 Indication timing diagram <Vertical> (NTSC) Timing example for use timing controller[TC200G02AF-0104(TOSHIBA)] NTSC ODD FIELD Line No 519 520 521 522 523 524 525 1 2 3 4 5 6 7 8 9 10 11 12 21 22 23 24 25 CSYNC VSY Display area POLS VR.VG.VB VCOM BLK EVEN FIELD 256 257 258 259 200 261 262 263 264 265 266 267 268 269 270 271 272 273 274 Line No 284 285 286 287 288 CSYNC VSY Display area POLS VR.VG.VB VCOM BLK

Display differs by a selection of display mode. (Refer to Right table)

An aforementioned timing chart shows display position around FULL-MODE.

Display Mode	Display p	eriod (Line No.)
P1 11 1	ODD	EVEN
FULL NORMAL-CENTER NORMAL-LEFT NORMAL-RIGHT WIDE	24-257	287-520
ZOOM1	53-228	316-491
ZOOM2	39-242	302-505
ZOOM3	54-257	316-520



Oisplay differs by a selection of display mode. (Refer to Right table)

An aforementioned timing chart shows display position around FULL-MODE

Page No. 18/27 LTA070B410A-351 Revision 1.0 Document Title <Horizontal> (NTSC / PAL) 1H=63.56(64.00) CSYNC (HD) 3.17(3.19)+Te 4.72(4.74) HSY 11.68(11.76) VSY 3.26(3.29)+Td BLK 11.89(11.92) 50.01(50.36) 9.76(9.83)-Td Display area VR,VG,VB POLS VCOM

Note 1: Refer to "2.7 Recommended operating condition".

Note 2 : Value in brackets correspond to PAL mode.

Note 3: f HDN=15.734kHz, f HDP=15.625kHz

Note 4: Ťd= 0.208(0.210)µs x n

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2.7 Recommended operating conditions

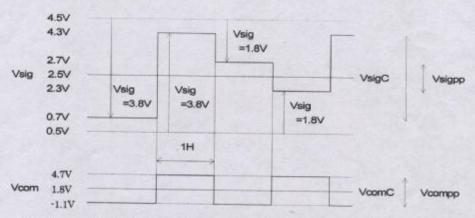
(Ta=25 ± 5°C, Vss=0V)

ITEMS	SYMBOL	CONDITONS	SPECIF	ICATIONS	(Note 1)	LINE	
TILINO	OTMBOL	CONDITONS	MIN.	TYP.	MAX.	UNIT	Remarks
SUPPLY	V _{DD1}		+3.00	+3.30	+3.60		
POWER VOLTAGE	V _{DD2}		+4.50	+5.00	+5.50	V	
	V _{GH}	0.00	+17.5	+18.5	+19.5	V	
	V _{GL}	400	-13.0	-12.0	-11.0		
NPUT VIDEO VOLTAGE	VsigC		+2.4	+2.5	+2.6	V	17.11.11
	Vsigpp	VsigC=2.5V	3.7	4.0	4.3	V(p-p)	
COMMON SIGNAL	VcomC	VsigC=2.5V	-	(+1.8)		V	Note 3
AMPLITUDE	Vcompp	-	≠5.5	5.8	7.0	V(p-p)	
INPUT SIGNAL	V _{IH}	H LEVEL	0.8V _{DD1}	-	V _{DD1}	100000000000000000000000000000000000000	
(C-MOS)	V _L	L LEVEL	Vss	-	0.2V _{DD1}	V	
	fclk	СРН	9.20	9.59 (9.53)	9.95	MHz	
Frame frequency	ICLK	CPV	15.00	15.734 (15.625)	16.50	KHz	
	fsr	STH	15.00	15.734 (15.625)	16.50	KHz	Note 2
	151	STV	57.143	59.939 (50)	62.852	Hz	

Note 1:The recommended operating conditions show the ranges in which the device can operate normally.

Operation beyond the limit of the recommended operating conditions is not assured, even though operating conditions are within the limit of the absolute maximum ratings.

Note 2: Value in brackets correspond to PAL mode. Note 3:Input signal timing.(Vsig,Vcom)



Optimization of input Voltage on viewing angle is necessary. Adjust for minimum flicker.

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2.8 Electrical characteristics

2.8.1 Electrical characteristics

Common test conditions: 7 a=25 ± 5°C, RH=65 ± 5%,

V DD1=+3.3V, V DD2=+5.0V, V GH=18.5V, V GL=-12.0V, V SS=0V, Vsigc=+2.5V, Vcompp=+5.8V(p-p), Vcomc = Adjust for minimum flicker.

Items	Symbol	Conditions	Specification (Note1)			Link	-
items	Symbol	Conditions	Min.	Тур.	Max.	Unit	Remarks
	CIN		1000		50	pF	Note 1
Input capacity	Cvi			-	200	pF	Note 2
	C com	f=fHD/2	-	-	300	nF	Vcom
Supply current	/ DD1		1.5	2.4	2.9	mA	15500
	1002		8.0	22.0	37.0	mA	Note 3
	/ GH	Vsigpp=4.0V(p-p)	27.0	65.0	95.0	μА	
	/gL		180	350	450	μА	
The state of the state of	topy		-	-	100	kHz	
	tcpvH		500	-	0.00	ns	
	topvi		500			ns	
	tcpH		-		20	kHz	T. Verse
Input timing	tсрнн		15	-		ns	1-1-1-10
input aming	tophi		15	-	100	ns	
	tsup		7	-	- 1	ns	
	thin .		7	-		ns	
	texsup		1.0	-		μs	
	taxino		50		114	ns	

Note 1 :STV1,STV2,CPV,OE1,OE2,OE3,CPH,STH1,STH2,CX

Note 2: VR,VG,VB

Note 3: Timing controller current is not include.

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2.9 Optical specifications

2.9.1 Optical characteristics

Common test conditions: T ==25 ± 5°C, RH=65 ± 5%,

V DD1=+3.3V, V DD2=+5.0V, V GH=18.5V, V GL= -12.0V, V SS=0V,

Vsigc=+2.5V, Vcompp=+5.8V(p-p), Vcomc = Adjust for minimum flicker.

Measured after 30 minutes operation.

ITEM	2 12	SYMBOL	CONDITIONS	SPEC	CIFICATIO	NS	UNIT	REMARKS
TICIVI		STWIDOL	MIDGE GOINDITIONS	MIN.	TYP.	MAX.	UNIT	REMARKS
RESPONSE T	IME	ton	Vsig=4→0V	-	40	100	me	
TALOI ONOL I	IIVIL.	TOFF	Vsig=0→4V	-	20	50	ms	
CONTRAST R	ATIO	CR	Vsig=0/4	50	200	-		Note1,2
VIEWING	L/R	910	Vsig=0/4V	45/45	55/55	-	0	
ANGLE	U/D	φ10	CR≥10	15/45	30/60	-		282
LUMINANCE		L	Vsig=1V	260	320	350	cd/m2	Note 2,3
AVERAGE BRIG	GHTNESS	L	Vsig=0V	78	-	-	cd/m2	Note 3,4
V-L THRESHO	DLD	Vth	90% LT	10 a-	1.0	1.5	V	
WHITE CHROMAT	TICITY	x	Vsig=0V		(0.31)	-		Reference
WHITE CHROWATION		У	vsig=uv		(0.33)	-		Note 3
UNIFORMITY D		DLUM	Vsig=0V	70			%	13375
SPECULAR REFL RATIO	ECTANCE	PSP	non-operating	-	1	3	%	

Note 1: 100% brightness.

Note 2 : These values vary with brightness input. Note 3 : This spec is spectroradiometer [BM-7A]. Note 4 : Refer to "4 Measuring Method" method A

2.10 Display quality

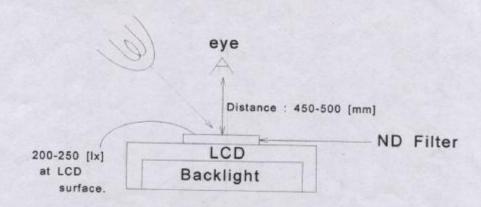
2.10.1 Display quality (Room temperature)

Common test conditions : $T = 25 \pm 5$ °C, RH=65 ± 5%,

V co1=+3.3V, V co2=+5.0V, V cH=18.5V, V cL= -12.0V, V sS=0V, Vsigc=+2.5V, Vcompp=+5.8V(p-p), Vcomc = Adjust for minimum flicker. Measured after 30 minutes operation.

Items		Definition	Standards	
	Line	Black, white, color line. Horizontal or vertical line missing.	No defect	
Display defect	Sub-pixel	1)Bright sub-pixel Bright defect (Black background : Vsig=4.0V) 2)Dark sub-pixel Dark defect (White Background : Vsig=0V)	Note 1	
	Smear	Spot defect whose luminance isn't uniform.		
	Unevenness	Partial luminance or chromatic non-uniformity.	Note 2 (next sheet	
Screen quality	Non-uniformity streak	Streak shaped luminance or chromatic non-uniformity.		
	Reverse Concentrated unevenness (sub-pixel unit).		- (Hoxt Sileet)	

Note 1 : Sub-pixel defect specifications Inspection condition of sub-pixel defects



Bright sub-pixels are classified into the following three category using appropriate Neutral Density (ND) Filter.

Bright sub-pixel (which are) not visible through 2.5% ND Filter are not counted as bright sub-pixel.

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(1) Sub-pixel defect

Items		Maximum count	Specifications		
Bright sub-pixel			0.3% ND	1.0% ND	2.5% ND
	Level 1	2	Visible	Visible	Visible
	Level 1,2	3	Invisible	Visible	Visible
	Level 1 to 3	5	Invisible	Invisible	Visible
Dark sub-pixel		10	Dark defect (Vsig=0V)		
Total		10 -	[Bright defect(Level 1+2+3) +Dark defect]		

(2) Cluster: (Bright sub-pixel density)

The maximum permissible sub-pixel defects number per specified area one as follows.

Bright sub-pixel (Level 2 + Level 2)	2 defects in
Bright sub-pixel (Level 2 + Level 3)	2 defects in è10mm area
Bright sub-pixel (Level 3 + Level 3)	2 defects in §10mm area
Dark sub-pixel	2 defects in \(\phi 10mm \) area
Total (Bright and dark defects)	2 defects in \$10mm area

(3) Linked sub-pixel defects

A)Three linked sub-pixel defects are not permissible.

B)Two linked sub-pixel defects;

Bright + Bright : Regarded as one bright defect and classified into Level 1, 2 or 3.

Dark + Dark : Regarded as two dark defects.

Dark + Bright : Regarded as one dark defect and one bright defect (classified into Level).

(4) If any sub-pixel is counted as bright sub-pixel and dark sub-pixel, the sub-pixel is regarded as bright sub-pixel.

(5) Dusts (in Cell)

	Size	Judgement
1	3 sub-pixels ≤ x	NG
2	2 sub-pixels ≤ x < 3 sub-pixels	Regarded as two linked dark defects.
3	1 sub-pixel ≤ x < 2 sub-pixels	Regarded as one dark defect.
4	≤x<1 sub-pixel	Uncounted.

Note 2 : Screen quality defects (smear etc.), which can not be found by using 2.5% ND filter, shall not be counted as defects.

Inspection condition: Same as sub-pixel defect condition.

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2.11 Reliability test 2.11.1 Environmental & life test

Items	Conditions	Judgement
High temperature operation	Ta=65 ± 2°C, RH ≤ 45%	time
High temperature and high humidity operation	Ta=65 ± 2°C, RH=90 ± 2%	192h
Low temperature operation	Ta=-30±3°C	192h
High temperature storage	Ta=85 ± 2°C, RH ≤ 45%	192h
Low temperature storage	Ta=-40 ± 3°C	
Light stability non-operating	Sun shine carbon arc, Ta=63 ± 2°C	192h
Thermal shock non-operating	-30°C(60min.) ←> 25°C(15min.) 80°C(60min.)	360h
Condensation test	-30°C(30min. non-operation)	20 cycle 10 cycle
Temperature and numidity cycling	25°C/95%(10min. operation) 1 cycle = 48h (25 to 65°C : 90 to 95%) 25°C 25°C	5 cycle
tatic discharge urability	C=200pF, R=0Ω, V= ± 150V Electric discharge between power supply terminals and other terminals [non-operating]	each terminals
urface electric ischarge durability	C=150pF, R =150 Ω , V = \pm 15kV Electric discharge between LCD panel and ground. (positive and negative) [non-operating]	positive and negative
ont force test	Push-pull head : \$12.7mm, 5 x 9.8N [non-operating]	5 times each
mospheric pressure	5.06 x 10 ⁴ Pa [non-operating]	a time

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2.11.2 Reliability test criteria

	Specification		(Note 1)		
Items	Contrast	Luminance	Supply current	Screen quality	Remark
High temperature operation	30 ≤ CR	≤±20%	≤+40%	Note 2	Note 3
High temperature and high humidity operation	25 ≤ CR	25 2.7			Note 3,4
Low temperature operation	30 ≤ CR				Note 3
High temperature storage					Note 3
Low temperature storage					Note 3
Light stability non-operating					Note 3
Thermal shock non-operating					Note 3
Condensation test	25 ≤ CR				Note 5
Temperature and humidity cycling non-operating					Note 5
Static discharge durability non-operating	No abnormal f	unction or indica	tion		
Surface electric discharge durability non-operating					
Front force test					
Atmospheric pressure stability non-operating					

Note 1: The Change quantity for initial value.

Note 2 : Screen quality should not have distinguished unevenness.

Note 3: Measured after 2 hours storage in room temperature.

Note 4: Degradation due to the degradation of polarizer is excluded.

Note 5 : Measured after 24 hours storage in room temperature.

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3. Measuring Method

3.1 Mechanical & electrical items

1	Inspection or,test items	Measuring systems or definition
1.	Mechanical	
	A)Critical dimensions	Vernier caliper
	B)Marking	Visual
	C)Appearance	Visual
2.	Electrical	
	1)Supply current	Ampere meter
	2)Input capacitance	Capacitance meter

3.2 Optical items

3.2.1 Standard measuring conditions

Measure on the following conditions.

Operating voltage: V co1=+3.3V, V co2=+5.0V, V GH=+18.5V, V GL= -12.0V, V SS=0V,

Vsigc=+2.5V, Vcompp=5.8V, Vcomc=Adjust for minimum flicker.

(Measured after 30 minutes operation)

Measurement Back-light: Standart back-light (Luminance: 5000 cd/m²)

Measurement temperature: Ta=25 ± 2°C

The Measurement point: Measuring one point of the screen center.

Set the luminance meter above the normal line of the LCD module.

Inspection or test items	Measuring systems or definition	System
1. Contrast ratio	CR= L1/L2 L1: Vsig=0V, L2: Vsig=4V	method A (Next sheet)
2. Luminance (L)	Raster pattern (Vsig = 0V)	method A
 Viewing angle left / right (θ) up / down (φ) 	CR is larger than 10	method A
4. V-L threshold (V _{th})	L = Vsig 0 to 4V (step : 0.2V) V = L (90%)	method A
5. Uniformity	L=Vsig(0V) Uniformity = L (min.) / L (max.) x 100 ACTIVE AREA [unit : %] 10mm	method A
6. Specular reflectance	ρsp= L 3 / L 4 L 3: Non-operating LCD luminance L 4 : Mirror luminance	method A
7. Display defect and screen quality	Inspection shall be performed right above the module, apart 500mm from the module to eyes. (Vsig=0 to 4V)	Visual inspection

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LCD drive circuit : Supplies raster pattern to LCD. Photometer : BM-7 A(made in TOPCON).

Spectroradiometer: BM-7A (made in TOPCON).