Toshiba Matsushita Display Technology Co., Ltd.

PRODUCT INFORMATION

28cm COLOUR TFT-LCD MODULE
(11.1 TYPE)

LTD111EXDA
(p-Si TFT)

TENTATIVE

FEATURES

(1) 11.1"WIDE-XGA+(1366x768 pixels) display size for notebook PC
(2) Wedge shape, Light weight design
(3) Glare surface

MECHANICAL SPECIFICATIONS

Item	Specifications
Dimensional Outline (typ.)	260.0(*W*) x 152.0 (*H*) x 5.9-3.7max(*D*) mm
Number of Pixels	1366 (*W*) x 768(*H*) pixels
Active Area	245.88(*W*) x 138.24(*H*) mm
Pixel Pitch	0.18(*W*) x 0.18(*H*)
Weight (approximately)	(200) g
Backlight	Single CCFL, Sidelight type

ABSOLUTE MAXIMUM RATINGS

Item		Min.	Max.	Unit
Supply Voltage	(V_{DD})	-0.3	4.0	V
	(V_{FL})	0	2.0	kV(rms)
FL Driving Frequency (f_{FL})		-	100	kHz
Input Signal Voltage (V_{IN})		-0.3	V_{DD}+0.3	V
Operating Temperature		0	50	°C
Storage Temperature		-20	60	°C
Storage Humidity		10	90	%(RH)

ELECTRICAL SPECIFICATION

Item		Min.	Typ.	Max.	Unit	Remarks
Supply Voltage	(V_{DD})	3.0	3.3	3.6	V	
	(V_{FL})	---	(625)	---	V(rms)	I_{FL}=6.0 mA(rms)
FL Start Voltage (*T*a=0°C)		1500	---	1800	V(rms)	
Common Mode Input Voltage	(V_{CM})	1.15	---	1.45	V	
Differential Input Amplitude	(V_{ID})	100	---	600	mV	
Differential Input Voltage		V_{CM} -V_{ID}/2	---	V_{CM}+V_{ID}/2	V	
Current Consumption	*1 (I_{DD})	---	(312)	---	mA	
	*2 (I_{FL})	---	(6.0)	---	mA(rms)	
*2 *3 Power Consumption		---	(4.78)	---	W	I_{FL}=6.0 mA(rms)

*1 : 8 color bars pattern
*2 : Excepting the efficiency FL inverter

OPTICAL SPECIFICATION (*T*a=25°C)

Item		Min.	Typ.	Max.	Unit	Remarks
Contrast Ratio (*CR*)		(300)	(600)	---	---	
Response Time	(t_{ON})+ (t_{OFF})	---	---	50	ms	
Luminance (*L*)		---	(200)	---	cd/m^2	I_{FL}=(6.0)mA(rms)

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*The information contained herein may be changed without prior notice. It is therefore advisable to contact Toshiba Matsushita Display technology before proceeding with the design of equipment incorporating this product.

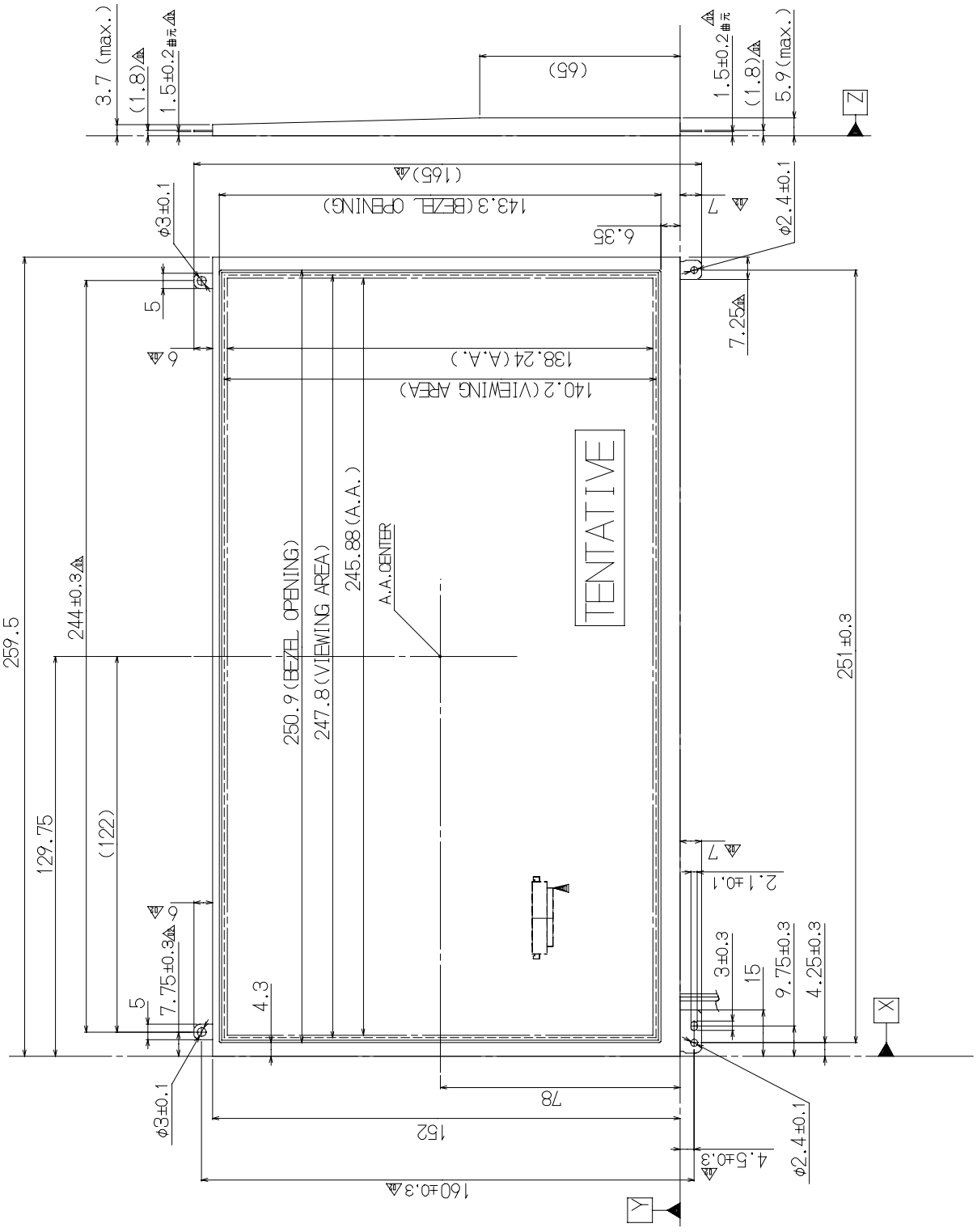
TENTATIVE

Unit : mm

Standard tolerance : ±0.5

DIMENSIONAL OUTLINE

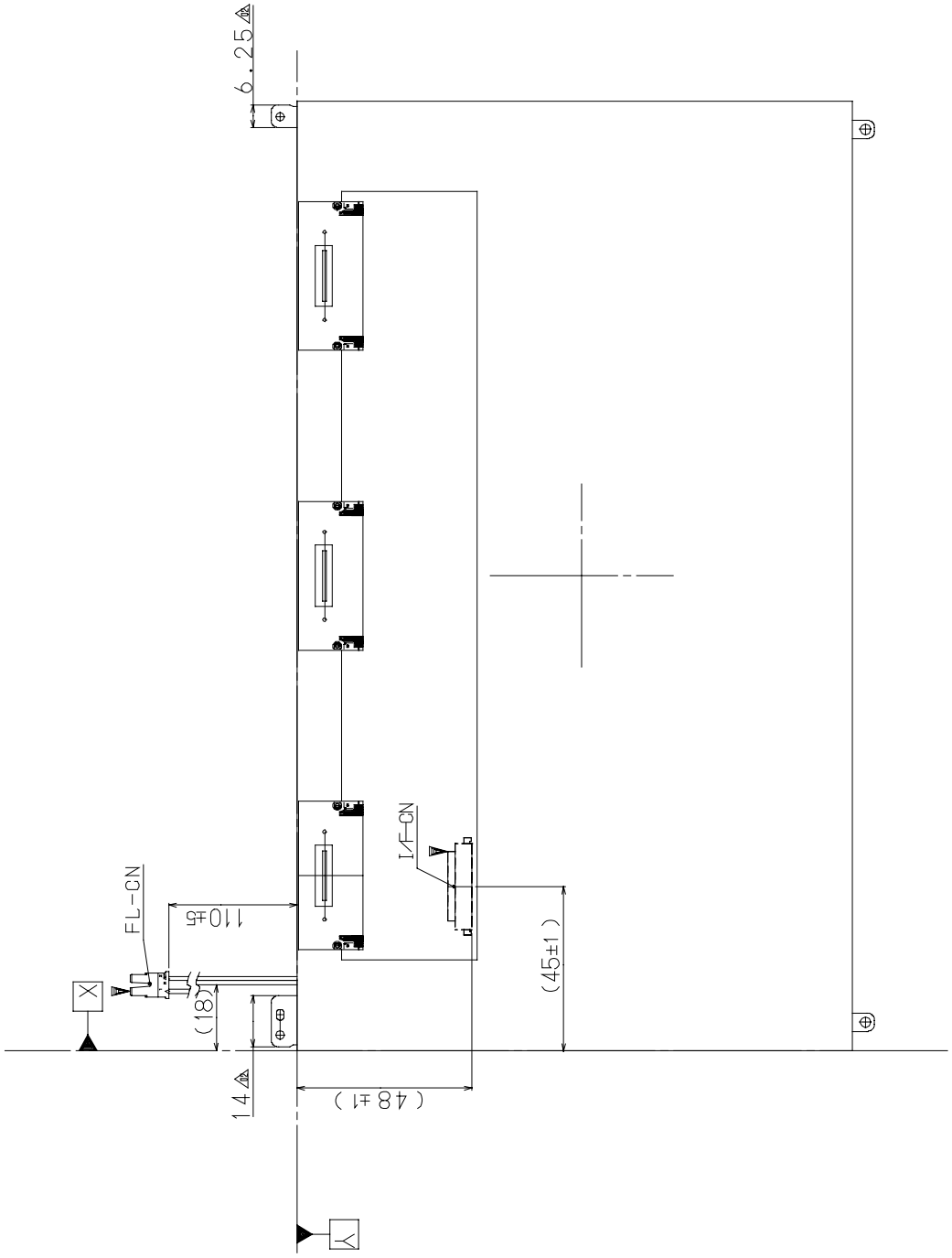
(Front side)



DIMENSIONAL OUTLINE

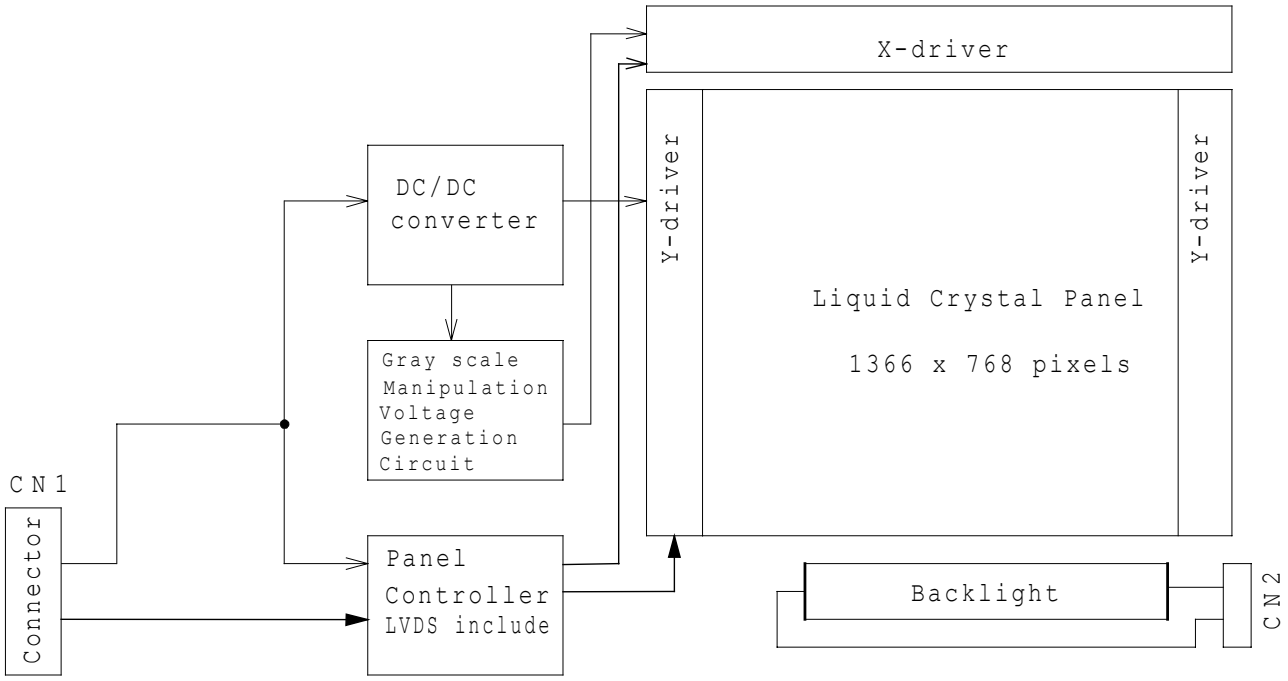
TENTATIVE

(Back side)



Note 1) Never push LCD back side. If LCD back side was pressed, It may cause damage of the back light system.

BLOCK DIAGRAM



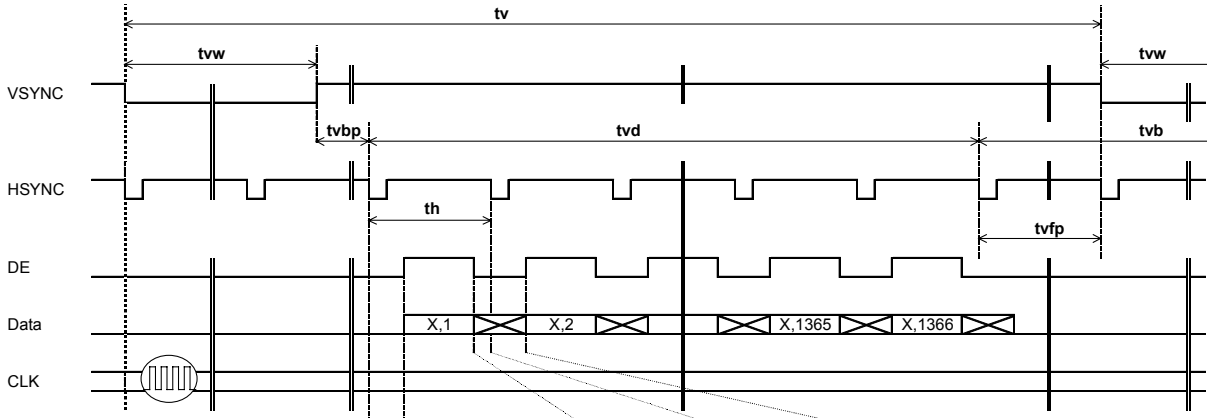
1366 pixels

1, 1	2, 1		$X2_{n-1}$, 1	$X2_n$, 1		1366, 1
1, 2						
1, Y			X_{2n-1}, Y	X_{2n}, Y		
1, 768						1366,768

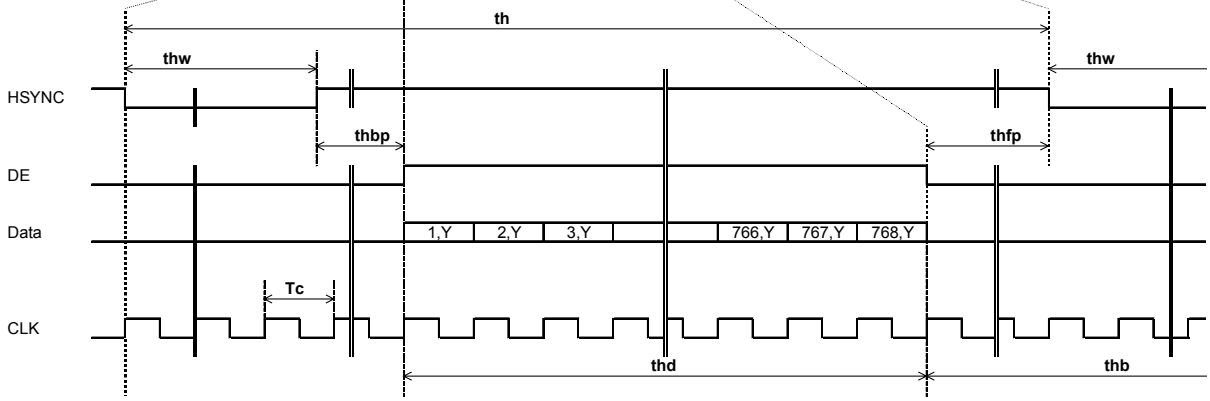
768 pixels

TIMING CHART

(1) Vertical Timing



(2) Horizontal Timing



TIMING SPECIFICATION [1) 2) 3) 4) 5) 6) 7)]

Item	Symbol	min.	typ.	max.	unit
Horizontal Scanning Term	*t*h	1656 x *t*c	1664 x *t*c		clock
		20.70	21.47		μs
H-sync Pulse Width	*t*hw	8 x *t*c	128 x *t*c	-	clock
Horizontal Front Porch	*t*hfp	8 x *t*c	64 x *t*c	-	clock
Horizontal Back Porch	*t*hbp	8 x *t*c	106 x *t*c	-	clock
Horizontal Display Term	*t*hd	1366 x *t*c	1366 x *t*c	1366 x *t*c	clock
Horizontal blanking	*t*hb	290 x *t*c	298 x *t*c		clock
Frame Period	*t*v	774 x *t*h	776 x *t*h		line
		16.02	16.66		ms
V-sync Pulse Width	*t*vw	1 x *t*h	1 x *t*h	-	line
Vertical blanking	*t*vd	6 x *t*c	8 x *t*c	-	line
Vertical Front Porch	*t*vfp	1 x *t*h	1 x *t*h	-	line
Vertical Back Porch	*t*vbp	4 x *t*h	6 x *t*h	-	line
Vertical Data Sync Period	*t*vds	2 x *t*h	27 x *t*h	-	line
Vertical Display Term	*t*vd	768 x *t*h	768 x *t*h	768 x *t*h	line
Clock Period	*t*c	12.50	12.903		ns
			77.5	80	MHz

Note 1) Refer to “Timing Chart” and LVDS (THC63LVDF84A-85) specifications by THine Electronics, Inc. corporation.

Note 2) If DE is fixed to "H" or "L" level for certain period while NCLK is supplied, the panel displays black with some flicker.

Note 3) If NCLK is fixed to "H" or "L" level for certain period while DE is supplied, the panel may be damaged.

Note 4) Please adjust LCD operating signal timing and FL driving frequency, to optimize the display quality.
There is a possibility that flicker is observed by the interference of LCD operating signal timing and FL driving condition (especially driving frequency), even if the condition satisfies above timing specifications and recommended operating conditions shown in 3.

Note5) tvb = tvw + tvfp + tvbp
thb = thw + thfp + thbp

Note6) In case of using the long frame period, the deterioration of display quality, noise etc. may be occurred.

Note7) NCLK count of each Horizontal Scanning Time should be always the same.
V-Blanking period should be “*n*” X “Horizontal Scanning Time”. (*n*: integer)
Frame period should be always the same.

CONNECTOR PIN ASSIGNMENT FOR INTERFACE

TENTATIVE

CN1 INPUT SIGNAL

Connector : DF19L-20P-1H / HIROSE

Mating Connector : DF19G-20S-1F (FPC), DF19G-20S-1C (Cable)

Terminal No.	Symbol	Function
1	V_{DD}	Power Supply : +3.3V
2	V_{DD}	Power Supply : +3.3V
3	V_{SS}	GND
4	V_{SS}	GND
5	RxIN0-	Negative LVDS differential data input (R0-R5,G0)
6	RxIN0+	Positive LVDS differential data input (R0-R5,G0)
7	V_{SS}	GND
8	RxIN1-	Negative LVDS differential data input (G1-G5, B0-B1)
9	RxIN1+	Positive LVDS differential data input (G1-G5, B0-B1)
10	VSS	GND
11	RxIN2-	Negative LVDS differential data input (B2-B5, HS, VS, DE)
12	RxIN2+	Positive LVDS differential data input (B2-B5, HS, VS, DE)
13	V_{SS}	GND
14	CLK-	Clock Signal(-)
15	CLK+	Clock Signal(+)
16	V_{SS}	GND
17	V_{SS}	GND
18	V_{SS}	GND
19	V_{SS}	GND
20	V_{SS}	GND

Note 1) Please connect GND pin to ground. Don't use it as no-connect nor connection with high impedance.

Note 2) Please connect NC to nothing. Don't connect it to ground nor to other signal input.

CN2 CCFL POWER SOURCE

Connector : BHSR-02VS-1 / JAPAN SOLDERLESS TERMINAL MFG CO., LTD.

Mating Connector : SM02B-BHS-1 / JAPAN SOLDERLESS TERMINAL MFG CO., LTD.

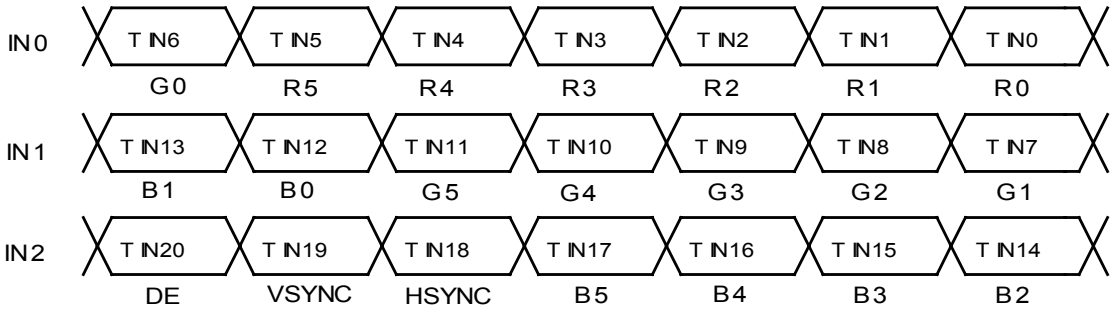
Terminal No.	Symbol	Function
1	V_{FLH}	CCFL Power Supply (high voltage)
2	V_{FLL}	CCFL Power Supply (low voltage)

RECOMMENDED TRANSMITTER (THC63LVDM63B) TO LTD111EXDA INTERFACE ASSIGNMENT

Case1: 6bit Transmitter

THC63LVDM63B					LTD111EXDA Interface (CN1)	
Input Terminal No.		Input Signal (Graphics controller output signal)		Output Signal Symbol		
Symbol	Terminal	Symbol	Function		Terminal	Symbol
TIN0	44	R0	Red Pixels Display Data (LSB)	TOUT0- TOUT0+	No.5 No.6	IN0- IN0+
TIN1	45	R1	Red Pixels Display Data			
TIN2	47	R2	Red Pixels Display Data			
TIN3	48	R3	Red Pixels Display Data			
TIN4	1	R4	Red Pixels Display Data			
TIN5	3	R5	Red Pixels Display Data (MSB)			
TIN6	4	G0	Green Pixels Display Data (LSB)			
TIN7	6	G1	Green Pixels Display Data	TOUT1- TOUT1+	No.8 No.9	IN1- IN1+
TIN8	7	G2	Green Pixels Display Data			
TIN9	9	G3	Green Pixels Display Data			
TIN10	10	G4	Green Pixels Display Data			
TIN11	12	G5	Green Pixels Display Data (MSB)			
TIN12	13	B0	Blue Pixels Display Data (LSB)			
TIN13	15	B1	Blue Pixels Display Data			
TIN14	16	B2	Blue Pixels Display Data	TOUT2- TOUT2+	No.11 No.12	IN2- IN2+
TIN15	18	B3	Blue Pixels Display Data			
TIN16	19	B4	Blue Pixels Display Data			
TIN17	20	B5	Blue Pixels Display Data (MSB)			
TIN18	22	HSYNC	H-Sync			
TIN19	23	VSYNC	V-Sync			
TIN20	25	DE	Compound Synchronization Signal			
CLK IN	26	CLK	Data Sampling Clock	TCLK OUT- TCLK OUT+	No.14 No.15	CLK- CLK+

Note 1) Please connect NC pin to nothing. Don't connect it to ground nor to other signal input.

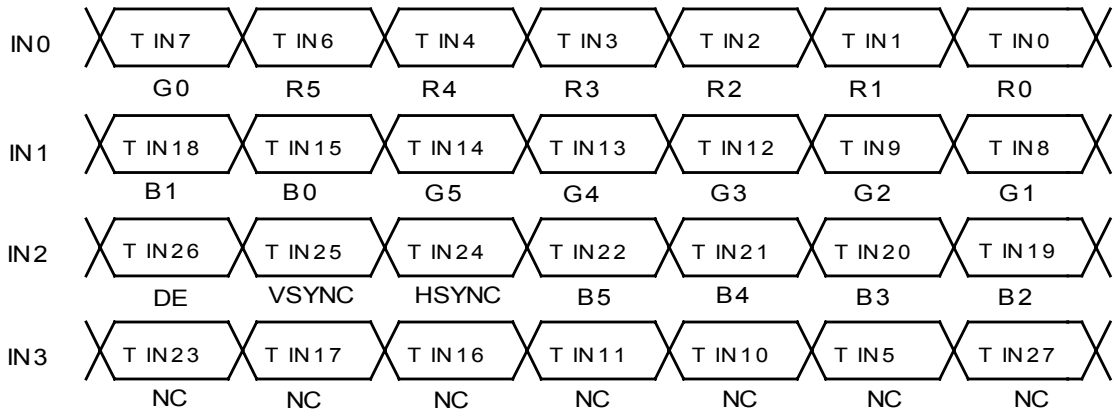


RECOMMENDED TRANSMITTER (THC63LVDM83R) TO LTD111EXDA INTERFACE ASSIGNMENT

Case2: 8bit Transmitter

THC63LVDM83R					LTD111EXDA Interface (CN1)	
Input Terminal No.		Input Signal (Graphics controller output signal)		Output Signal Symbol		
Symbol	Terminal	Symbol	Function		Terminal	Symbol
TIN0	51	R0	Red Pixels Display Data (LSB)	TOUT0- TOUT0+	No.5 No.6	IN0- IN0+
TIN1	52	R1	Red Pixels Display Data			
TIN2	54	R2	Red Pixels Display Data			
TIN3	55	R3	Red Pixels Display Data			
TIN4	56	R4	Red Pixels Display Data			
TIN6	3	R5	Red Pixels Display Data (MSB)			
TIN7	4	G0	Green Pixels Display Data(LSB)			
TIN8	6	G1	Green Pixels Display Data	TOUT1- TOUT1+	No.8 No.9	IN1- IN1+
TIN9	7	G2	Green Pixels Display Data			
TIN12	11	G3	Green Pixels Display Data			
TIN13	12	G4	Green Pixels Display Data			
TIN14	14	G5	Green Pixels Display Data(MSB)			
TIN15	15	B0	Blue Pixels Display Data (LSB)			
TIN18	19	B1	Blue Pixels Display Data			
TIN19	20	B2	Blue Pixels Display Data	TOUT2- TOUT2+	No.11 No.12	IN2- IN2+
TIN20	22	B3	Blue Pixels Display Data			
TIN21	23	B4	Blue Pixels Display Data			
TIN22	24	B5	Blue Pixels Display Data (MSB)			
TIN24	27	HSYNC	H-Sync			
TIN25	28	VSYNC	V-Sync			
TIN26	30	DE	Compound Synchronization Signal			
TIN27	50	NC	Non Connection (open)	TOUT3- TOUT3+	———	———
TIN5	2	NC	Non Connection (open)			
TIN10	8	NC	Non Connection (open)			
TIN11	10	NC	Non Connection (open)			
TIN16	16	NC	Non Connection (open)			
TIN17	18	NC	Non Connection (open)			
TIN23	25	NC	Non Connection (open)			
CLK IN	31	CLK	Data Sampling Clock	TCLK OUT- TCLK OUT+	No.14 No.15	CLK- CLK+

Note 1) Please connect NC pin to nothing. Don't connect it to ground nor to other signal input.



256k (k=1024) COLORS COMBINATION TABLE

	Display	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0	Gray Scale Level
Basic Color	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	-
	Green	L	L	L	L	L	L	H	H	H	H	H	H	L	L	L	L	L	L	-
	Light Blue	L	L	L	L	L	L	H	H	H	H	H	H	H	H	H	H	H	H	-
	Red	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	-
	Purple	H	H	H	H	H	H	L	L	L	L	L	L	H	H	H	H	H	H	-
	Yellow	H	H	H	H	H	H	H	H	H	H	H	H	L	L	L	L	L	L	-
	White	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	-
Gray Scale of Red	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L 0
	Dark ↑ ↓ Light	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L 1
		L	L	L	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L 2
		⋮						⋮						⋮						L3… L60
		H	H	H	H	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L61
		H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L62
	Red	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	Red L63
Gray Scale of Green	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L 0
	Dark ↑ ↓ Light	L	L	L	L	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L 1
		L	L	L	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L 2
		⋮						⋮						⋮						L3… L60
		L	L	L	L	L	L	H	H	H	H	L	H	L	L	L	L	L	L	L61
		L	L	L	L	L	L	H	H	H	H	H	L	L	L	L	L	L	L	L62
	Green	L	L	L	L	L	L	H	H	H	H	H	H	L	L	L	L	L	L	Green L63
Gray Scale of Blue	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L 0
	Dark ↑ ↓ Light	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L 1
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L	L 2
		⋮						⋮						⋮						L3… L60
		L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	L	H	L61
		L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	L	L62
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	Blue L63
Gray Scale of White & Black	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L 0
	Dark ↑ ↓ Light	L	L	L	L	L	H	L	L	L	L	L	H	L	L	L	L	L	H	L 1
		L	L	L	L	H	L	L	L	L	L	H	L	L	L	L	L	H	L	L 2
		⋮						⋮						⋮						L3… L60
		H	H	H	H	L	H	H	H	H	H	L	H	H	H	H	H	L	H	L61
		H	H	H	H	H	L	H	H	H	H	H	L	H	H	H	H	H	L	L62
	White	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	White L63

FOR SAFETY

LCD module is generally designed with precise parts to achieve light weighted thin mechanical dimensions.
In using our Modules, make certain that you fully understand and put into practice the warnings and safety precautions detailed in Engineering Information No.EE-D-001A,"CAUTIONS AND INSTRUCTIONS FOR TOSHIBA MATSUSHITA DISPLAY TECHNOLOGY CO., LTD LCD MODULES".
Refer to individual specifications and TECHNICAL DATA sheets (hereinafter called "TD") for more detailed technical information.

1) SPECIAL PURPOSES

A) Toshiba Matsushita Display technology's Standard LCD Modules have not been customized for operation in extreme environments or for use in applications where performance failures could be life-threatening or otherwise catastrophic.

B) Since Toshiba Matsushita Display technology's Standard LCD Modules have not been designed for operation in extreme environments, they must never be used in devices that will be exposed to abnormally high levels of vibration or shock which exceed Toshiba Matsushita Display technology's published specification limits.

C) In addition, since Toshiba Matsushita Display technology Standard LCD Modules have not been designed for use in applications where performance failures could be life-threatening or catastrophic, they must never be installed in aircraft navigation control systems (such as, but not limited to Traffic Collision Avoidance System and Air Traffic Indicator), in military defense or weapons systems, in critical industrial process-control systems (e.g., those involved in the production of nuclear energy), or in critical medical device or patient life-support systems.

2) DISASSEMBLING OR MODIFICATION

DO NOT DISASSEMBLE OR MODIFY the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display.
Toshiba Matsushita Display technology doses not warrant the module, if customer disassembled or modified it.

3) BREAKAGE OF LCD PANEL

DO NOT INGEST liquid crystal material, DO NOT INHALE this material, and DO NOT CONTACT the material with skin, if LCD panel is broken and liquid crystal material spills out.
If liquid crystal material comes into mouth or eyes, rinse mouth or eyes out with water immediately.
If this material contact with skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.

4) GLASS OF LCD PANEL

BE CAREFUL WITH CHIPS OF GLASS that may cause injuring fingers or skin, when the glass is broken.

5) ELECTRIC SHOCK

DISCONNECT POWER SUPPLY before handling LCD module.
DO NOT TOUCH the parts inside LCD module and the fluorescent lamp's connector or cables in order to prevent electric shock, because high voltage is supplied to these parts from the inverter unit while power supply is turned on.

6) ABSOLUTE MAXIMUM RATINGS AND POWER PROTECTION CIRCUIT

DO NOT EXCEED the absolute maximum rating values under the worst probable conditions caused by the supply voltage variation, input voltage variation, variation in parts' constants, environmental temperature, etc., otherwise LCD module may be damaged.
Employ protection circuit for power supply, whenever the specification or TD specifies it.
Suitable protection circuit should be applied for each system design.

7) DISPOSAL

When dispose LCD module, obey to the applicable environmental regulations.