# Toshiba Matsushita Display Technology Co., Ltd.,

33.7cm COLOUR TFT-LCD MODULE

(13.3 TYPE)

LTD133EWDA (p-Si TFT)

PRODUCT INFORMATION

All information is subject to change without notice. Please read bottom notes.

### **FEATURES**

- (1) 13.3"WIDE-XGA(1280x800 pixels) display size for notebook PC
- (2) LED Backlight with LED controller IC
- (3) Glare Surface
- (4) Bezel less structure



## **MECHANICAL SPECIFICATIONS**

Item	Specifications
Dimensional Outline (typ.)	304.0(W) x 202.4 (H) x 3.3(D) mm
Number of Pixels	1280 (W) x 800(H) pixels
Active Area	286.08(W) x 178.8(H) mm
Pixel Pitch	0.2235(W) x 0.2235(H) mm
Weight (approximately)	(225) g
Backlight	LED

### **ABSOLUTE MAXIMUM RATINGS**

Item		Min.	Max.	Unit
Supply Voltage	oply Voltage $(V_{DD})$ -0.3 4.		4.0	V
	$(V_{LED})$	-0.3	22	V
LED Currency (I <sub>LED</sub> )		-	(30)	mA
Input Signal Voltage (V <sub>IN</sub> )		-0.3	$V_{\rm DD} + 0.3$	V
Operating Temperature		0	50	°C
Storage Temperature		-20	60	°C
Storage Humidity		10	90	%(RH)

## **ELECTRICAL SPECIFICATION**

Item		Min.	Тур.	Max.	Unit	Remarks
Supply Voltage	$(V_{DD})$	3.0	3.3	3.6	V	
Supply LED Voltage	$(V_{LED})$	7.5	12	21	V	
Common Mode Input Voltage	$(V_{CM})$	0.7	-	1.75	V	
Differential Input Amplitude	$(V_{ID})$	100		600	mV	
Current Consumption	*1 (I <sub>DD</sub> )		300	400	mA	
	*2 (I <sub>LED</sub> )		15	17.5	mA	
Power Consumption			TBD.		W	PWM=100%:15 mA

<sup>\*1 : 8</sup> color bars pattern

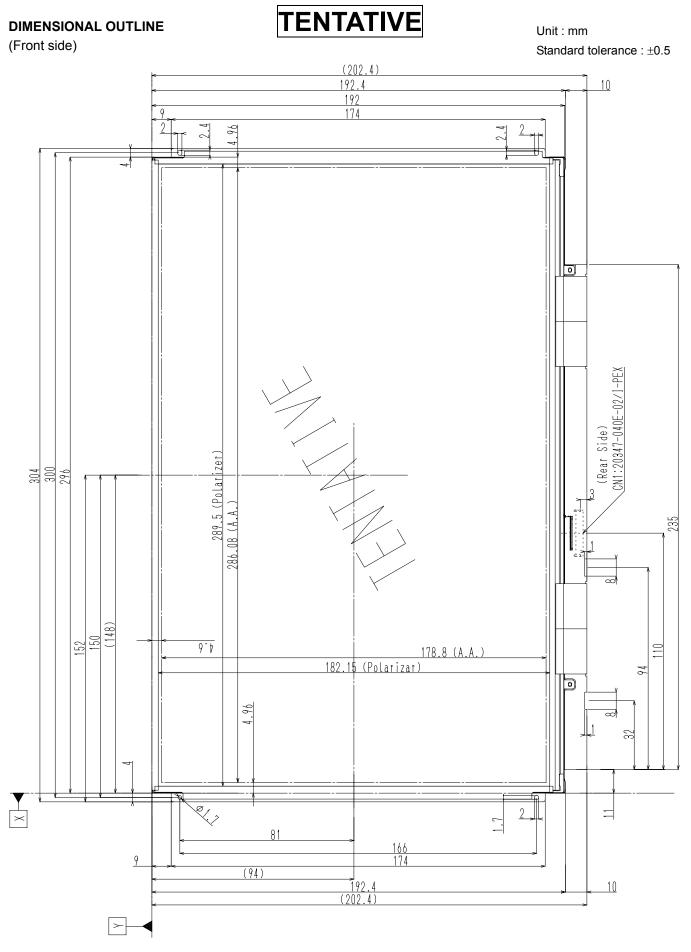
### **OPTICAL SPECIFICATION** (*T*a=25°C)

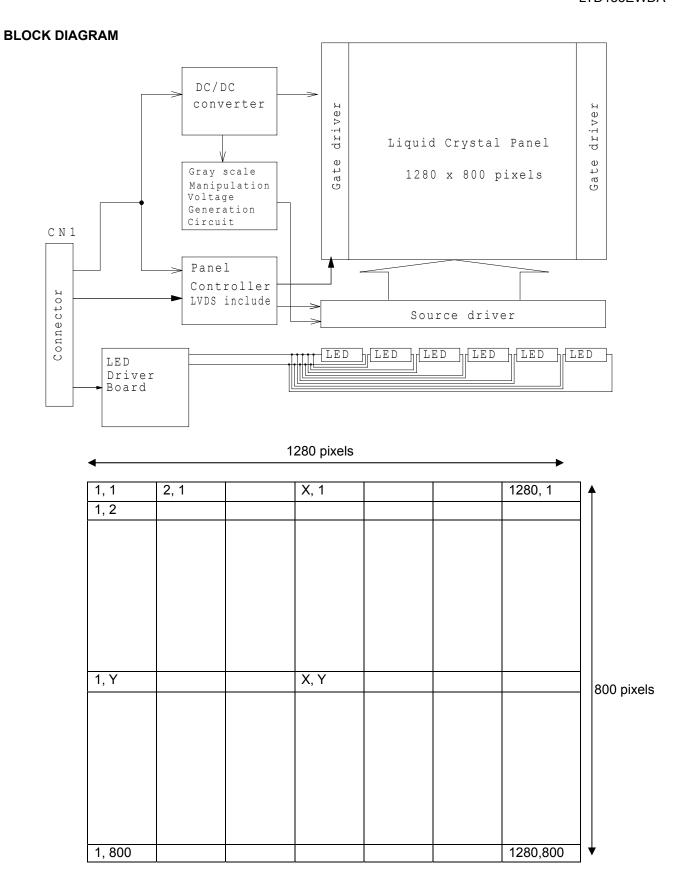
Item	·	Min.	Тур.	Max.	Unit	Remarks
Contrast Ratio (CR)		300	500			
Response Time	$(t_{\text{ON}})$ + $(t_{\text{OFF}})$		-	(50)	ms	
Luminance (L)		210	300		cd/m <sup>2</sup>	PWM=100%:15mA

<sup>\*</sup>The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by Toshiba Matsushita Display technology or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Toshiba Matsushita Display technology or others.

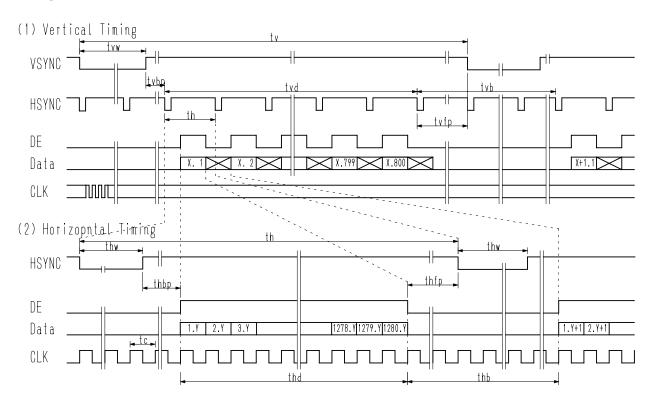
<sup>\*2 :</sup> The current value of each row should be the same value.

<sup>\*</sup>The information contained herein may be changed without prior notice. It is therefore advisable to contact Toshiba Matsushita Display technology before proceeding with the design of equipment incorporating this product.





# **TIMING CHART**



# TIMING SPECIFICATION $^{1)2)3)4)5)6)$

Item	Symbol	min.	typ.	max.	unit
Horizontal Scanning Term	<i>t</i> h	-	1448 x tc	-	clock
H-sync Pulse Width	<i>t</i> hw	4 x tc	32 x tc	-	clock
Horizontal Front Porch	<i>t</i> hfp	4 x tc	48 x tc	-	clock
Horizontal Back Porch	<i>t</i> hbp	4 x tc	80 x tc	-	clock
Horizontal Blanking Period(*8)	<i>t</i> hb	-	168 x tc	-	clock
Horizontal Display Term	<i>t</i> hd	1280 x tc	1280 x tc	1280 x tc	line
Frame Period	tv	-	830 x th	-	line
V-sync Pulse Width	tvw	1 x <i>t</i> h	6 x <i>t</i> h	-	line
Vertical Front Porch	<i>t</i> vfp	1 x <i>t</i> h	3 x <i>t</i> h	-	line
Vertical Back Porch	<i>t</i> vbp	2 x <i>t</i> h	14 x <i>t</i> h	-	line
Vertical Blanking Period(*9)	<i>t</i> vb	-	30 x th	-	ns
Vertical Display Term	<i>t</i> vd	800 x th	800 x th	800 x th	
Clock Period	tc	-	13.87	-	

Note 1) Refer to "Timing Chart" and LVDS specifications in TIA/EIA-644.

Note 2) If DE is fixed to "H" or "L" level for certain period while NCLK is supplied, the panel displays black with some flicker.

Note 3) If NCLK is fixed to "H" or "L" level for certain period while DE is supplied, the panel may be damaged.

Note4) 
$$tvb = tvw + tvfp + tvbp$$
  
 $thb = thw + thfp + thbp$ 

Note5) In case of using the long frame period, the deterioration of display quality, noise etc. may be occurred.

Note6) NCLK count of each Horizontal Scanning Time should be always the same.

V-Blanking period should be "n" X "Horizontal Scanning Time". (n: integer)

Frame period should be always the same.

# **CONNECTOR PIN ASSIGNMENT FOR INTERFACE**

CN1 INPUT SIGNAL

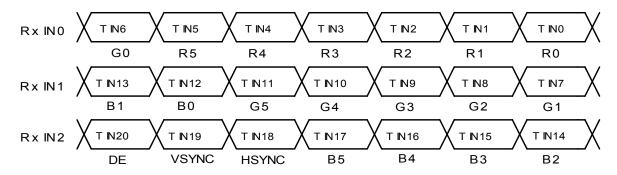
CN1 INPUT SIGNAL (20347-040E-02 / I-PEX)

[ Mating Connector : 20345-\*40T-## / I-PEX ]

1         GND         GND           2         NC         Non-Connection           3         VDD         Power Supply: +3.3V           4         VDD         Power Supply: +3.3V           5         VDD         Power Supply: +3.3V           6         VEDID         EDID 3.3V power           7         NC         Non-Connection           8         CLK         EDID clock           9         DATA         EDID data           10         GND         GND           11         GND         GND           12         NC         Non-Connection           13         RxIN0-         Negative LVDS differential data input (R0-R5, G0)           14         RxIN0+         Positive LVDS differential data input (R0-R5, G0)           15         GND         GND           16         RxIN1-         Negative LVDS differential data input (G1-G5, B0-B1)	Terminal No.	Symbol	Function
3         VDD         Power Supply: +3.3V           4         VDD         Power Supply: +3.3V           5         VDD         Power Supply: +3.3V           6         VEDID         EDID 3.3V power           7         NC         Non-Connection           8         CLK         EDID clock           9         DATA         EDID data           10         GND         GND           11         GND         GND           12         NC         Non-Connection           13         RxIN0-         Negative LVDS differential data input (R0-R5, G0)           14         RxIN0+         Positive LVDS differential data input (R0-R5, G0)           15         GND         GND           16         RxIN1-         Negative LVDS differential data input (G1-G5, B0-B1)	· ·	GND	GND
4         VDD         Power Supply: +3.3V           5         VDD         Power Supply: +3.3V           6         VEDID         EDID 3.3V power           7         NC         Non-Connection           8         CLK         EDID clock           9         DATA         EDID data           10         GND         GND           11         GND         GND           12         NC         Non-Connection           13         RxIN0-         Negative LVDS differential data input (R0-R5, G0)           14         RxIN0+         Positive LVDS differential data input (R0-R5, G0)           15         GND         GND           16         RxIN1-         Negative LVDS differential data input (G1-G5, B0-B1)		NC	Non-Connection
5         VDD         Power Supply: +3.3V           6         VEDID         EDID 3.3V power           7         NC         Non-Connection           8         CLK         EDID clock           9         DATA         EDID data           10         GND         GND           11         GND         GND           12         NC         Non-Connection           13         RxIN0-         Negative LVDS differential data input (R0-R5, G0)           14         RxIN0+         Positive LVDS differential data input (R0-R5, G0)           15         GND         GND           16         RxIN1-         Negative LVDS differential data input (G1-G5, B0-B1)			Power Supply: +3.3V
6         VEDID         EDID 3.3V power           7         NC         Non-Connection           8         CLK         EDID clock           9         DATA         EDID data           10         GND         GND           11         GND         GND           12         NC         Non-Connection           13         RxIN0-         Negative LVDS differential data input (R0-R5, G0)           14         RxIN0+         Positive LVDS differential data input (R0-R5, G0)           15         GND         GND           16         RxIN1-         Negative LVDS differential data input (G1-G5, B0-B1)			Power Supply: +3.3V
7         NC         Non-Connection           8         CLK         EDID clock           9         DATA         EDID data           10         GND         GND           11         GND         GND           12         NC         Non-Connection           13         RxIN0-         Negative LVDS differential data input (R0-R5, G0)           14         RxIN0+         Positive LVDS differential data input (R0-R5, G0)           15         GND         GND           16         RxIN1-         Negative LVDS differential data input (G1-G5, B0-B1)			
8         CLK         EDID clock           9         DATA         EDID data           10         GND         GND           11         GND         GND           12         NC         Non-Connection           13         RxIN0-         Negative LVDS differential data input (R0-R5, G0)           14         RxIN0+         Positive LVDS differential data input (R0-R5, G0)           15         GND         GND           16         RxIN1-         Negative LVDS differential data input (G1-G5, B0-B1)			·
9         DATA         EDID data           10         GND         GND           11         GND         GND           12         NC         Non-Connection           13         RxIN0-         Negative LVDS differential data input (R0-R5, G0)           14         RxIN0+         Positive LVDS differential data input (R0-R5, G0)           15         GND         GND           16         RxIN1-         Negative LVDS differential data input (G1-G5, B0-B1)	-		
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11 GND GND 12 NC Non-Connection 13 RxIN0- Negative LVDS differential data input (R0-R5, G0) 14 RxIN0+ Positive LVDS differential data input (R0-R5, G0) 15 GND GND 16 RxIN1- Negative LVDS differential data input (G1-G5, B0-B1)			
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15 GND GND 16 RxIN1- Negative LVDS differential data input (G1-G5, B0-B1)			
16 RxIN1- Negative LVDS differential data input (G1-G5, B0-B1)			
17 RxIN1+ Positive LVDS differential data input (G1-G5, B0-B1)	-		
18 GND GND 19 RxIN2- Negative LVDS differential data input (B2-B5, HS, VS, DE)			* · · =
			Negative LVDS differential data input (B2-B5, H5, V5, DE)
20 RxIN2+ Positive LVDS differential data input (B2-B5, HS, VS, DE) 21 GND GND			
			9.1-
<u> </u>			
= 0 ( )			
25 PWM PWM brightness control (10kHz +/- 5%)			
26 VBL- LED power return			
27 VBL- LED power return	<u> </u>		•
28 VBL- LED power return			•
29 VBL- LED power return	29		
30 VBL- LED power return	30		•
31 NC Non-Connection	31	NC	Non-Connection
32 VBL+ 7.5V - 21V LED power	32	VBL+	7.5V - 21V LED power
33 VBL+ 7.5V - 21V LED power	33	VBL+	7.5V - 21V LED power
34 VBL+ 7.5V - 21V LED power		VBL+	7.5V - 21V LED power
35 VBL+ 7.5V - 21V LED power		VBL+	7.5V - 21V LED power
36 VBL+ 7.5V - 21V LED power			
37 NC Non-Connection		NC	'
38 SMB_CLK SMBus Clock			
39 SMB_DAT SMBus Data	-	SMB_DAT	SMBus Data
40 GND GND		GND	GND

Note 1) Please connect GND pin to ground. Don't use it as no-connect nor connection with high impedance.

Note 2) Please connect NC to nothing. Don't connect it to ground nor to other signal input.



# 256k (k=1024) COLORS COMBINATION TABLE

	Display	R5 R4 R3 R2 R1 R0 G5 G4 G3 G2 G1 G0 B5 B4 B3 B2 B1 B0	Gray Scale
	Black		Level -
-	Blue		_
-	Green		-
Basic	Light Blue		-
Color	Red		-
Coloi	Purple	H H H H H H L L L L L H H H H H H	-
	Yellow	H H H H H H H H H H H L L L L L L	-
	White	H H H H H H H H H H H H H H H H H	-
	Black		L 0
	Diack		L 1
	Dark		L 2
Gray	Daik ↑		L3
Scale of Red	<b>\</b>		L60
	Light	H H H H L H L L L L L L L L L L L L L L	L61
		H H H H L L L L L L L L L L L L L L L L	L62
	Red	H H H H H H L L L L L L L L L L L L L L	Red L63
	Black		L 0
			L 1
Gray	Dark		L 2
Scale of	<u> </u>		L3 L60
Green	$\downarrow$		
0.00	Light		L61
			L62
	Green		Green L63
	Black		L 0
			L 1
Gray	Dark		L 2
Scale of	<u> </u>	: : : : :	L3 L60
Blue	$\downarrow$		
2.00	Light		L61
			L62
	Blue		Blue L63
	Black		L 0
			L 1 L 2
Gray	Dark		
Scale of	<u> </u>		L3
White &	$\downarrow$		L60
Black	Light	H H H H L H H H H H H H L H	L61
		H H H H L H H H H H L H H H H L L	L62
	White	н н н н н н н н н н н н н н н	White L63



## **FOR SAFETY**

LCD module is generally designed with precise parts to achieve light weighted thin mechanical dimensions.

In using our Modules, make certain that you fully understand and put into practice the warnings and safety precautions detailed in Engineering Information No.EE-D-001A,"CAUTIONS AND INSTRUCTIONS FOR TOSHIBA MATSUSHITA DISPLAY TECHNOLOGY CO., LTD LCD MODULES".

Refer to individual specifications and TECHNICAL DATA sheets (hereinafter called "TD") for more detailed technical information.

### 1) SPECIAL PURPOSES

- A) Toshiba Matsushita Display technology's Standard LCD Modules have not been customized for operation in extreme environments or for use in applications where performance failures could be life-threatening or otherwise catastrophic.
- B) Since Toshiba Matsushita Display technology's Standard LCD Modules have not been designed for operation in extreme environments, they must never be used in devices that will be exposed to abnormally high levels of vibration or shock which exceed Toshiba Matsushita Display technology's published specification limits.
- C) In addition, since Toshiba Matsushita Display technology Standard LCD Modules have not been designed for use in applications where performance failures could be life-threatening or catastrophic, they must never be installed in aircraft navigation control systems (such as, but not limited to Traffic Collision Avoidance System and Air Traffic Indicator), in military defense or weapons systems, in critical industrial process-control systems (e.g., those involved in the production of nuclear energy), or in critical medical device or patient life-support systems.

### 2) DISASSEMBLING OR MODIFICATION

DO NOT DISASSEMBLE OR MODIFY the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display.

Toshiba Matsushita Display technology doses not warrant the module, if customer disassembled or modified it.

# 3) BREAKAGE OF LCD PANEL

DO NOT INGEST liquid crystal material, DO NOT INHALE this material, and DO NOT CONTACT the material with skin, if LCD panel is broken and liquid crystal material spills out.

If liquid crystal material comes into mouth or eyes, rinse mouth or eyes out with water immediately.

If this material contact with skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.

### 4) GLASS OF LCD PANEL

BE CAREFUL WITH CHIPS OF GLASS that may cause injuring fingers or skin, when the glass is broken.

## 5) ELECTRIC SHOCK

DISCONNECT POWER SUPPLY before handling LCD module.

DO NOT TOUCH the parts inside LCD module and the connector or cables in order to prevent electric shock, because high voltage is supplied to these parts from power supply is turned on.

## 6) ABSOLUTE MAXIMUM RATINGS AND POWER PROTECTION CIRCUIT

DO NOT EXCEED the absolute maximum rating values under the worst probable conditions caused by the supply voltage variation, input voltage variation, variation in parts' constants, environmental temperature, etc., otherwise LCD module may be damaged.

Employ protection circuit for power supply, whenever the specification or TD specifies it.

Suitable protection circuit should be applied for each system design.

### 7) DISPOSAL

When dispose LCD module, obey to the applicable environmental regulations.